

# **GIGABYTE™**

# **H263-V11-LAW1**

HPC/AI Arm Server  
NVIDIA GH200 Grace Hopper Superchip  
2U 4-Node 16-Bay Gen5 NVMe  
Direct Liquid Cooling

## **User Manual**

Rev. 1.0

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## **Documentation Classifications**

In order to assist in the use of this product, Giga Computing provides the following types of documentation:

- User Manual: detailed information & steps about the installation, configuration and use of this product (e.g. motherboard, server barebones), covering hardware and BIOS.
- User Guide: detailed information about the installation & use of an add-on hardware or software component (e.g. BMC firmware, rail-kit) compatible with this product.
- Quick Installation Guide: a short guide with visual diagrams that you can reference easily for installation purposes of this product (e.g. motherboard, server barebones).

Please see the support section of the online product page to check the current availability of these documents.

## **For More Information**

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## Conventions

The following conventions are used in this user's guide:

	<b>NOTE!</b> Pieces of additional information related to the current topic.
	<b>CAUTION!</b> Precautionary measures to avoid possible hardware or software problems.
	<b>WARNING!</b> Alerts to any damage that might result from doing or not doing specific actions.

## Server Warnings and Cautions

Before installing a server, be sure that you understand the following warnings and cautions.



### **WARNING!**

#### **To reduce the risk of electric shock or damage to the equipment:**

- Do not disable the power cord grounding plug. The grounding plug is an important safety feature.
- Plug the power cord into a grounded (earthed) electrical outlet that is easily accessible at all times.
- Unplug the power cord from the power supply to disconnect power to the equipment.
- Do not route the power cord where it can be walked on or pinched by items placed against it. Pay particular attention to the plug, electrical outlet, and the point where the cord extends from the server.



### **WARNING!**

**To reduce the risk of personal injury from hot surfaces, allow the drives and the internal system components to cool before touching them.**



### **WARNING!**

**This server is equipped with high speed fans. Keep away from hazardous moving fan blades during servicing.**



### **WARNING!**

**The equipment should only be repaired, maintained or replaced by skilled personnel.**



### **CAUTION!**

- Do not operate the server for long periods with the access panel open or removed. Operating the server in this manner results in improper airflow and improper cooling that can lead to thermal damage.
- Danger of explosion if battery is incorrectly replaced.
- Replace battery with the same or equivalent type recommended by the manufacturer.
- Dispose of used batteries according to the manufacturer's instructions.



### **CAUTION!**

Risk of explosion if battery is replaced incorrectly or with an incorrect type. Replace the battery only with the same or equivalent type recommended by the manufacturer. Dispose of used batteries according to the manufacturer's instructions.



## Electrostatic Discharge (ESD)

### CAUTION!

ESD CAN DAMAGE DRIVES, BOARDS, AND OTHER PARTS. WE RECOMMEND THAT YOU PERFORM ALL PROCEDURES AT AN ESD WORKSTATION. IF ONE IS NOT AVAILABLE, PROVIDE SOME ESD PROTECTION BY WEARING AN ANTI-STATIC WRIST STRAP ATTACHED TO CHASSIS GROUND -- ANY UNPAINTED METAL SURFACE -- ON YOUR SERVER WHEN HANDLING PARTS.

Always handle boards carefully, they can be extremely sensitive to ESD. Hold boards only by their edges without touching any components or connectors. After removing a board from its protective ESD bag or from the system, place the board component side up on a grounded, static free surface. Use a conductive foam pad if available but not the ESD bag. Do not slide the board over any surface.

**System power on/off:** To service components within the server, please ensure the power has been disconnected.

e.g. Remove the node from the server chassis (to disconnect power) or disconnect the power from the server chassis.

Make sure the system is removed from the rack before opening the chassis, adding, or removing any non hot-plug components.

**Hazardous conditions, devices and cables:** Hazardous electrical conditions may be present on power, telephone, and communication cables. Turn off the system chassis and disconnect the cables attached to the system before servicing the chassis. Otherwise, personal injury or equipment damage can result.

**Electrostatic discharge (ESD) and ESD protection:** ESD can damage drives, boards, and other parts. We recommend that you perform all procedures in this chapter only at an ESD workstation. If one is not available, provide some ESD protection by wearing an antistatic wrist strap attached to chassis ground (any unpainted metal surface on the server) when handling parts.

**ESD and handling boards:** Always handle boards carefully. They can be extremely sensitive to electrostatic discharge (ESD). Hold boards only by their edges. After removing a board from its protective wrapper or from the system, place the board component side up on a grounded, static free surface. Use a conductive foam pad if available but not the board wrapper. Do not slide board over any surface.

**Installing or removing jumpers:** A jumper is a small plastic encased conductor that slips over two jumper pins. Some jumpers have a small tab on top that can be gripped with fingertips or with a pair of fine needle nosed pliers. If the jumpers do not have such a tab, take care when using needle nosed pliers to remove or install a jumper; grip the narrow sides of the jumper with the pliers, never the wide sides. Gripping the wide sides can damage the contacts inside the jumper, causing intermittent problems with the function controlled by that jumper. Take care to grip with, but not squeeze, the pliers or other tool used to remove a jumper, or the pins on the board may bend or break.

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# Chapter 1 Hardware Installation

## 1-1 Installation Precautions

The motherboard/system contain numerous delicate electronic circuits and components which can become damaged as a result of electrostatic discharge (ESD). Prior to installation, carefully read the user manual and follow these procedures:







- Prior to installation, do not remove or break motherboard S/N (Serial Number) sticker or warranty sticker provided by your dealer. These stickers are required for warranty validation.
- Always remove the AC power by unplugging the power cord from the power outlet before installing or removing the motherboard or other hardware components.
- When connecting hardware components to the internal connectors on the motherboard, make sure they are connected tightly and securely.
- When handling the motherboard, avoid touching any metal leads or connectors.
- It is best to wear an electrostatic discharge (ESD) wrist strap when handling electronic components such as a motherboard, CPU or memory. If you do not have an ESD wrist strap, keep your hands dry and first touch a metal object to eliminate static electricity.
- Prior to installing the motherboard, please have it on top of an antistatic pad or within an electrostatic shielding container.
- Before unplugging the power supply cable from the motherboard, make sure the power supply has been turned off.
- Before turning on the power, make sure the power supply voltage has been set according to the local voltage standard.
- Before using the product, please verify that all cables and power connectors of your hardware components are connected.
- To prevent damage to the motherboard, do not allow screws to come in contact with the motherboard circuit or its components.
- Make sure there are no leftover screws or metal components placed on the motherboard or within the computer casing.
- Do not place the computer system on an uneven surface.
- Do not place the computer system in a high-temperature environment.
- Turning on the computer power during the installation process can lead to damage to system components as well as physical harm to the user.
- If you are uncertain about any installation steps or have a problem related to the use of the product, please consult a certified computer technician.

# 1-2 Product Specifications



**NOTE:**

We reserve the right to make any changes to the product specifications and product-related information without prior notice.

 System	<ul style="list-style-type: none"><li>◆ 2U 4-Node - Rear access</li></ul>
 Dimension	<ul style="list-style-type: none"><li>◆ 440 (W) x 87.5 (H) x 850 (D) mm</li></ul>
 Superchip	<ul style="list-style-type: none"><li>◆ NVIDIA Grace Hopper Superchip:</li><li>◆ 1 x Grace CPU</li><li>◆ 1 x Hopper H100 GPU</li><li>◆ Connected with NVLink-C2C</li><li>◆ TDP up to 1000W (CPU + GPU + memory)</li></ul>
 Memory	<p><b>Per Node:</b></p> <p>NVIDIA Grace CPU:</p> <ul style="list-style-type: none"><li>◆ Up to 480GB of LPDDR5X memory with ECC</li><li>◆ Memory bandwidth up to 512GB/s</li></ul> <p>NVIDIA Hopper GPU:</p> <ul style="list-style-type: none"><li>◆ Up to 96GB HBM3</li><li>◆ Memory bandwidth up to 4TB/s</li></ul>
 LAN	<p><b>Per Node:</b></p> <ul style="list-style-type: none"><li>◆ 2 x 10GbE LAN ports (1 x Intel® X550-AT2)</li><li>◆ Support NCSI function</li><li>◆ 1 x Dedicated management port</li></ul> <p><b>Total:</b></p> <ul style="list-style-type: none"><li>◆ 8 x 10GbE LAN ports (4 x Intel® X550-AT2)</li><li>◆ Support NCSI function</li><li>◆ 4 x Dedicated management ports</li><li>◆ 1 x CMC port</li></ul>
 Video	<ul style="list-style-type: none"><li>◆ Integrated in Aspeed® AST2600</li><li>◆ 2D Video Graphic Adapter with PCIe bus interface</li><li>◆ 1920x1200@60Hz 32bpp</li></ul> <p>Management chip on CMC board:</p> <ul style="list-style-type: none"><li>◆ Integrated in Aspeed® AST2520A2-GP</li></ul>



## Storage

### Per node:

- ◆ 4 x 2.5" Gen5 NVMe hot-swappable bays, from PEX89048

### Total:

- ◆ 8 x 2.5" Gen5 NVMe hot-swappable bays, from CPU



## Expansion Slots

### Per node:

#### PCIe Cable:

- ◆ 1 x PCIe x16 (Gen5 x16) FHHL slot, from CPU

1 x OCP 3.0 slot with PCIe Gen5 x16 bandwidth, from CPU  
Supports NCSI function

2 x M.2 slots:

- ◆ M-key
- ◆ PCIe Gen5 x4, from PEX89048
- ◆ Supports 2280/22110 cards

### Total:






#### PCIe cable x 4:

- ◆ \*4 x PCIe x16 (Gen5 x16) FHHL slots, from CPU

4 x OCP 3.0 slots with PCIe Gen5 x16 bandwidth, from CPU  
Support NCSI function

8 x M.2 slots:

- ◆ M-key
- ◆ PCIe Gen5 x4, from PEX89048
- ◆ Support 2280/22110 cards

	Internal I/O	<p><b>Per Node:</b></p> <ul style="list-style-type: none"> <li>◆ 1 x TPM header</li> </ul>
	Front I/O	<p><b>Per node:</b></p> <ul style="list-style-type: none"> <li>◆ 1 x Power button with LED</li> <li>◆ 1 x ID button with LED</li> <li>◆ 1 x Status LED</li> <li>◆ 1 x System reset button</li> </ul> <p><b>Total:</b></p> <ul style="list-style-type: none"> <li>◆ 4 x Power buttons with LED</li> <li>◆ 4 x ID buttons with LED</li> <li>◆ 4 x Status LEDs</li> <li>◆ 4 x System reset buttons</li> <li>◆ *1 x CMC status LED</li> <li>◆ *1 x CMC reset button</li> </ul> <p><b>*Only one CMC status LED and reset button per system.</b></p>
	Rear I/O	<p><b>Per Node:</b></p> <ul style="list-style-type: none"> <li>◆ 2 x USB 3.2 Gen1</li> <li>◆ 1 x Mini-DP</li> <li>◆ 2 x RJ45</li> <li>◆ 1 x MLAN</li> <li>◆ 1 x ID LED</li> </ul> <p><b>Total:</b></p> <ul style="list-style-type: none"> <li>◆ 8 x USB 3.2 Gen1</li> <li>◆ 4 x Mini-DP</li> <li>◆ 8 x RJ45</li> <li>◆ 4 x MLAN</li> <li>◆ 1 x ID LED</li> <li>◆ *1 x CMC port</li> </ul> <p><b>*Only one CMC port per system.</b></p>
	Backplane Board	<ul style="list-style-type: none"> <li>◆ Speed and bandwidth:</li> <li>◆ PCIe Gen5 x4</li> </ul>
	TPM	<ul style="list-style-type: none"> <li>◆ 1 x TPM header with SPI interface</li> <li>◆ Optional TPM2.0 kit: CTM012</li> </ul>



## Power Supply

- ◆ Triple 3000W 80 PLUS Titanium redundant power supply

### AC Input:

- ◆ 100-127V~/ 16A, 50/60Hz
- ◆ 200-207V~/ 16A, 50/60Hz
- ◆ 208-240V~/ 16A, 50/60Hz

### DC Input: (Only for China):

- ◆ 240Vdc/ 16A
- ◆

### DC Output:

- ◆ Max 1200W/ 100-127V~
- ◆ +12.2V/ 98.36A
- ◆ +12.2Vsb/ 3A
- ◆ Max 2600W/ 200-207V~
- ◆ +12.2V/ 213A
- ◆ +12.2Vsb/ 3A
- ◆ Max 3000W/ 208-240V~ or 240Vdc Input
- ◆ +12.2V/ 245.9A
- ◆ +12.2Vsb/ 3A

NOTE: The system power supply requires C19 type power cord



## System Management

- ◆ Aspeed® AST2600 management controller
- ◆ GIGABYTE Management Console (AMI MegaRAC SP-X) web interface
  
- ◆ Dashboard
- ◆ HTML5 KVM
- ◆ Sensor Monitor (Voltage, RPM, Temperature, CPU Status ...etc.)
- ◆ Sensor Reading History Data
- ◆ FRU Information
- ◆ SEL Log in Linear Storage / Circular Storage Policy
- ◆ Hardware Inventory
- ◆ Fan Profile
- ◆ System Firewall
- ◆ Power Consumption
- ◆ Power Control
- ◆ Advanced power capping
- ◆ LDAP / AD / RADIUS Support
- ◆ Backup & Restore Configuration
- ◆ Remote BIOS/BMC/CPLD Update
- ◆ Event Log Filter
- ◆ User Management
- ◆ Media Redirection Settings
- ◆ PAM Order Settings
- ◆ SSL Settings
- ◆ SMTP Settings



## Operating Properties

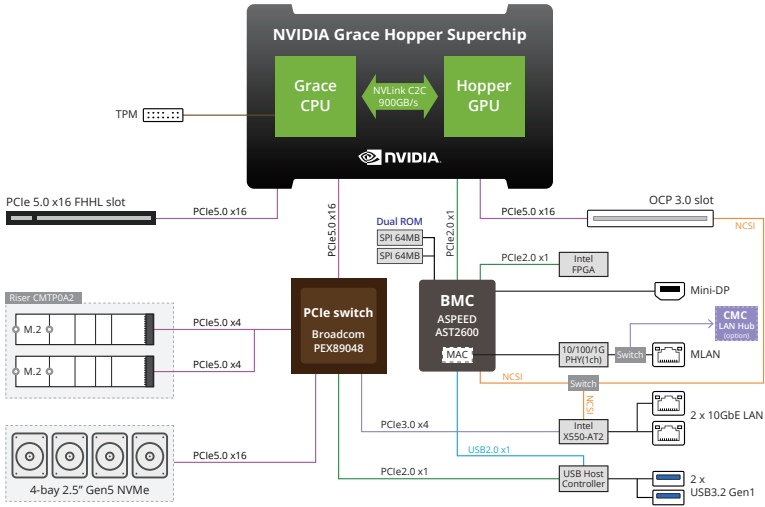
- ◆ Operating temperature: 10°C to 40°C
- ◆ Operating humidity: 8-80% (non-condensing)
- ◆ Non-operating temperature: -40°C to 60°C
- ◆ Non-operating humidity: 20%-95% (non-condensing)

### Note:

1) The ambient temperature and relative humidity of the environment depend on the inlet supply water temperature and the coolant flow rate.

2) If the relative humidity surpasses 60%, maintain the inlet water temperature between 40°C and 45°C to prevent condensation and ensure optimal system performance.

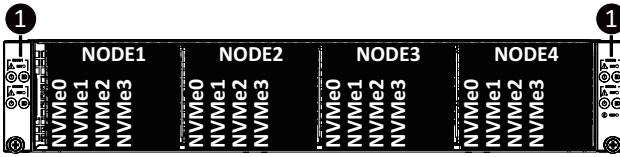
# 1-3 System Block Diagram



• Please Go to Chapter 4 Motherboard Components for Riser Slot information.

# Chapter 2 System Appearance

## 2-1 Front View



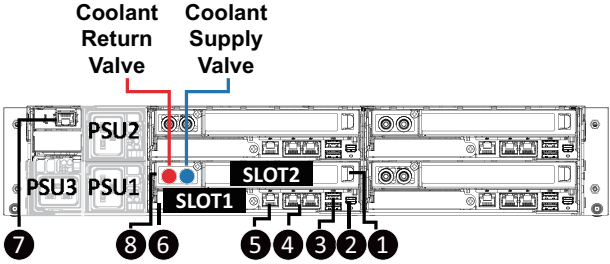
No.	Description
1.	Front Panel LEDs and buttons
<b>NOTE! Drives with green latches support NVMe.</b>	



- Please Go to Chapter 2-3 Front Panel LED and Buttons for detail description of function LEDs.

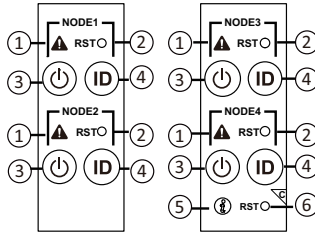


## 2-2 Rear View



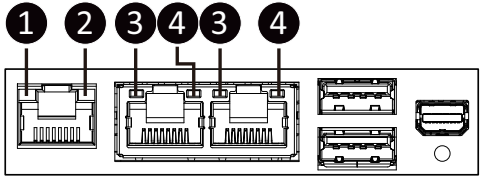
No.	Description
1.	PCIe Slot
2.	Mini DP Port
3.	2 x USB 3.2 Gen1
4.	2 x 10GbE LAN Port
5.	BMC Server Management LAN Port
6.	OCP 3.0 Slot (SFF Type)
7.	CMC LAN Port
8.	Coolant Pipe Slot

## 2-3 Front Panel LED and Buttons



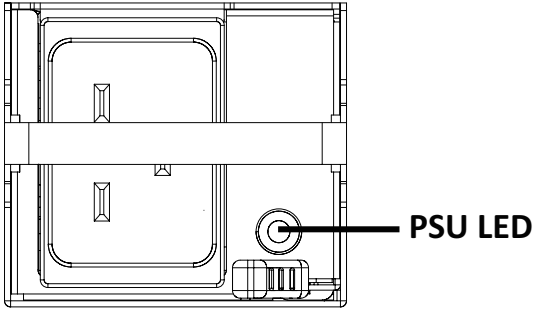
No.	Name	Color	Status	Description
1.	System Status LED	Green	Solid On	System is operating normally.
		Amber	Solid On	Critical condition, may indicate: System fan failure System temperature
			Blink	Non-critical condition, may indicate: Redundant power module failure Temperature and voltage issue Chassis intrusion
		N/A	Off	System is not ready, may indicate: POST error NMI error <u>Processor or terminator missing</u>
2.	Reset Button	--	--	Press this button to reset the system.
3.	Power button with LED	Green	On	System is powered on
		Green	Blink	System is in ACPI S1 state (sleep mode)
		N/A	Off	<ul style="list-style-type: none"> <li>System is not powered on or in ACPI S5 state (power off)</li> <li>System is in ACPI S4 state (hibernate mode)</li> </ul>
4.	ID Button with LED	Blue	On	System identification is active.
		N/A	Off	System identification is disabled.
5.	Enclosure	Green	On	System is operating normally.
		Amber	On	Critical condition, may indicate: Power module failure System fan failure Power supply voltage issue System temperature
			Blink	Non-critical condition, may indicate: Redundant power module failure Temperature and voltage issue
6.	CMC Reset Button	--	--	Press this button to reset the CMC.

## 2-4 Rear System LAN LEDs



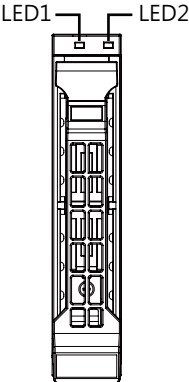
No.	Name	Color	Status	Description
1.	1GbE Speed LED	Yellow	On	1Gbps data rate
		Green	On	100 Mbps data rate
		N/A	Off	10 Mbps data rate
2.	1GbE Link/Activity LED	Green	On	Link between system and network or no access
			Blink	Data transmission or receiving is occurring
		N/A	Off	No data transmission or receiving is occurring
3.	1GbE Speed LED	Yellow	On	10Gbps data rate
		Green	On	1Gbps data rate
		N/A	Off	100 Mbps data rate
4.	1GbE Link/Activity LED	Green	On	Link between system and network or no access
			Blink	Data transmission or receiving is occurring
		N/A	Off	No data transmission or receiving is occurring

## 2-5 Power Supply Unit LED



State	Description
OFF	No AC power to all power supplies
1Hz Green Blinking	AC present / only standby on / Cold redundant mode
2Hz Green Blinking	Power supply firmware update mode
Amber	AC cord unplugged or AC power lost; with a second power supply in parallel still with AC input power
	Power supply critical event causing shut down: failure, OCP, OVP, fan failure and UVP
1Hz Amber Blinking	Power supply warning events where the power supply continues to operate: high temp, high power, high current and slow fan

# 2-6 Hard Disk Drive LEDs



LED #1	HDD Present	HDD Access	No HDD
Green	ON	BLINK (*1)	OFF

LED #2	HDD Locate	Fault
Green	BLINK (*1)	OFF
Yellow	OFF	ON

**NOTE:**

\*1: Blink cycle depends on HDD's activity signal.

## Chapter 3 System Hardware Installation



### Pre-installation Instructions

Computer components and electronic circuit boards can be damaged electrostatic discharge. Working on computers that are still connected to a power supply can be extremely dangerous. Follow the simple guidelines below to avoid damage to your computer or injury to yourself.

- Always disconnect the computer from the power outlet whenever you are working inside the computer case.
- If possible, wear a grounded wrist strap when you are working inside the computer case. Alternatively, discharge any static electricity by touching the bare metal system of the computer case, or the bare metal body of any other grounded appliance.
- Hold electronic circuit boards by the edges only. Do not touch the components on the board unless it is necessary to do so. Do not flex or stress the circuit board.
- Leave all components inside the static-proof packaging until you are ready to use the component for the installation.

### 3-1 Installing the Hard Disk Drive

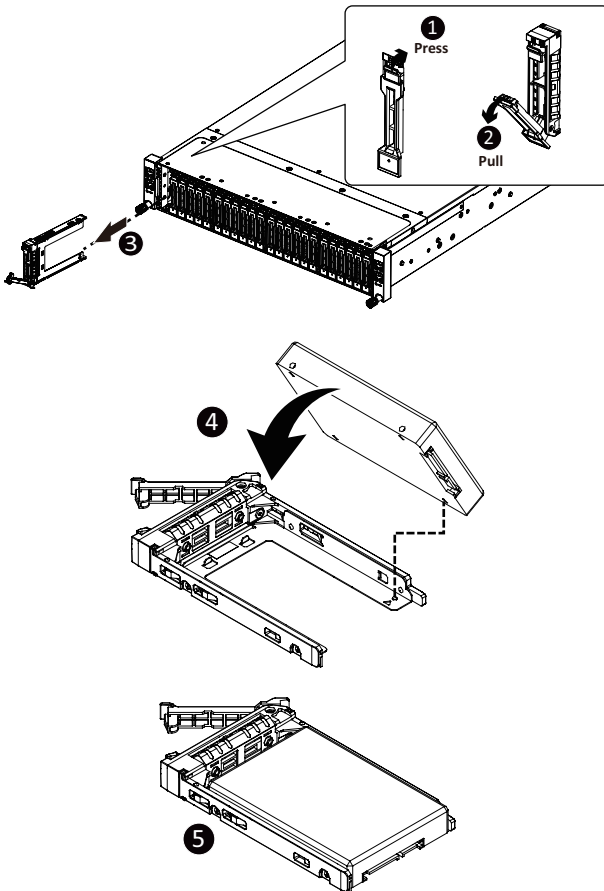


Read the following guidelines before you begin to install the Hard disk drive:

- Take note of the drive tray orientation before sliding it out.
- The tray will not fit back into the bay if inserted incorrectly.
- Make sure that the HDD is connected to the HDD connector on the backplane.

Follow these instructions to install the Hard disk drive:

1. Press the release button.
2. Extend the locking lever.
3. Pull the locking lever to remove the HDD tray.
4. Align the hard disk drive with the positioning stud on the HDD tray.
5. Slide hard disk drive into the blank HDD tray.
6. Reinsert the HDD tray into the slot and close the locking lever.



## 3-2 Removing the Node

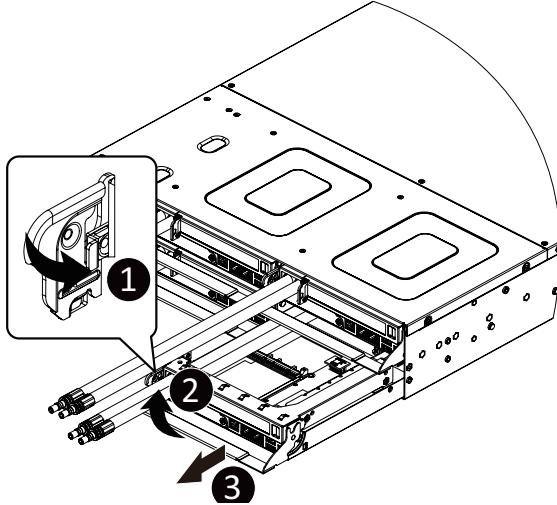


Read the following guidelines before you begin to replace a node:

- To make sure the system operates normally, please power off only the node that will be replaced or reconfigured.

Follow these instructions to remove a node:

1. Press the release latch while simultaneously pushing down the tray handle for the node.
2. Pull the node out of the system.
3. To install the node, push the node back into the system.





### 3-3 Removing Chassis Cover

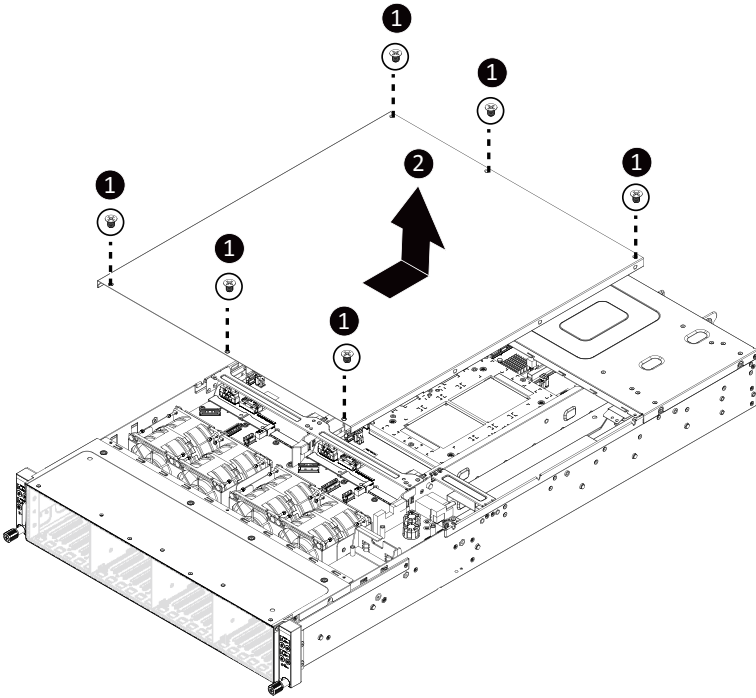


Before you remove or install the system cover

- Make sure the system is not turned on or connected to AC power.

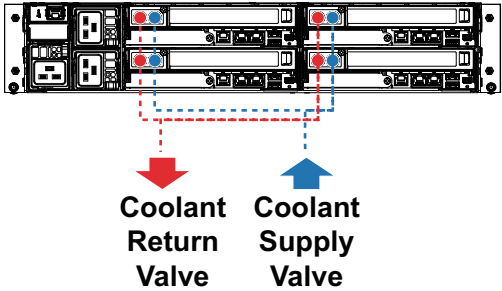
Follow these instructions to remove the system cover:

1. Loosen and remove the six screws securing the back cover.
2. Slide the cover to the rear of the system and remove the cover in the direction of the arrow.

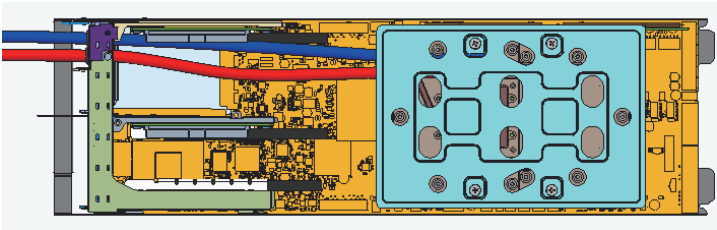


### 3-4 Liquid Cooling Assembly Information

The liquid cooling assembly is designed to capture heat from the processors in the node and cool the components. The coolant flow for the assembly is produced by the eight quick disconnects at the rear of the server chassis.



The flow of the coolant is shown below:



Flow Order	Component
First	CPU Cold Plate
Second	To coolant return

### 3-4-1 Liquid Cooling Specifications

The cold plate assembly in the liquid cooling assembly mounts directly on top of the processors.

The retention mechanism for installation on top of the processors is integrated into the cold plate. The liquid coolant contained within the tubes, is a mixture of demineralized water and propylene glycol with the following beneficial features: Anti-Freeze, Anti-Corrosion and Anti Bacterial. The following table lists the features and specifications of the liquid cooling assembly.

Specification	Value
Cold plate material	Copper CDA110
Thermal Interface Material	Honeywell PCM6000/PCM7900 (PCM), TGP3500PT, SF10
Operating liquid temperature	Minimum: 20°C (avoid condensation)
Operating Air temperature	Maximum: 40°C
Coolant Flow Rate	35°C
Operating Humidity	1.3 l/min Typical
Storage Temperature	5 - 90%
<b>Dimensions</b>	
Height	30.00 mm
Weight	~1.5 KG

**NOTE:** In the event that the room's relative humidity exceeds 60% then the inlet water temperature must be set between 40° to 45°C to avoid condensation and to keep the system running optimally.

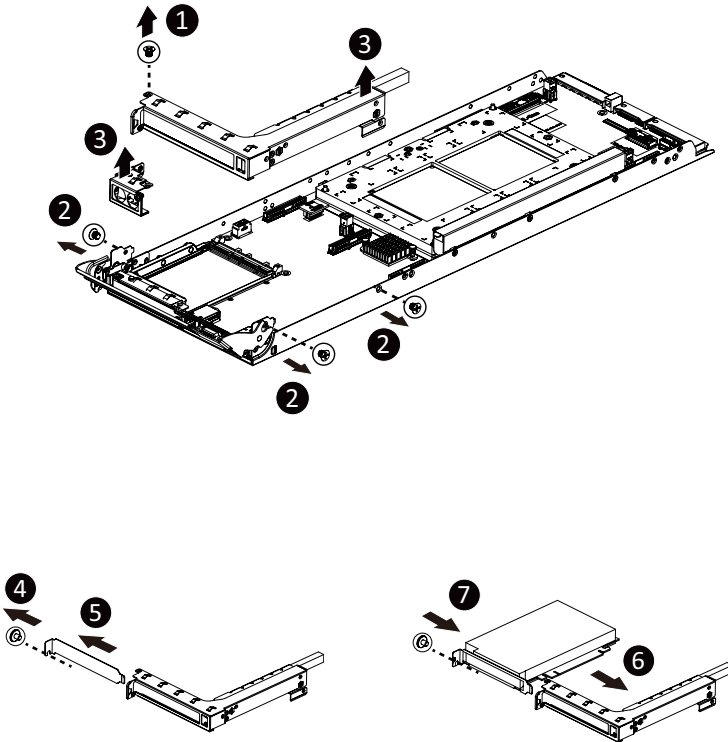
### 3-5 Installing the PCI Expansion Card

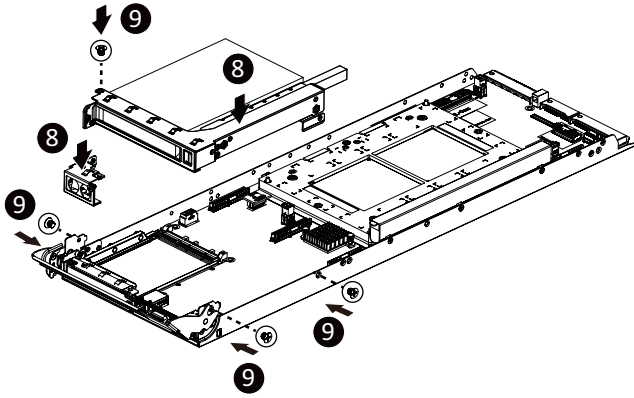


- The PCI riser assembly does not include a riser card or any cabling as standard. To install a PCI card, a riser card must be installed.

Follow these instructions to install the PCI Expansion card:

1. Remove the screw on the riser bracket.
2. Remove the three screws at the two side of the compute node.
3. Lift up the riser bracket and the coolant pipe bracket out of compute node.
4. Remove the screw securing the side bracket to the riser bracket.
5. Align the PCIe card to the riser guide slot and push in the direction of the arrow until the PCI-E card sits in the PCI card connector.
6. Secure the PCIe card with a screw.
7. Secure the side bracket to the riser bracket with a screw.
8. Reverse steps 1 - 4 to install the riser bracket and the coolant pipe bracket back into the system.





## 3-6 Installing the Mezzanine Card

### 3-6-1 Installing the OCP 3.0 Mezzanine Card

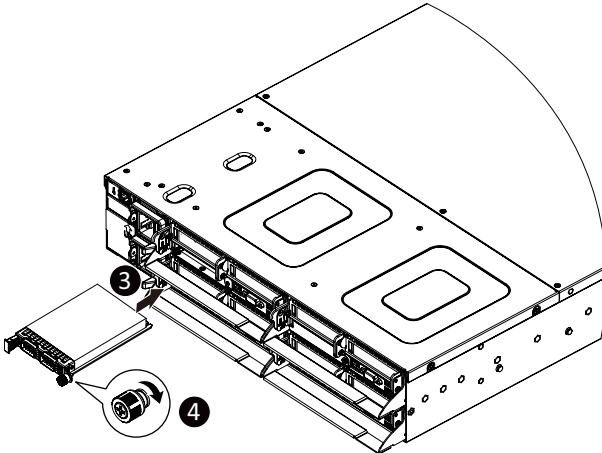
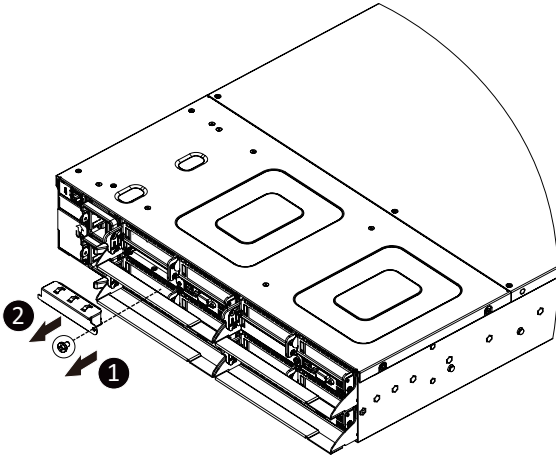


Use of the following type of OCP 3.0 NIC is recommended:

- OCP 3.0 SFF with pull tab
- OCP 3.0 SFF with ejector latch

**Follow these instructions to install an OCP 3.0 Mezzanine card:**

1. Remove the one screw securing the OCP 3.0 card slot cover.
2. Remove the slot cover from the system.
3. Insert the OCP 3.0 card into the card slot ensuring that the card is firmly connected to the connector on the motherboard.
4. Tighten the thumbnail screw to secure the OCP 3.0 card in place.
5. Reverse steps 3-4 to replace the OCP 3.0 card.



### 3-7 Replacing the Fan Assembly

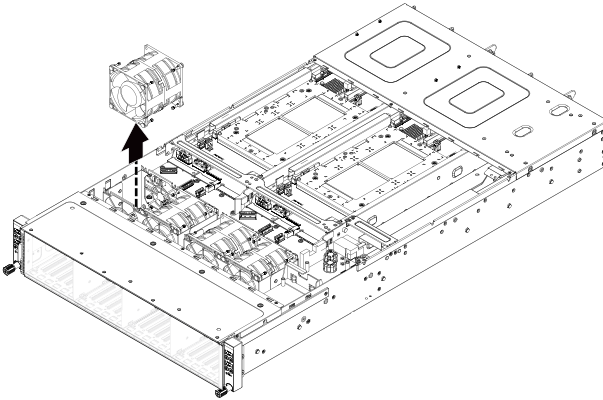


- Voltages can be present within the server whenever an AC power source is connected. This voltage is present even when the main power switch is in the off position. Ensure that the system is powered-down and all power sources have been disconnected from the server prior to replacing a system fan.

Failure to observe these warnings could result in personal injury or damage to equipment.

Follow these instructions to replace the fan assembly:

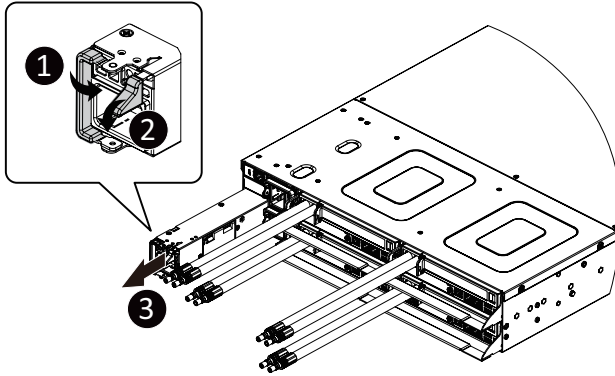
1. Lift up the fan assembly from the chassis.
2. Reverse the previous steps to install the replacement fan assembly.



## 3-8 Replacing the Power Supply

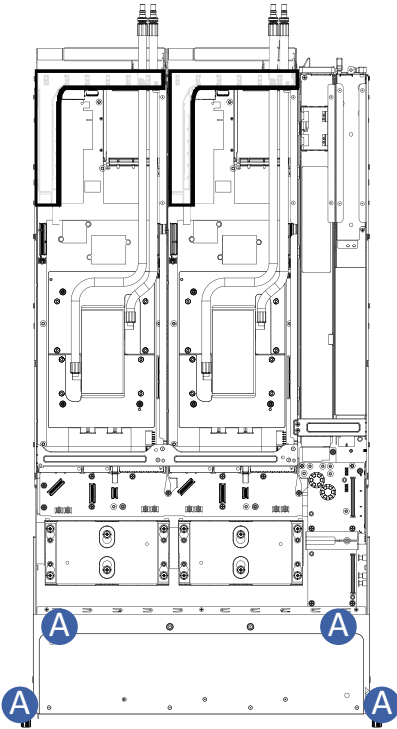
Follow these instructions to replace the power supply:

1. Pull up the power supply handle and press the retaining clip on the right side of the power supply along the direction of the arrow. At the same time, pull out the power supply by using its handle.
2. Insert the replacement power supply firmly into the chassis. Connect the AC power cord to the replacement power supply.

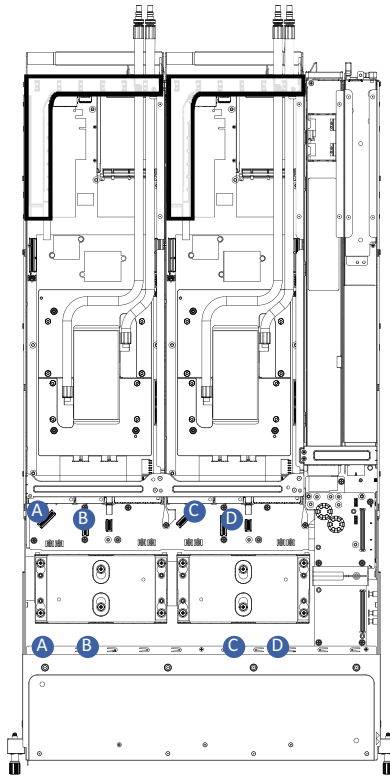




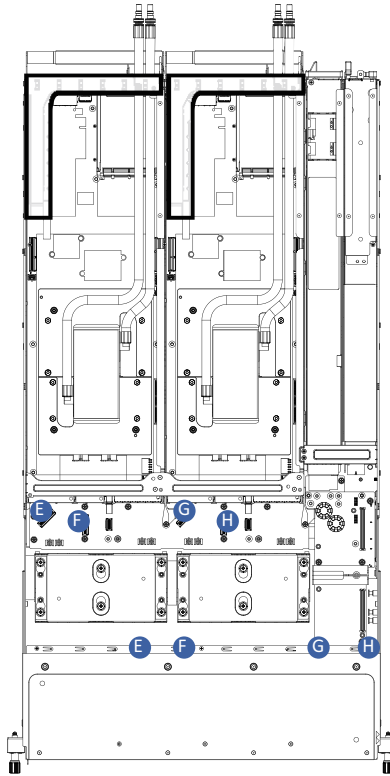
### 3-9 Cable Routing



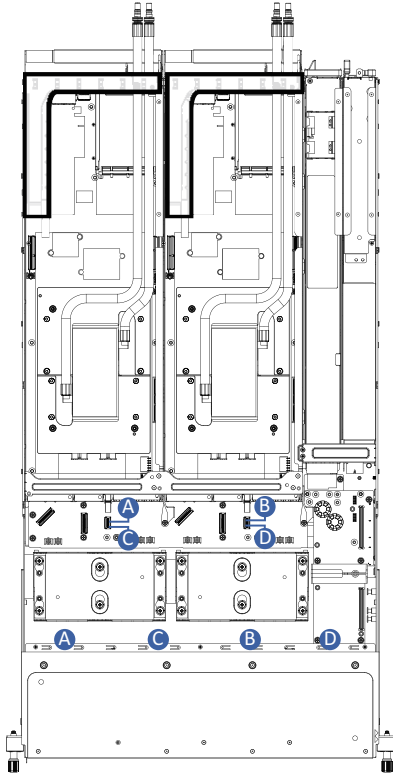
A	Front Panel LEDs and Buttons Cable	Motherboard: FP_1
		Front IO Board: FP_1
		Front IO Board: FP_1



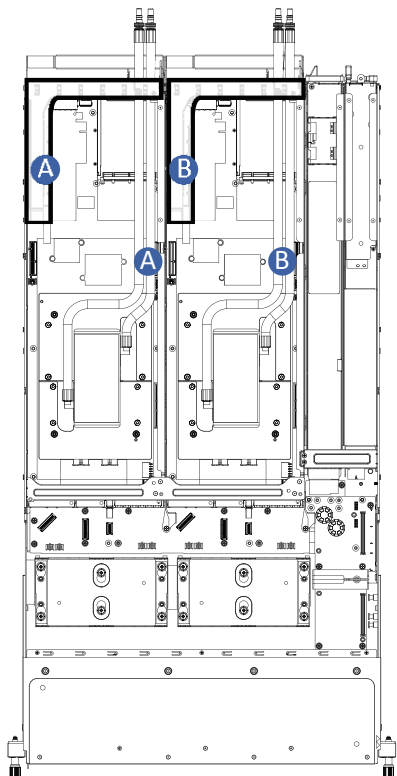
A	Top Middle Board to HDD Back Plane Board Cable (NVMe/Node1)	Middle Board: N1_U2_B
		Front HDD Board: N1 U.2 A
B	Top Middle Board to HDD Back Plane Board Cable (NVMe/Node1)	Middle Board: N1_U2_A
		Front HDD Board: N1 U.2 B
C	Top Middle Board to HDD Back Plane Board Cable (NVMe/Node3)	Middle Board: N3_U2_B
		Front HDD Board: N3 U.2 A
D	Top Middle Board to HDD Back Plane Board Cable (NVMe/Node3)	Middle Board: N3_U2_A
		Front HDD Board: N3 U.2 B



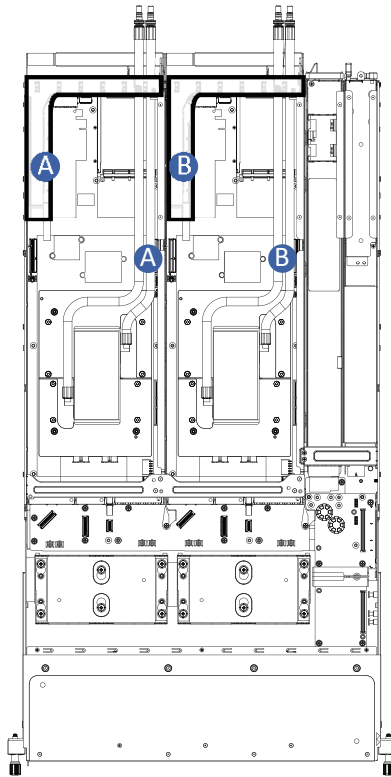
E	Bottom Middle Board to HDD Back Plane Board Cable (NVMe/Node2)	Middle Board: N2_U2_B
		Front HDD Board: N2_U.2 A
F	Bottom Middle Board to HDD Back Plane Board Cable (NVMe/Node2)	Middle Board: N1_U2_A
		Front HDD Board: N2_U.2 B
G	Bottom Middle Board to HDD Back Plane Board Cable (NVMe/Node4)	Middle Board: N4_U2_B
		Front HDD Board: N4_U.2 A
H	Bottom Middle Board to HDD Back Plane Board Cable (NVMe/Node4)	Middle Board: N1_U2_A
		Front HDD Board: N4_U.2 B



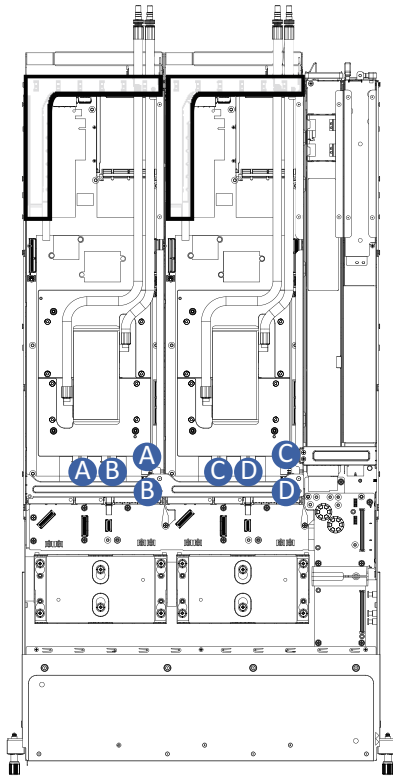
A	Top Middle Board to HDD Back Plane Board Signal Cable	Middle Board: N1_SATA
		Front HDD Board: N1 SATA
B	Top Middle Board to HDD Back Plane Board Signal Cable	Middle Board: N3_SATA
		Front HDD Board: N3 SATA
C	Bottom Middle Board to HDD Back Plane Board Signal Cable	Middle Board: N2_SATA
		Front HDD Board: N2 SATA
D	Bottom Middle Board to HDD Back Plane Board Signal Cable	Middle Board: N4_SATA
		Front HDD Board: N4 SATA



A	PCIe Riser Power Cable	Motherboard: AIC_PWR1
B		Riser: Cable
		Motherboard: AIC_PWR1
		Riser: Cable



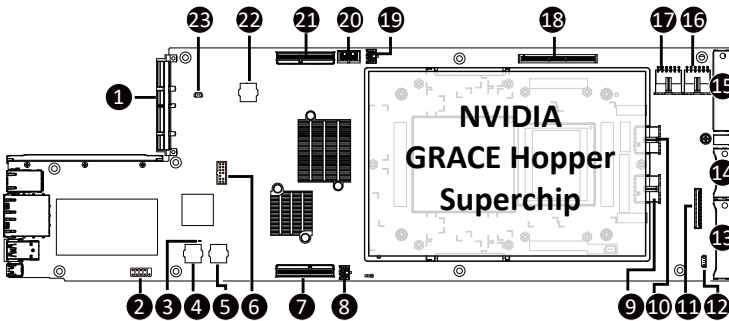
A	PCIe Riser Signal Cable	Motherboard: U2_P0_PE1
		Riser: Cable
B		Motherboard: U2_3
		Motherboard: U2_P0_PE1



A	CG1 Module Power Cable	CG1 Module: --
		Motherboard: 12VHPWR1
B		CG1 Module: --
		Motherboard: 12VHPWR0
C		CG1 Module: --
		Motherboard: 12VHPWR1
D		CG1 Module: --
		Motherboard: 12VHPWR0

# Chapter 4 Motherboard Components

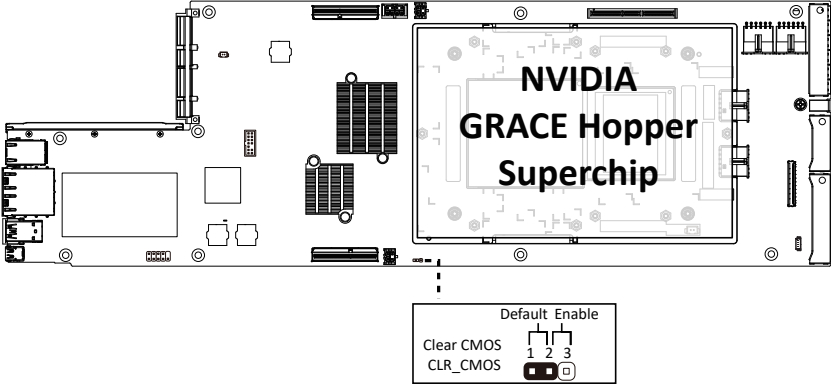
## 4-1 Motherboard Components



Item	Description
1	OCP 3.0 Connector (PCIe Gen5 x16)
2	Serial Cable Connector
3	BMC Readiness LED
4	BMC Flash ROM #1
5	BMC Flash ROM #2
6	TPM Module Connector
7	MCIO Connector (U2_P0_PE2/PCIe Gen5)
8	PCIe Power Connector (AIC_PWR_0)
9	Power Connector for GH Superchip
10	Power Connector for GH Superchip
11	CABLINE-CA II PLUS Connector (U2_1)
12	PSOUT Connector
13	PCIe/NVMe Connector (CA_1_2)
14	PCIe/NVMe Connector (CA_1_1_GF_1_1)
15	Power Connector (PCON5/PCON4/PCON6/PCON2/PCON1/PCON3)
16	12V Power Connector (12VHPWR_0)
17	12V Power Connector (12VHPWR_1)
18	Proprietary PCIe Slot (Gen 5/ x16 slot/GENZ_0)
19	PCIe Power Connector (AIC_PWR_1)
20	2 x 4 Pin 12V Power Connector (P12V_S1)
21	MCIO Connector (U2_P0_PE1/PCIe Gen5)
22	SPI Flash ROM
23	System Battery Cable Connector

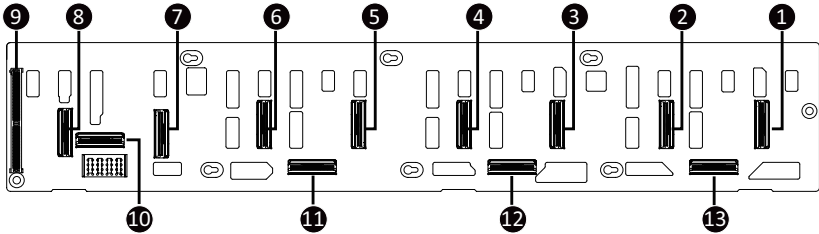


# 4-2 Jumper Setting



## 4-3 Backplane Board Storage Connector

### 4-3-1 CBPH701



Item	Description
1	SlimLine SAS Connector (N1 U.2 A)
2	SlimLine SAS Connector (N1 U.2 B)
3	SlimLine SAS Connector (N2 U.2 A)
4	SlimLine SAS Connector (N2 U.2 B)
5	SlimLine SAS Connector (N3 U.2 A)
6	SlimLine SAS Connector (N3 U.2 B)
7	SlimLine SAS Connector (N4 U.2 A)
8	SlimLine SAS Connector (N4 U.2 B)
9	Proprietary PCIe Slot (x8 slot/ GF_HDD)
10	SlimLine SAS Connector (N4 SATA)
11	SlimLine SAS Connector (N3 SATA)
12	SlimLine SAS Connector (N2 SATA)
13	SlimLine SAS Connector (N1 SATA)

## Chapter 5 BIOS Setup

BIOS (Basic Input and Output System) records hardware parameters of the system in the EFI on the motherboard. Its major functions include conducting the Power-On Self-Test (POST) during system startup, saving system parameters, loading the operating system etc. The BIOS includes a BIOS Setup program that allows the user to modify basic system configuration settings or to activate certain system features. When the power is turned off, the battery on the motherboard supplies the necessary power to the CMOS to keep the configuration values in the CMOS.

To access the BIOS Setup program, press the <DEL> key during the POST when the power is turned on.



- BIOS flashing is potentially risky, if you do not encounter any problems when using the current BIOS version, it is recommended that you don't flash the BIOS. To flash the BIOS, do it with caution. Inadequate BIOS flashing may result in system malfunction.
- It is recommended that you not alter the default settings (unless you need to) to prevent system instability or other unexpected results. Inadequately altering the settings may result in system's failure to boot. If this occurs, try to clear the CMOS values and reset the board to default values. (Refer to the **Exit** section in this chapter or introductions of the battery/clearing CMOS jumper in Chapter 4 for how to clear the CMOS values.)

### BIOS Setup Program Function Keys

<<-><->>	Move the selection bar to select the screen
<↑><↓>	Move the selection bar to select an item
<+>	Increase the numeric value or make changes
<->	Decrease the numeric value or make changes
<Enter>	Execute command or enter the submenu
<Esc>	Main Menu: Exit the BIOS Setup program Submenus: Exit current submenu
<F1>	Show descriptions of general help
<F3>	Restore the previous BIOS settings for the current submenus
<F9>	Load the Optimized BIOS default settings for the current submenus
<F10>	Save all the changes and exit the BIOS Setup program

■ **Main**

This setup page includes all the items of the standard compatible BIOS.

■ **Advanced**

This setup page includes all the items of AMI BIOS special enhanced features.

(ex: Auto detect fan and temperature status, automatically configure hard disk parameters.)

■ **Chipset**

This setup page includes all the submenu options for configuring the functions of the Platform Controller Hub.

■ **Server Management**

Server additional features enabled/disabled setup menus.

■ **Security**

Change, set, or disable supervisor and user password. Configuration supervisor password allows you to restrict access to the system and BIOS Setup.

A supervisor password allows you to make changes in BIOS Setup.

A user password only allows you to view the BIOS settings but not to make changes.

■ **Boot**

This setup page provides items for configuration of the boot sequence.

■ **Save & Exit**

Save all the changes made in the BIOS Setup program to the CMOS and exit BIOS Setup. (Pressing <F10> can also carry out this task.)

Abandon all changes and the previous settings remain in effect. Pressing <Y> to the confirmation message will exit BIOS Setup. (Pressing <Esc> can also carry out this task.)

# 5-1 The Main Menu

Once you enter the BIOS Setup program, the Main Menu (as shown below) appears on the screen. Use arrow keys to move among the items and press <Enter> to accept or enter other sub-menu.

## Main Menu Help

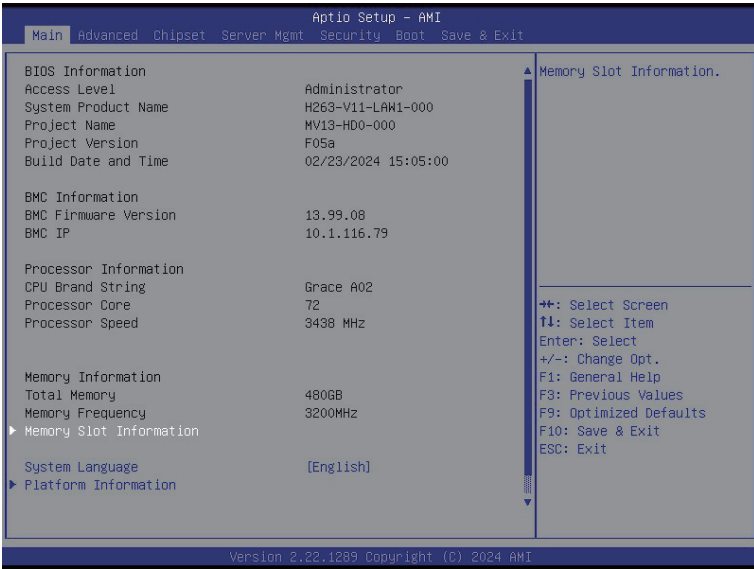
The on-screen description of a highlighted setup option is displayed on the bottom line of the Main Menu.

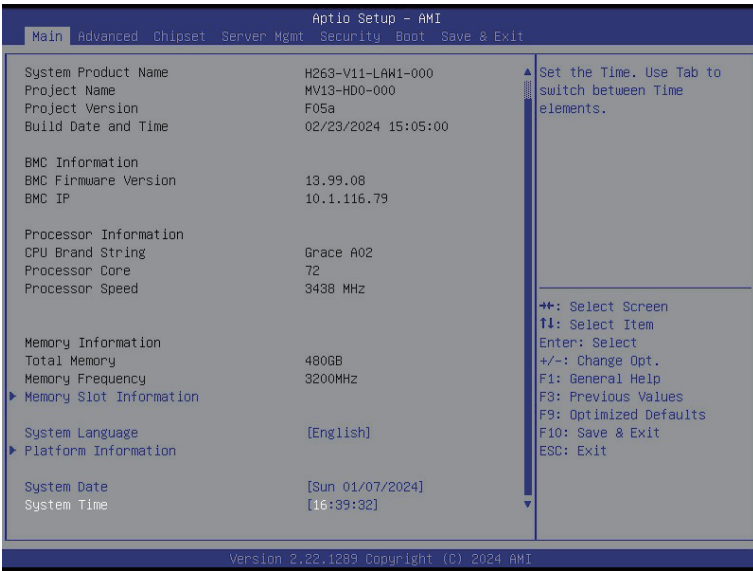
## Submenu Help

While in a submenu, press <F1> to display a help screen (General Help) of function keys available for the menu. Press <Esc> to exit the help screen. Help for each item is in the Item Help block on the right side of the submenu.



- When the system is not stable as usual, select the **Restore Defaults** item to set your system to its defaults.
- The BIOS Setup menus described in this chapter are for reference only and may differ by BIOS version.





Parameter	Description
<b>BIOS Information</b>	
Access Level	Displays the current access level depending on the type of password protection used. (If no password is set, the default will display as Administrator.) The Administrator level allows you to make changes to all BIOS settings; the User level only allows you to make changes to certain BIOS settings but not all.
System Product Name	Displays the system product name information.
Project Name	Displays the project name information.
Project Version	Displays version number of the BIOS setup utility.
Build Date and Time	Displays the date and time when the BIOS setup utility was created.
BMC Information <sup>(Note1)</sup>	
BMC Firmware Version <sup>(Note1)</sup>	Displays BMC firmware version information.
BMC IP	Displays BMC IP information.

(Note1) Functions available on selected models.

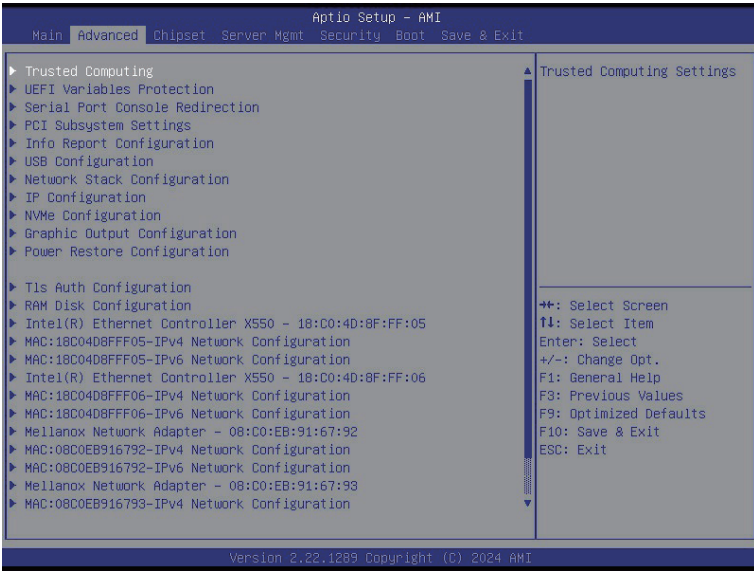
(Note2) This section will display capacity and frequency information of the memory that the customer has installed.

Processor Information	
CPU Brand String/ Max CPU Speed	Displays the technical information for the installed processor(s).
Memory Information <sup>(Note2)</sup>	
Total Memory	Displays the total memory size of the installed memory.
Memory Frequency	Displays the installed memory frequency information.
Memory Slot Information	Press [Enter] for advanced items.
Memory Frequency	Displays the frequency information of the installed memory.
System Language	Press [Enter] to select preferred language.
Platform Information	Press [Enter] for advanced items.
System Date	Sets the date following the weekday-month-day-year format.
System Time	Sets the system time following the hour-minute-second format.

(Note2) This section will display capacity and frequency information of the memory that the customer has installed.

# 5-2 Advanced Menu

The Advanced Menu displays submenu options for configuring the function of various hardware components. Select a submenu item, then press <Enter> to access the related submenu screen.





## 5-2-1 Trusted Computing



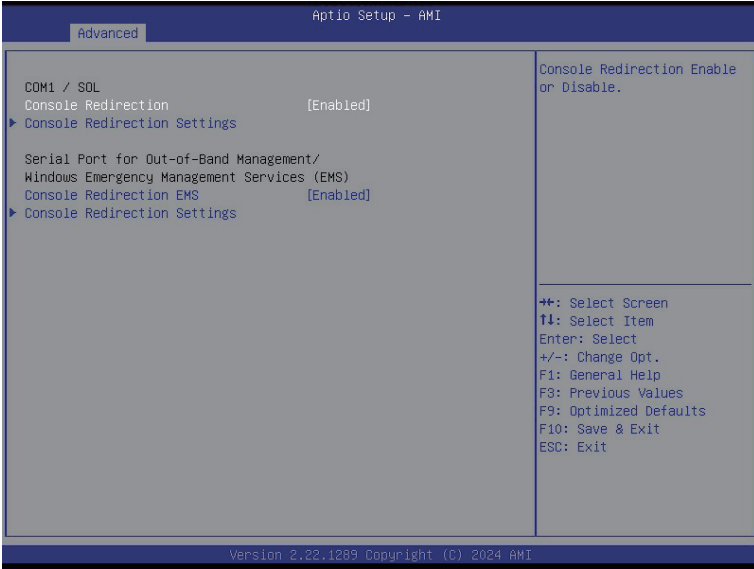
Parameter	Description
Configuration	
Security Device Support	<p>Enable/Disable BIOS support for security device. OS will not show security device. TCG EFI protocol and INT1A interface will not be available.</p> <p>Options available: Disable, Enable. Default setting is <b>Enable</b>.</p>

## 5-2-2 UEFI Variables Protection



Parameter	Description
Password protection of Runtime Variables	Control the NVRAM Runtime Variables protection through System Admin Password. Options available: Disable, Enable. Default setting is <b>Enable</b> .

### 5-2-3 Serial Port Console Redirection



Parameter	Description
COM1 Console Redirection <sup>(Note)</sup>	<p>Console redirection enables the users to manage the system from a remote location.</p> <p>Options available: Enabled, Disabled. Default setting is <b>Disabled</b>.</p>
COM1 Console Redirection Settings	<p>Press [Enter] to configure advanced items.</p> <p><b>Please note that this item is configurable when COM1 Console Redirection is set to Enabled.</b></p> <ul style="list-style-type: none"> <li>◆ Terminal Type <ul style="list-style-type: none"> <li>– Selects a terminal type to be used for console redirection.</li> <li>– Options available: VT100, VT100PLUS, VT-UTF8, ANSI. Default setting is <b>VT100PLUS</b>.</li> </ul> </li> <li>◆ Bits per second <ul style="list-style-type: none"> <li>– Selects the transfer rate for console redirection.</li> <li>– Options available: 9600, 19200, 38400, 57600, 115200. Default setting is <b>115200</b>.</li> </ul> </li> <li>◆ Data Bits <ul style="list-style-type: none"> <li>– Selects the number of data bits used for console redirection.</li> <li>– Options available: 7, 8. Default setting is <b>8</b>.</li> </ul> </li> </ul>

(Note) Advanced items prompt when this item is defined.

Parameter	Description
COM1 Console Redirection Settings (continued)	<ul style="list-style-type: none"> <li>◆ Parity <ul style="list-style-type: none"> <li>– A parity bit can be sent with the data bits to detect some transmission errors.</li> <li>– Even: parity bit is 0 if the num of 1's in the data bits is even.</li> <li>– Odd: parity bit is 0 if num of 1's in the data bits is odd.</li> <li>– Mark: parity bit is always 1. Space: Parity bit is always 0.</li> <li>– Mark and Space Parity do not allow for error detection.</li> <li>– Options available: None, Even, Odd, Mark, Space. Default setting is <b>None</b>.</li> </ul> </li> <li>◆ Stop Bits <ul style="list-style-type: none"> <li>– Stop bits indicate the end of a serial data packet. (A start bit indicates the beginning). The standard setting is 1 stop bit. Communication with slow devices may require more than 1 stop bit.</li> <li>– Options available: 1, 2. Default setting is <b>1</b>.</li> </ul> </li> <li>◆ Flow Control <ul style="list-style-type: none"> <li>– Flow control can prevent data loss from buffer overflow. When sending data, if the receiving buffers are full, a 'stop' signal can be sent to stop the data flow. Once the buffers are empty, a 'start' signal can be sent to re-start the flow. Hardware flow control uses two wires to send start/stop signals.</li> <li>– Options available: None, Hardware RTS/CTS. Default setting is <b>None</b>.</li> </ul> </li> <li>◆ VT-UTF8 Combo Key Support <ul style="list-style-type: none"> <li>– Enable/Disable the VT-UTF8 Combo Key Support.</li> <li>– Options available: Enabled, Disabled. Default setting is <b>Enabled</b>.</li> </ul> </li> <li>◆ Recorder Mode <ul style="list-style-type: none"> <li>– When this mode enabled, only texts will be send. This is to capture Terminal data.</li> <li>– Options available: Enabled, Disabled. Default setting is <b>Disabled</b>.</li> </ul> </li> <li>◆ Resolution 100x31 <ul style="list-style-type: none"> <li>– Enable/Disable extended terminal resolution.</li> <li>– Options available: Enabled, Disabled. Default setting is <b>Enabled</b>.</li> </ul> </li> <li>◆ Putty Keypad <ul style="list-style-type: none"> <li>– Selects Function Key and Keypad on Putty.</li> <li>– Options available: VT100, LINUX, XTERMR6, SC0, ESCN, VT400. Default setting is <b>VT100</b>.</li> </ul> </li> </ul>

Parameter	Description
Serial Port for Out-of-Band Management / Windows Emergency Management Services (EMS) Console Redirection <sup>(Note)</sup>	<p>EMS console redirection allows the user to configure Console Redirection Settings to support Out-of-Band Serial Port management.</p> <p>Options available: Enabled, Disabled. Default setting is <b>Disabled</b>.</p>
Serial Port for Out-of-Band EMS Console Redirection Settings	<p>Press [Enter] to configure advanced items.</p> <p><b>Please note that this item is configurable when Serial Port for Out-of-Band Management EMS Console Redirection is set to Enabled.</b></p> <ul style="list-style-type: none"> <li>◆ Out-of-Band Mgmt Port <ul style="list-style-type: none"> <li>– Microsoft Windows Emergency Management Service (EMS) allows for remote management of a Windows Server OS through a serial port.</li> <li>– Default setting is <b>COM1</b>.</li> </ul> </li> <li>◆ Terminal Type EMS <ul style="list-style-type: none"> <li>– Selects a terminal type to be used for console redirection.</li> <li>– Options available: VT100, VT100PLUS, VT-UTF8, ANSI. Default setting is <b>VT100PLUS</b>.</li> </ul> </li> <li>◆ Bits per second EMS <ul style="list-style-type: none"> <li>– Selects the transfer rate for console redirection.</li> <li>– Options available: 9600, 19200, 57600, 115200. Default setting is <b>115200</b>.</li> </ul> </li> <li>◆ Flow Control EMS <ul style="list-style-type: none"> <li>– Flow control can prevent data loss from buffer overflow. When sending data, if the receiving buffers are full, a 'stop' signal can be sent to stop the data flow. Once the buffers are empty, a 'start' signal can be sent to re-start the flow. Hardware flow control uses two wires to send start/stop signals.</li> <li>– Options available: None, Hardware RTS/CTS, Software Xon/Xoff. Default setting is <b>None</b>.</li> </ul> </li> </ul>

## 5-2-4 PCI Subsystem Settings

Advanced		
Aptio Setup - AMI		
PCI Bus Driver Version	A5.01.30	Enable/Disable OCP3_1 I/O ROM.
OCP3_1_A I/O ROM	[Enabled]	
U2_PO_PE1_A I/O ROM	[Enabled]	
U2_PO_PE2_A I/O ROM	[Enabled]	
Onboard LAN1 I/O ROM	[Enabled]	
PCI Devices Common Settings:		
PCI Latency Timer	[32 PCI Bus Clocks]	
VGA Palette Snoop	[Disabled]	
PERR# Generation	[Disabled]	
SERR# Generation	[Disabled]	
Above 4G Decoding	[Enabled]	
SR-IOV Support	[Disabled]	
BME DMA Mitigation	[Disabled]	
▶ PCI Express Settings		
▶ PCI Express GEN 2 Settings		
		++: Select Screen ↑↓: Select Item Enter: Select +/-: Change Opt. F1: General Help F3: Previous Values F9: Optimized Defaults F10: Save & Exit ESC: Exit
Version 2.22.1289 Copyright (C) 2024 AMI		

Parameter	Description
PCI Bus Driver Version	Displays the PCI Bus Driver version information.
OCP3_1_A ROM <sup>(Note1)</sup> U2_P0_PE1_A ROM <sup>(Note1)</sup> U2_P0_PE2_A ROM <sup>(Note1)</sup>	When enabled, this setting will initialize the device expansion ROM for the related PCI-E slot. Options available: Enabled, Disabled. Default setting is <b>Enabled</b> .
Onboard LAN1/ LAN2 Controller <sup>(Note2)</sup>	Enable/Disable the onboard LAN controller. Options available: Enabled, Disabled. Default setting is <b>Enabled</b> .
Onboard LAN1/ LAN2 I/O ROM <sup>(Note2)</sup>	Enable/Disable the onboard LAN devices, and initializes device expansion ROM. Options available: Enabled, Disabled. Default setting is <b>Enabled</b> .
PCI Devices Common Settings	
PCI Latency Timer	Value to be programmed into PCI Latency Timer Register. Options available: 32 PCI Bus Clock, 64 PCI Bus Clock, 96 PCI Bus Clock, 128 PCI Bus Clock, 160 PCI Bus Clock, 192 PCI Bus Clock, 224 PCI Bus Clock, 248 PCI Bus Clock. Default setting is <b>32 PCI Bus Clock</b> .
VGA Palette Snoop	Enables or Disables VGA Palette Registers Snooping. Options available: Enabled, Disabled. Default setting is <b>Disabled</b> .
PERR# Generation	Enables or Disables PCI Device to generate PERR#. Options available: Enabled, Disabled. Default setting is <b>Disabled</b> .
SERR# Generation	Enables or Disables PCI Device to generate SERR#. Options available: Enabled, Disabled. Default setting is <b>Disabled</b> .
Above 4G Decoding	Enable/Disable memory mapped I/O to 4GB or greater address space (Above 4G Decoding). Options available: Enabled, Disabled. Default setting is <b>Enabled</b> .
SR-IOV Support	If the system has SR-IOV capable PCIe devices, this item Enable/Disable Single Root IO Virtualization Support. Options available: Enabled, Disabled. Default setting is <b>Disabled</b> .
Above 4G Decoding	Enable/Disable memory mapped I/O to 4GB or greater address space (Above 4G Decoding). Options available: Enabled, Disabled. Default setting is <b>Enabled</b> .
BME DMA Mitigation	Re-enable Bus Master Attributes disabled during PCI enumeration PCI Bridges after SMM Locked. Options available: Enabled, Disabled. Default setting is <b>Disabled</b> .

(Note1) This section is dependent on the available PCI Slot.

## 5-2-4-1 PCI Express Device Register Settings



Parameter	Description
PCI Express Device Register Settings	Press [Enter] for configuration of advanced items.
Relaxed Ordering	Enable/Disable PCI Express Device Relaxed Ordering feature. Options available: Enabled, Disabled. Default setting is <b>Enabled</b> .
Extended Tag	When this feature is enabled, the system will allow device to use 8-bit Tag field as are requester. Options available: Enabled, Disabled. Default setting is <b>Disabled</b> .
No Snoop	Enable/Disable PCI Express Device No Snoop option. Options available: Enabled, Disabled. Default setting is <b>Enabled</b> .
Maximum Payload	Set maximum payload for PCI Express Device or allow system BIOS to select the value. Options available: Auto, 128 Bytes, 256 Bytes, 512 Bytes, 1024 Bytes, 2048 Bytes, 4096 Bytes. Default setting is <b>Auto</b> .
Maximum Read Request	Set maximum Read Request size for PCI Express Device or allow system BIOS to select the value. Options available: Auto, 128 Bytes, 256 Bytes, 512 Bytes, 1024 Bytes, 2048 Bytes, 4096 Bytes. Default setting is <b>Auto</b> .



Parameter	Description
PCI Express Link Register Settings	
ASPM Support	Set the ASPM level Force L0s - Force all links to L0s State. Options available: Auto, Disabled, L0s. Default setting is <b>Disabled</b> .
Extended Synch	When this feature is enabled, the system will allow generation of Extended Synchronization patterns. Options available: Enabled, Disabled. Default setting is <b>Disabled</b> .
Link Training Retry	Define the number of Retry Attempts software will take to retrain the link if previous training attempt was unsuccessful. Press <+> / <-> keys to increase or decrease the desired values.
Link Training Timeout (us)	Define the number of Microseconds software will wait before polling 'Link Training' bit in Link Status register. Press <+> / <-> keys to increase or decrease the desired values. Value rang is from 10 to 10000 us.
Unpopulated Links	When this item is set to 'Disable Link, the system will operate power save feature for those unpopulated PCI Express links. Options available: Keep Link ON, Disable Link Default setting is <b>Keep Link ON</b> .

## 5-2-4-2 PCI Express GEN 2 Settings

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Advanced

PCI Express GEN2 Device Register Settings	
ARI Forwarding	[Disabled]
AtomicOp Requester Enable	[Disabled]
AtomicOp Egress Blocking	[Disabled]
ID0 Request Enable	[Disabled]
ID0 Completion Enable	[Disabled]
LTR Mechanism Enable	[Disabled]
End-End TLP Prefix Blocking	[Disabled]
PCI Express GEN2 Link Register Settings	
Target Link Speed	[Auto]
Clock Power Management	[Disabled]
Compliance SOS	[Disabled]
Hardware Autonomous Width	[Enabled]
Hardware Autonomous Speed	[Enabled]

If supported by hardware and set to 'Enabled', the Downstream Port disables its traditional Device Number field being 0 enforcement when turning a Type1 Configuration Request into a Type0 Configuration Request, permitting access to Extended Functions in an

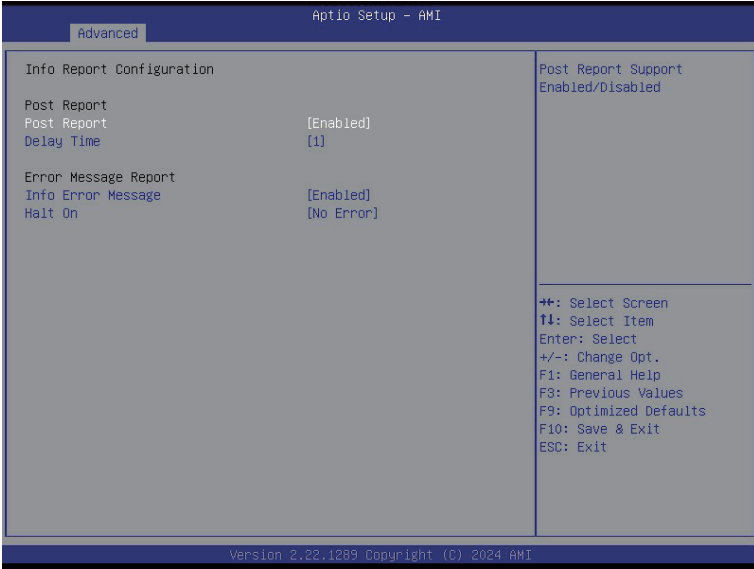
+/: Select Screen  
↑↓: Select Item  
Enter: Select  
+/-: Change Opt.  
F1: General Help  
F8: Previous Values  
F9: Optimized Defaults  
F10: Save & Exit  
ESC: Exit

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Parameter	Description
PCI Express GEN2 Setting	<p data-bbox="438 159 788 181">PCI Express GEN2 Device Register Settings</p> <ul style="list-style-type: none"> <li data-bbox="438 189 921 385">◆ Completion Timeout <ul style="list-style-type: none"> <li data-bbox="476 217 921 385">– In device Functions that support Completion Timeout programmability, allows system software to modify the Completion Timeout value. 'Default' 50us to 50ms. If 'Shorter' is selected, software will use shorter timeout ranges supported by hardware. If 'Longer' is selected, software will use longer timeout ranges.</li> </ul> </li> </ul> <p data-bbox="438 393 903 445">Options available: Default/Shorter/Longer/Disabled. Default setting is <b>Default</b>.</p> <ul style="list-style-type: none"> <li data-bbox="438 482 937 678">◆ ARI Forwarding <ul style="list-style-type: none"> <li data-bbox="476 511 937 678">– If supported by hardware and set to 'Enabled', the Downstream Port disables its traditional Device Number field being 0 enforcement when turning a Type1 Configuration Request into a Type0 Configuration Request, permitting access to Extended Functions in an ARI Device immediately below the Port.</li> </ul> </li> </ul> <p data-bbox="438 686 903 738">Options available: Default/Shorter/Longer/Disabled. Default setting is <b>Default</b>.</p> <ul style="list-style-type: none"> <li data-bbox="438 776 934 882">◆ AtomicOp Requester Enable <ul style="list-style-type: none"> <li data-bbox="476 804 934 882">– If supported by hardware and set to 'Enabled', this function initiates AtomicOp Requests only if Bus Master Enable bit is in the Command Register Set.</li> </ul> </li> </ul> <p data-bbox="438 890 945 912">Options available: Enabled/Disabled. Default setting is <b>Disabled</b>.</p> <ul style="list-style-type: none"> <li data-bbox="438 950 937 1030">◆ AtomicOp Egress Blocking <ul style="list-style-type: none"> <li data-bbox="476 978 937 1030">– If supported by hardware and set to 'Enabled', outbound AtomicOp Requests via Egress Ports will be blocked.</li> </ul> </li> </ul> <p data-bbox="438 1038 945 1059">Options available: Enabled/Disabled. Default setting is <b>Disabled</b>.</p> <ul style="list-style-type: none"> <li data-bbox="438 1097 929 1204">◆ IDO Request Enable <ul style="list-style-type: none"> <li data-bbox="476 1125 929 1204">– If supported by hardware and set to 'Enabled', this permits setting the number of ID-Based Ordering (IDO) bit (Attribute[2]) requests to be initiated..</li> </ul> </li> </ul> <p data-bbox="438 1212 945 1234">Options available: Enabled/Disabled. Default setting is <b>Disabled</b>.</p>

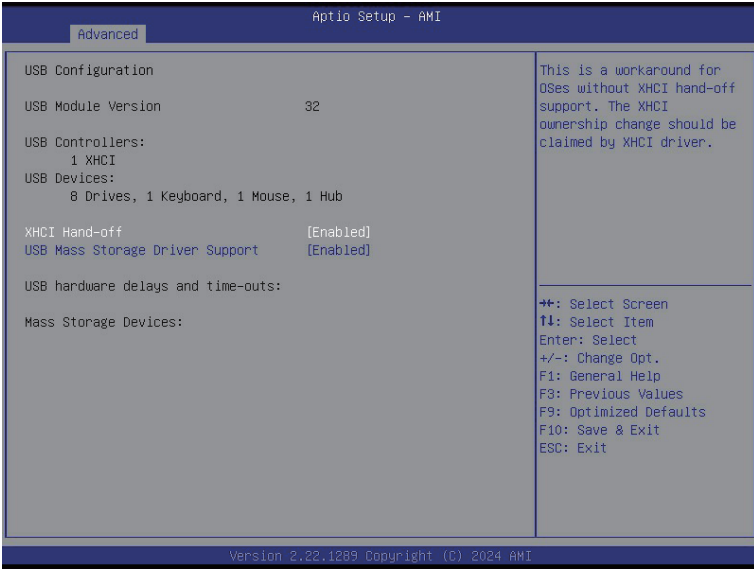
Parameter	Description
PCI Express GEN2 Setting	<p data-bbox="438 159 788 181">PCI Express GEN2 Device Register Settings</p> <ul style="list-style-type: none"> <li data-bbox="438 216 929 326">◆ IDO Request Enable <ul style="list-style-type: none"> <li data-bbox="476 244 929 326">– If supported by hardware and set to 'Enabled', this permits setting the number of ID-Based Ordering (IDO) bit (Attribute[2]) requests to be initiated.</li> </ul> </li> </ul> <p data-bbox="438 335 944 357">Options available: Enabled/Disabled. Default setting is <b>Disabled</b>.</p> <ul style="list-style-type: none"> <li data-bbox="438 366 929 476">◆ IDO Completion Enable <ul style="list-style-type: none"> <li data-bbox="476 395 929 476">– If supported by hardware and set to 'Enabled', this permits setting the number of ID-Based Ordering (IDO) bit (Attribute[2]) requests to be initiated.</li> </ul> </li> </ul> <p data-bbox="438 486 944 508">Options available: Enabled/Disabled. Default setting is <b>Disabled</b>.</p> <ul style="list-style-type: none"> <li data-bbox="438 517 891 620">◆ LTR Mechanism Enable <ul style="list-style-type: none"> <li data-bbox="476 545 891 620">– If supported by hardware and set to 'Enabled', this enables the Latency Tolerance Reporting (LTR) Mechanism.</li> </ul> </li> </ul> <p data-bbox="438 630 944 652">Options available: Enabled/Disabled. Default setting is <b>Disabled</b>.</p> <ul style="list-style-type: none"> <li data-bbox="438 661 919 771">◆ End-End TLP Prefix Blocking <ul style="list-style-type: none"> <li data-bbox="476 689 919 771">– If supported by hardware and set to 'Enabled', this function will block forwarding of TLPs containing End-End TLP Prefixes.</li> </ul> </li> </ul> <p data-bbox="438 780 944 802">Options available: Enabled/Disabled. Default setting is <b>Disabled</b>.</p> <p data-bbox="438 834 756 856">PCI Express GEN2 Device Link Settings</p> <ul style="list-style-type: none"> <li data-bbox="438 893 919 1031">◆ Compliance SOS <ul style="list-style-type: none"> <li data-bbox="476 921 919 1031">– If supported by hardware and set to 'Enabled', this will force LTSSM to send SKP Ordered Sets between sequences when sending Compliance Pattern or Modified Compliance Pattern.</li> </ul> </li> </ul> <p data-bbox="438 1041 944 1063">Options available: Enabled/Disabled. Default setting is <b>Disabled</b>.</p> <ul style="list-style-type: none"> <li data-bbox="438 1072 940 1210">◆ Hardware Autonomous Width <ul style="list-style-type: none"> <li data-bbox="476 1100 940 1210">– If supported by hardware and set to 'Disabled', this will disable the hardware's ability to change link width except width size reduction for the purpose of correcting unstable link operation.</li> </ul> </li> </ul> <p data-bbox="438 1219 944 1241">Options available: Enabled/Disabled. Default setting is <b>Disabled</b>.</p> <ul style="list-style-type: none"> <li data-bbox="438 1251 944 1389">◆ Hardware Autonomous Speed <ul style="list-style-type: none"> <li data-bbox="476 1279 944 1389">– If supported by hardware and set to 'Disabled', this will disable the hardware's ability to change link speed except speed rate reduction for the purpose of correcting unstable link operation</li> </ul> </li> </ul> <p data-bbox="438 1398 944 1420">Options available: Enabled/Disabled. Default setting is <b>Disabled</b>.</p>

### 5-2-5 Info Report Configuration



Parameter	Description
Info Report Configuration	
Post Report	Post report enabled/disabled. Options available: Enabled, Disabled. Default setting is <b>Enabled</b> .
Delay Time	Press <+>/<-> to configure the value.
Error Message Report	
Info Error Message	Enable/Disable the POST Error Message support. Options available: Enabled, Disabled. Default setting is <b>Enabled</b> .
Halt On	Options available: No Error, All Error. Default setting is <b>No Error</b> .

## 5-2-6 USB Configuration



Parameter	Description
USB Configuration	
USB Module Version	Displays the USB module version information.
USB Controllers	Displays the supported USB controllers.
USB Devices:	Displays the USB devices connected to the system.
XHCI Hand-off	Enable/Disable the XHCI Hand-off support. Options available: Enabled, Disabled. Default setting is <b>Enabled</b> .
USB Mass Storage Driver Support <sup>(Note)</sup>	Enable/Disable the USB Mass Storage Driver Support. Options available: Disabled, Enabled. Default setting is <b>Enabled</b> .
USB hardware delays and time-outs	
USB transfer time-out	Selects the time-out value for USB Control/Bulk/Interrupt transfers. Options available: 1 sec, 5 sec, 10 sec, 20 sec. Default setting is <b>20 sec</b> .

(Note) This item is present only if you attach USB devices.

## 5-2-7 Network Stack Configuration



Parameter	Description
Network Stack	Enable/Disable the UEFI network stack. Options available: Enabled, Disabled. Default setting is <b>Enabled</b> .
IPv4 PXE Support	Enable/Disable the IPv4 PXE feature. Options available: Enabled, Disabled. Default setting is <b>Enabled</b> .
IPv4 HTTP Support	Enable/Disable the IPv4 HTTP feature. Options available: Enabled, Disabled. Default setting is <b>Disabled</b> .
IPv6 PXE Support	Enable/Disable the IPv6 PXE feature. Options available: Enabled, Disabled. Default setting is <b>Disabled</b> .
IPv6 HTTP Support	Enable/Disable the IPv6 HTTP feature. Options available: Enabled, Disabled. Default setting is <b>Disabled</b> .
PXE boot wait time	Wait time in seconds to press ESC key to abort the PXE boot. Press the <+> / <-> keys to increase or decrease the desired values.
Media detect count	Number of times the presence of media will be checked. Press the <+> / <-> keys to increase or decrease the desired values.

## 5-2-8 IP Configuration Settings



Parameter	Description
Auto Configuration	Allows user to set IP. Options available: Disabled, Every Boot, On Demand. Default setting is <b>Disabled</b> .

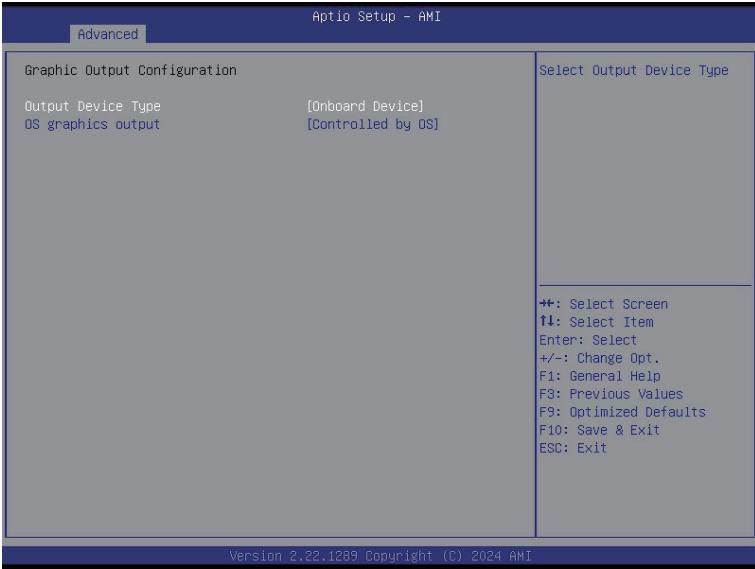


## 5-2-9 NVMe Configuration



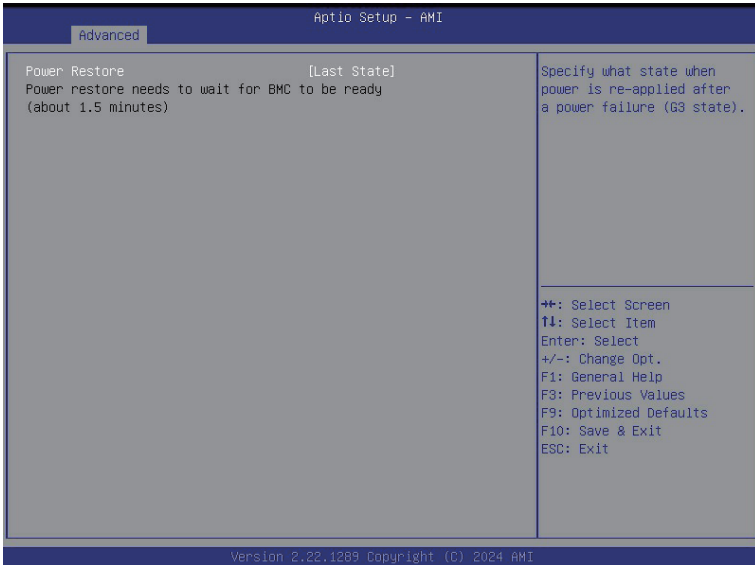
Parameter	Description
NVMe Configuration	Displays the NVMe devices connected to the system.

## 5-2-10 Graphic Output Configuration



Parameter	Description
Output Device Type	Selects output device type. Options available: First loaded Device, Onboard Device, External Device, Specific Device. Default setting is <b>Onboard Device</b> .
OS graphics output	Selects OS graphic output. Options available: Controlled by OS. Default setting is <b>Controlled by OS</b> .

## 5-2-11 Power Restore Configuration



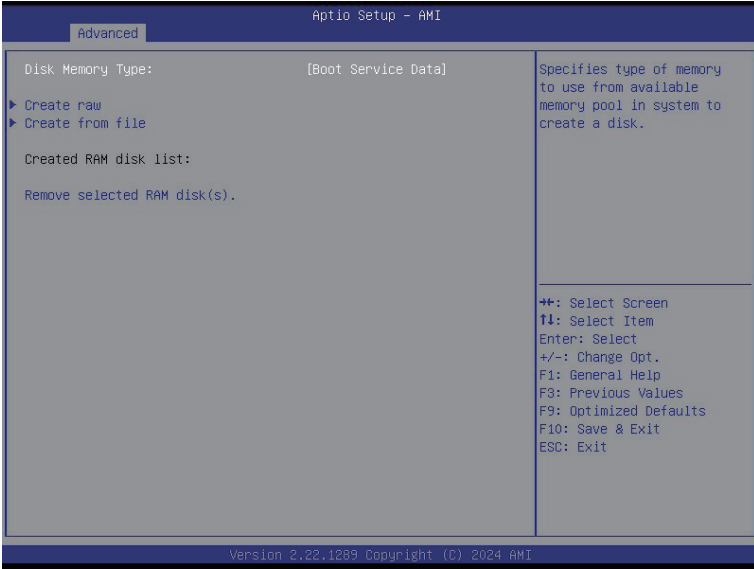
Parameter	Description
Power Restore	Specify what state when power is re-applied after a power failure (G3 state). Options available: Last State/Power On/Power Off. Default setting is <b>Last State</b> .

## 5-2-12 Tls Auth Configuration



Parameter	Description
Server CA Configuration	<p>Press [Enter] for configuration of advanced items.</p> <ul style="list-style-type: none"> <li>◆ Enroll Cert <ul style="list-style-type: none"> <li>– Press [Enter] to enroll a certificate <ul style="list-style-type: none"> <li>• Enroll Cert Using File</li> <li>• Cert GUID <ul style="list-style-type: none"> <li>Input digit character in 1111111-2222-3333-4444-1234567890ab format.</li> </ul> </li> </ul> </li> <li>– Commit Changes and Exit</li> <li>– Discard Changes and Exit</li> </ul> </li> <li>◆ Delete Cert</li> </ul>
Client Cert Configuration	Press [Enter] for configuration of advanced items.

## 5-2-13 RAM Disk Configuration



Parameter	Description
Disk Memory Type	Specifies the type of memory to use from available memory pool in system to create a disk. Options available: Boot Service Data, Reserved. Default setting is <b>Boot Service Data</b> .
Create Raw	Creates a raw RAM disk. <ul style="list-style-type: none"> <li>◆ Size (Hex) <ul style="list-style-type: none"> <li>– Input a valid RAM disk size that should be multiple of the RAM disk block size.</li> </ul> </li> <li>◆ Create &amp; Exit</li> <li>◆ Discard &amp; Exit</li> </ul>
Create from file	Creates a RAM disk from a given file.
Created RAM disk list	
Remove selected RAM disk(s)	Selects the RAM disk(s) to remove.

# 5-2-14 Intel(R) Ethernet Controller X550

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Advanced

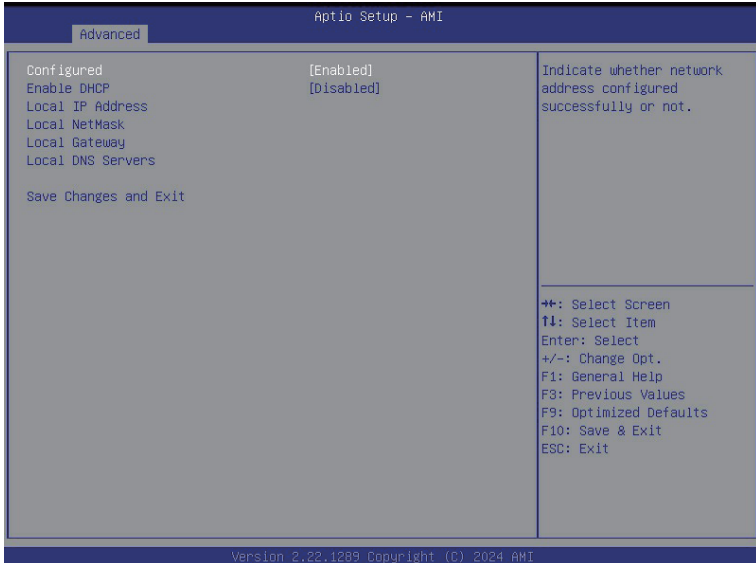
► NIC Configuration

Blink LEDs	0	Click to configure the network device port.
UEFI Driver	Intel(R) 10GbE Open Source	
Adapter PBA	8.1.04 AArch64	
Device Name	000000-000	
Chip Type	Intel(R) Ethernet Controller X550	
PCI Device ID	Intel X550	
PCI Address	1563	
	0E:00:00	
Link Status	[Disconnected]	↔: Select Screen
MAC Address	18:00:4D:8F:FF:05	↑↓: Select Item
Virtual MAC Address	00:00:00:00:00:00	Enter: Select
		+/-: Change Opt.
		F1: General Help
		F3: Previous Values
		F9: Optimized Defaults
		F10: Save & Exit
		ESC: Exit

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Parameter	Description
NIC Configuration	<p>Press [Enter] to configure advanced items.</p> <ul style="list-style-type: none"> <li>◆ Link Speed <ul style="list-style-type: none"> <li>– Allows for automatic link speed adjustment.</li> <li>– Options available: Auto Negotiated/10 Mbps Half/10 Mbps Full/100 Mbps Half/100 Mbps Full. Default setting is <b>Auto Negotiated</b>.</li> </ul> </li> <li>◆ Wake On LAN <ul style="list-style-type: none"> <li>– Enables power on of the system via LAN. Note that configuring Wake on LAN in the operating system does not change the value of this setting, but does override the behavior of Wake on LAN in OS controlled power states.</li> <li>– Options available: Enabled/Disabled. Default setting is <b>Enabled</b>.</li> </ul> </li> </ul>
Blink LEDs	Identifies the physical network port by blinking the associated LED. Press the numeric keys to adjust desired values.
UEFI Driver	Displays the technical specifications for the Network Interface Controller.
Adapter PBA	Displays the technical specifications for the Network Interface Controller.
Device Name	Displays the technical specifications for the Network Interface Controller.
Chip Type	Displays the technical specifications for the Network Interface Controller.
PCI Device ID	Displays the technical specifications for the Network Interface Controller.
PCI Address	Displays the technical specifications for the Network Interface Controller.
Link Status	Displays the technical specifications for the Network Interface Controller.
MAC Address	Displays the technical specifications for the Network Interface Controller.
Virtual MAC Address	Displays the technical specifications for the Network Interface Controller.

## 5-2-15 MAC IPv4 Network Configuration

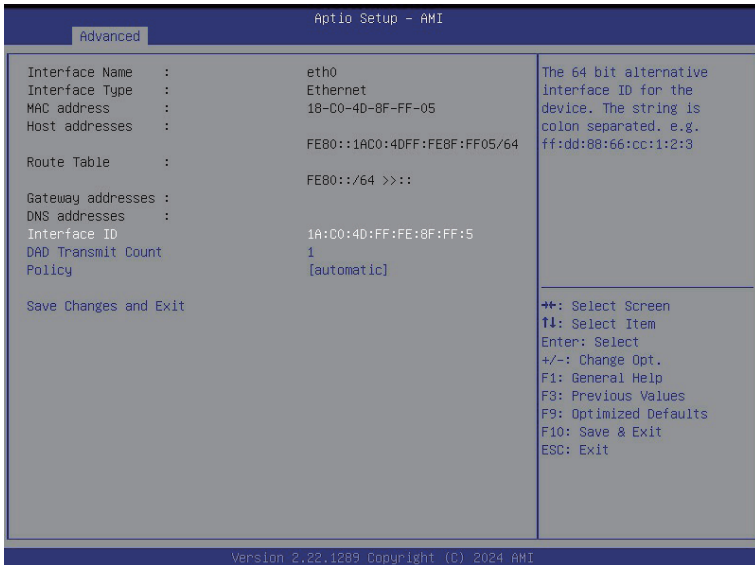


Parameter	Description
Configured	Indicates whether network address is configured successfully or not. Options available: Enabled, Disabled. Default setting is <b>Disabled</b> .
Enable DHCP <sup>(Note)</sup>	Options available: Enabled, Disabled. Default setting is <b>Disabled</b> .
Local IP Address <sup>(Note)</sup>	Press [Enter] to configure local IP address.
Local NetMask <sup>(Note)</sup>	Press [Enter] to configure local NetMask.
Local Gateway <sup>(Note)</sup>	Press [Enter] to configure local Gateway
Local DNS Servers <sup>(Note)</sup>	Press [Enter] to configure local DNS servers
Save Changes and Exit	Press [Enter] to save all configurations.

(Note) This item appears when **Configured** is set to **Enabled**.

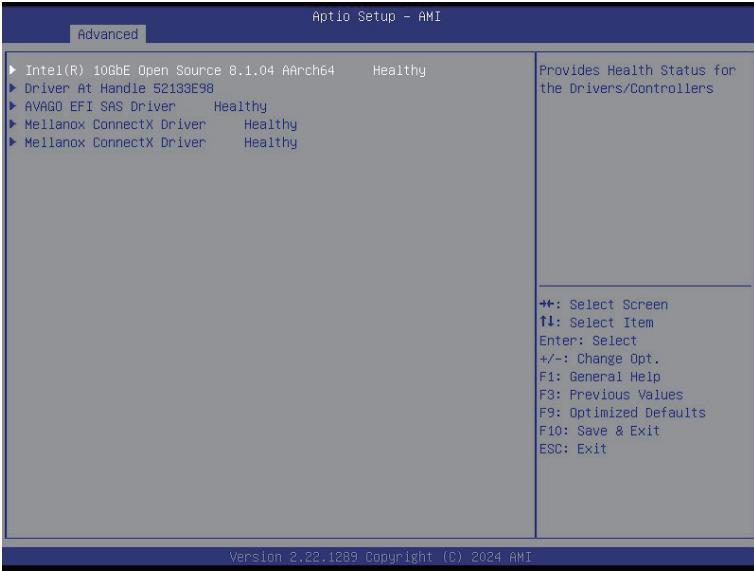


## 5-2-16 MAC IPv6 Network Configuration



Parameter	Description
Enter Configuration Menu	<p>Press [Enter] to configure advanced items.</p> <ul style="list-style-type: none"> <li>◆ Displays the MAC Address information.</li> <li>◆ Interface ID <ul style="list-style-type: none"> <li>– The 64 bit alternative interface ID for the device. The string is colon separated. e.g. ff:dd:88:66:cc:1:2:3.</li> </ul> </li> <li>◆ DAD Transmit Count <ul style="list-style-type: none"> <li>– The number of consecutive Neighbor solicitation messages sent while performing Duplicate Address Detection on a tentative address. A value of zero indicates that Duplicate Address Detection is not performed.</li> </ul> </li> <li>◆ Policy <ul style="list-style-type: none"> <li>– Options available: automatic, manual. Default setting is <b>automatic</b>.</li> </ul> </li> <li>◆ Save Changes and Exit <ul style="list-style-type: none"> <li>– Press [Enter] to save all configurations.</li> </ul> </li> </ul>

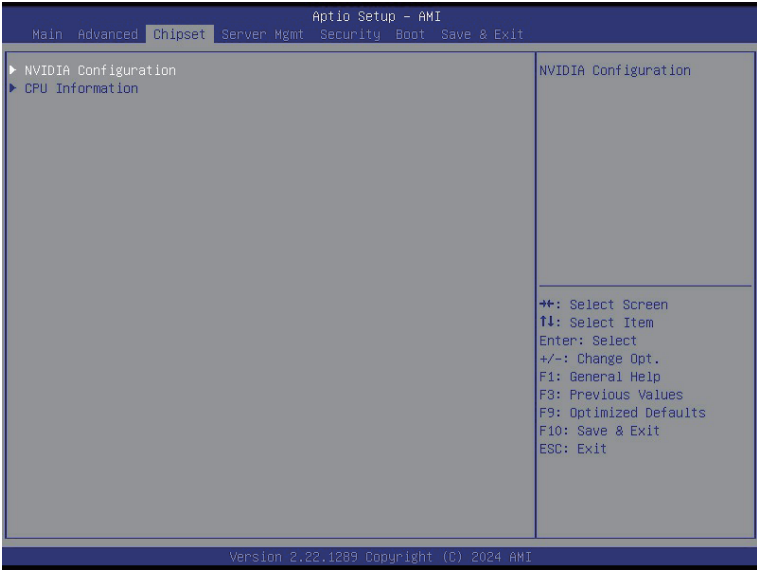
## 5-2-17 Driver Health



Parameter	Description
Driver Health	Displays driver health status of the devices/controllers if installed

### 5-3 Chipset Menu

Chipset Setup menu displays submenu options for configuring the function of Platform Controller Hub(PCH). Select a submenu item, then press <Enter> to access the related submenu screen.

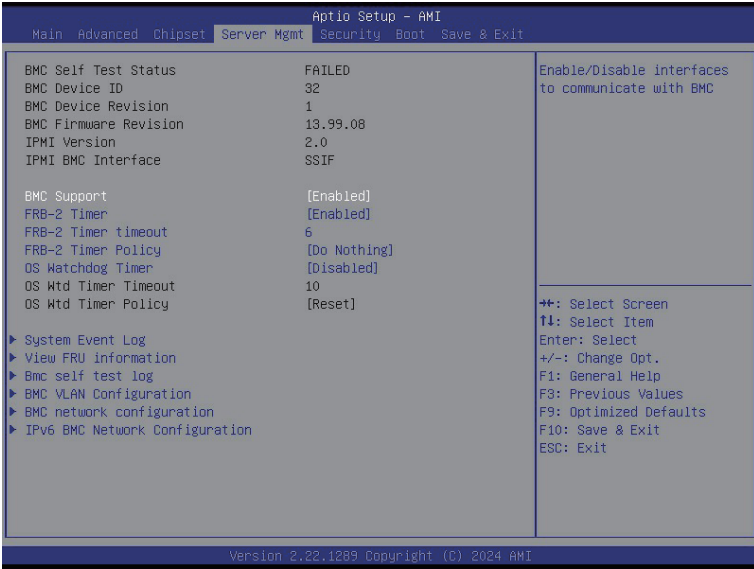


### 5-3-1 NVIDIA Configuration



Parameter	Description
NVIDIA Configuration	<p>Press [Enter] to configure advanced items.</p> <ul style="list-style-type: none"> <li>◆ Serial Port Configuration <ul style="list-style-type: none"> <li>– Used to select serial port configuration. <ul style="list-style-type: none"> <li>• Option available: Console Enabled -SBSA, Port Disabled, Serial Deebug Enabled -SBSA. Default setting is <b>Console Enabled -SBSA</b>.</li> </ul> </li> </ul> </li> </ul>
Boot Configuration	<p>Press [Enter] to configure advanced items.</p> <ul style="list-style-type: none"> <li>◆ Enable ACPI Timer <ul style="list-style-type: none"> <li>– Enable/Disable ACPI Timer. <ul style="list-style-type: none"> <li>• Option available: Enabled, Disabled. Default setting is <b>Disabled</b>.</li> </ul> </li> </ul> </li> </ul>
Grace Configuration	<p>Press [Enter] to configure advanced items.</p> <ul style="list-style-type: none"> <li>◆ EGM <ul style="list-style-type: none"> <li>– Used to enable EGM mode. <ul style="list-style-type: none"> <li>• Option available: Enabled, Disabled. Default setting is <b>Disabled</b>.</li> </ul> </li> </ul> </li> <li>◆ EGM Hypervisor reserved memory <ul style="list-style-type: none"> <li>– This item is configurable when EGM is set to enabled.</li> </ul> </li> <li>◆ UPHY spread spectrum <ul style="list-style-type: none"> <li>– Enable UPHY spread spectrum. <ul style="list-style-type: none"> <li>• Option available: Enabled, Disabled. Default setting is <b>Disabled</b>.</li> </ul> </li> </ul> </li> <li>◆ Mods Secure Partition <ul style="list-style-type: none"> <li>– Enable Mods secure partition to allow running system diagnostics. <ul style="list-style-type: none"> <li>• Option available: Enabled, Disabled. Default setting is <b>Enabled</b>.</li> </ul> </li> </ul> </li> <li>◆ Performance Version <ul style="list-style-type: none"> <li>– Specify Desired Performance Version. <ul style="list-style-type: none"> <li>• Option available: Default, C1-NCM.</li> <li>• Default setting is <b>C1-NCM</b>.</li> </ul> </li> </ul> </li> <li>◆ Error Injection <ul style="list-style-type: none"> <li>– Enable Error Injection ACPI Table. <ul style="list-style-type: none"> <li>• Option available: Enabled, Disabled. Default setting is <b>Enabled</b>.</li> </ul> </li> </ul> </li> <li>◆ Socket 0/1 Configuration <ul style="list-style-type: none"> <li>– Press [Enter] for advanced configuration for socket 0 and 1.</li> </ul> </li> </ul>

## 5-4 Server Management Menu



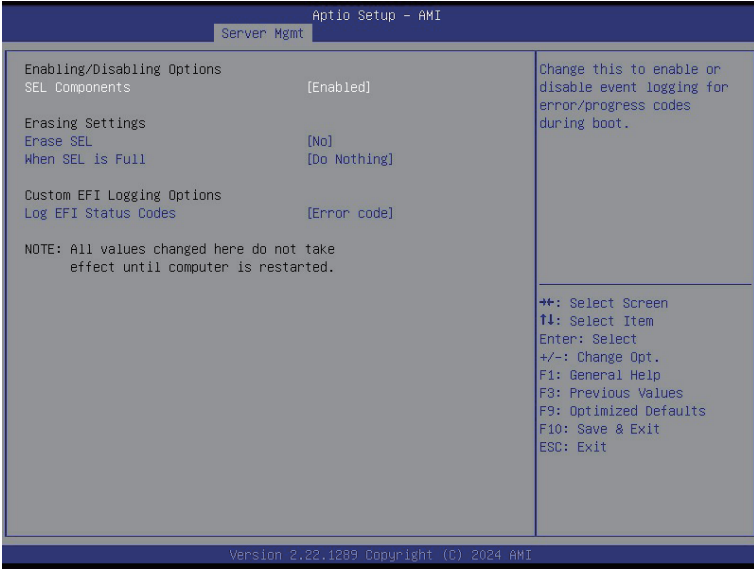
Parameter	Description
BMC Support	Enable/Disable interfaces to communicate with BMC. Options available: Enabled, Disabled. Default setting is <b>Enabled</b> .
FRB-2 Timer	Enable/Disable FRB-2 timer (POST timer). Options available: Enabled, Disabled. Default setting is <b>Enabled</b> .
FRB-2 Timer <sup>(Note1)</sup> timeout	Configures the FRB2 Timer timeout. The value is between 1 to 30 minutes. Default setting is <b>6 minutes</b> .
FRB-2 Timer Policy <sup>(Note1)</sup>	Configures the FRB2 Timer policy. Options available: Do Nothing, Reset, Power Down, Power Cycle. Default setting is <b>Do Nothing</b> .
OS Watchdog Timer	Enable/Disable OS Watchdog Timer function. Options available: Enabled, Disabled. Default setting is <b>Disabled</b> .
OS Wtd Timer Timeout <sup>(Note2)</sup>	Configures OS Watchdog Timer. The value is between 1 to 30 minutes. Default setting is <b>10 minutes</b> .
OS Wtd Timer Policy <sup>(Note2)</sup>	Configure OS Watchdog Timer Policy. Options available: Reset, Do Nothing, Power Down, Power Cycle. Default setting is <b>Reset</b> .

(Note1) This item is configurable when **FRB-2 Timer** is set to **Enabled**.

(Note2) This item is configurable when **OS Watchdog Timer** is set to **Enabled**.

<b>Parameter</b>	<b>Description</b>
System Event Log	Press [Enter] to configure advanced items.
View FRU Information	Press [Enter] to view the FRU information.
BMC self test log	Press [Enter] to configure advanced items.
BMC VLAN Configuration	Press [Enter] to configure advanced items.
BMC network Configuration	Press [Enter] to configure advanced items.
IPv6 BMC Network Configuration	Press [Enter] to configure advanced items.

## 5-4-1 System Event Log

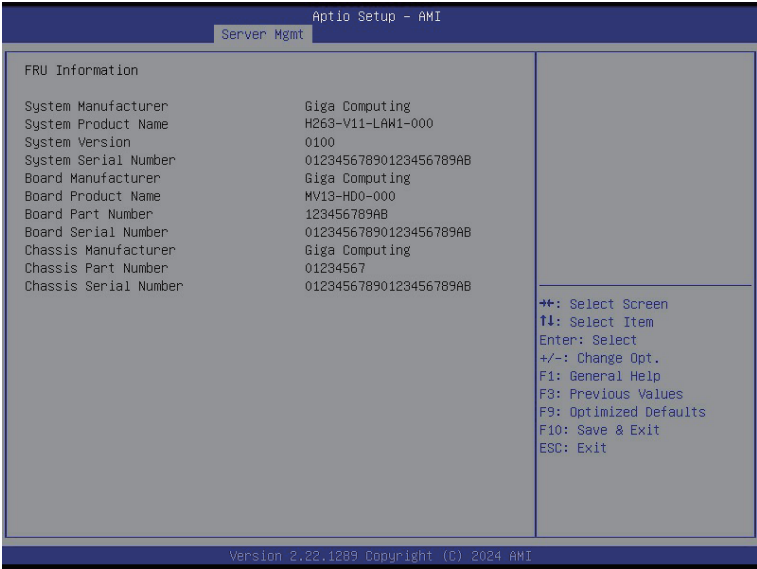


Parameter	Description
Enabling / Disabling Options	
SEL Components	Change this item to enable or disable all features of System Event Logging during boot. Options available: Enabled, Disabled. Default setting is <b>Enabled</b> .
Erasing Settings	
Erase SEL	Choose options for erasing SEL. Options available: No, Yes, On next reset, Yes, On every reset. Default setting is <b>No</b> .
When SEL is Full	Choose options for reactions to a full SEL. Options available: Do Nothing, Erase Immediately, Delete Oldest Record. Default setting is <b>Do Nothing</b> .
Custom EFI Logging Options	
Log EFI Status Codes	Enable/Disable the logging of EFI Status Codes (if not already converted to legacy). Options available: Disabled, Both, Error code, Progress code. Default setting is <b>Error code</b> .



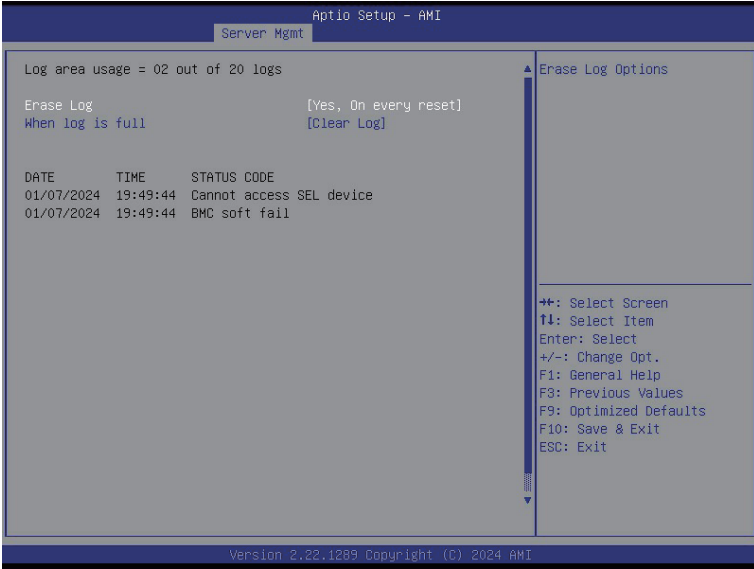
## 5-4-2 View FRU Information

The FRU page is a simple display page for basic system ID information, as well as System product information. Items on this window are non-configurable.



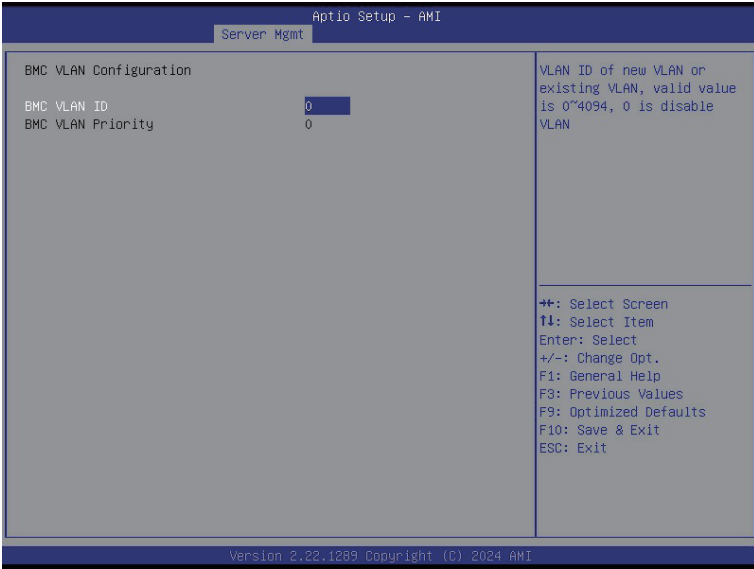
(Note) The model name will vary depends on the product you purchased

### 5-4-3 BMC self test log



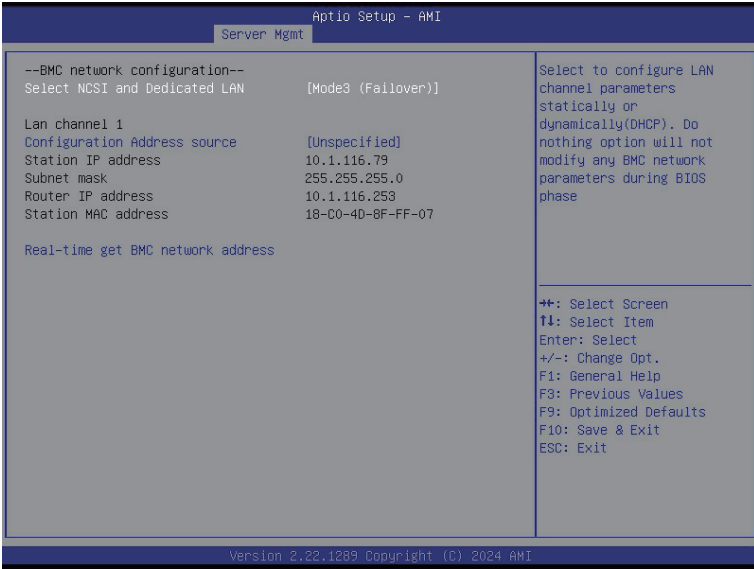
Parameter	Description
Erase Log	Options available: No/Yes, On next reset/Yes, On every reset. Default setting is <b>No</b> .
When Log is full	Configuration for reactions to a full log. Option available: Do not log any more/Clear Log. Default setting is <b>Do not log any more</b> .

## 5-4-4 BMC VLAN Configuration



Parameter	Description
BMC VLAN Configuration	
BMC VLAN ID	Select to configure BMC VLAN ID. The valid range is from 0 to 4094. When set to 0, BMC VLAN ID will be disabled.
BMC VLAN Priority	Select to configure BMC VLAN Priority. The valid range is from 0 to 7. When BMC VLAN ID is set to 0, BMC VLAN Priority will not be selected.

## 5-4-5 BMC Network Configuration



Parameter	Description
BMC network configuration	
Select NCSI and Dedicated LAN	Options available: Do Nothing, Model1(Dedicated), Model2(NCSI), Mode3(Failover). Default setting is <b>Do Nothing</b> .
Lan Channel 1	
Configuration Address source	Selects to configure LAN channel parameters statically or dynamically (DHCP). Options available: Unspecified, Static, DynamicBmcDhcp. Default setting is <b>DynamicBmcDhcp</b> .
Station IP address	Displays IP Address information.
Subnet mask	Displays Subnet Mask information. Please note that the IP address must be in three digitals, for example, 192.168.000.001.
Router IP address	Displays the Router IP Address information.
Station MAC address	Displays the MAC Address information.
Real-time get BMC network address	Press [Enter] will set LAN mode and Address source and then get IP, Subnet, Gateway and MAC address.

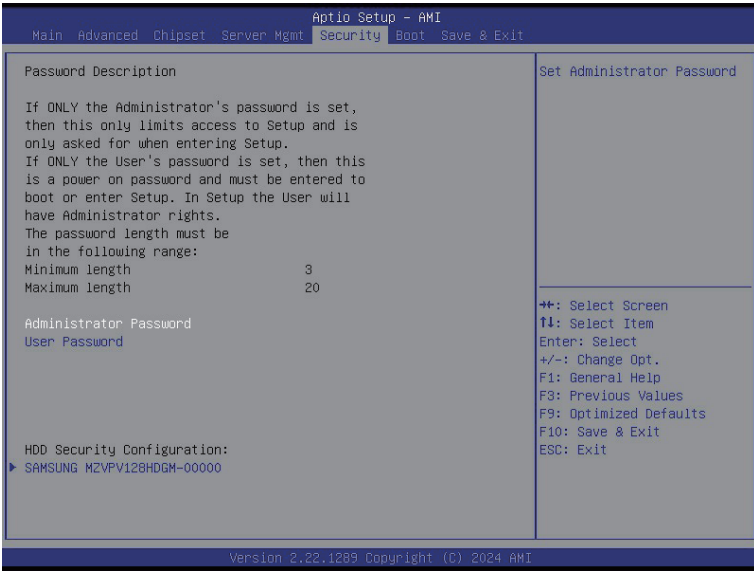
## 5-4-6 IPv6 BMC Network Configuration



Parameter	Description
IPv6 BMC network configuration	
IPv6 BMC Lan Channel 1	
IPv6 BMC Lan Option	Enable/Disable IPv6 BMC LAN channel function. When this item is disabled, the system will not modify any BMC network during BIOS phase. Options available: Unspecified, Disable, Enable. Default setting is <b>Enable</b> .
IPv6 BMC Lan IP Address Source	Selects to configure LAN channel parameters statically or dynamically (by BIOS or BMC). Options available: Unspecified, Static, Dynamic-Obtained by BMC running DHCP. Default setting is <b>Dynamic-Obtained by BMC running DHCP</b> .
IPv6 BMC Lan IP Address/Prefix Length	Check if the IPv6 BMC LAN IP address matches those displayed on the screen.

## 5-5 Security Menu

The Security menu allows you to safeguard and protect the system from unauthorized use by setting up access passwords.



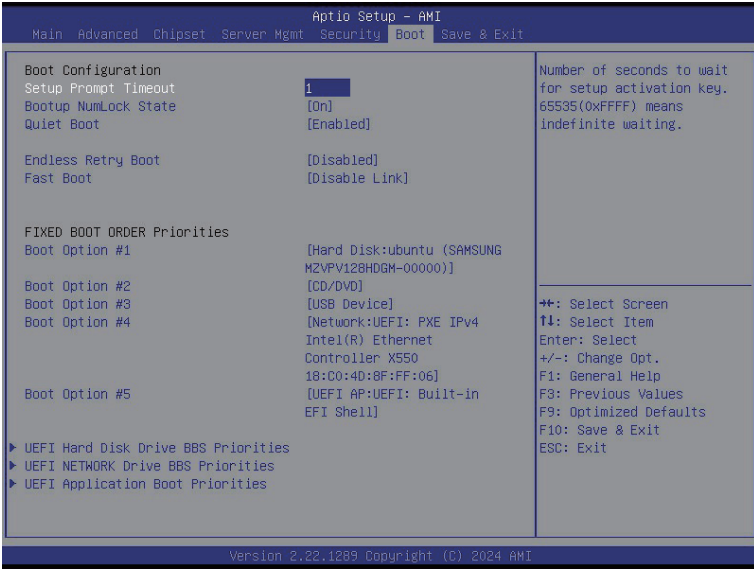
There are two types of passwords that you can set:

- Administrator Password  
Entering this password will allow the user to access and change all settings in the Setup Utility.
- User Password  
Entering this password will restrict a user's access to the Setup menus. To enable or disable this field, a Administrator Password must first be set. A user can only access and modify the System Time, System Date, and Set User Password fields.

Parameter	Description
Administrator Password	Press [Enter] to configure the administrator password.
User Password	Press [Enter] to configure the user password.
Secure Boot	Press [Enter] to configure advanced items.

## 5-6 Boot Menu

The Boot menu allows you to set the drive priority during system boot-up. BIOS setup will display an error message if the legacy drive(s) specified is not bootable.



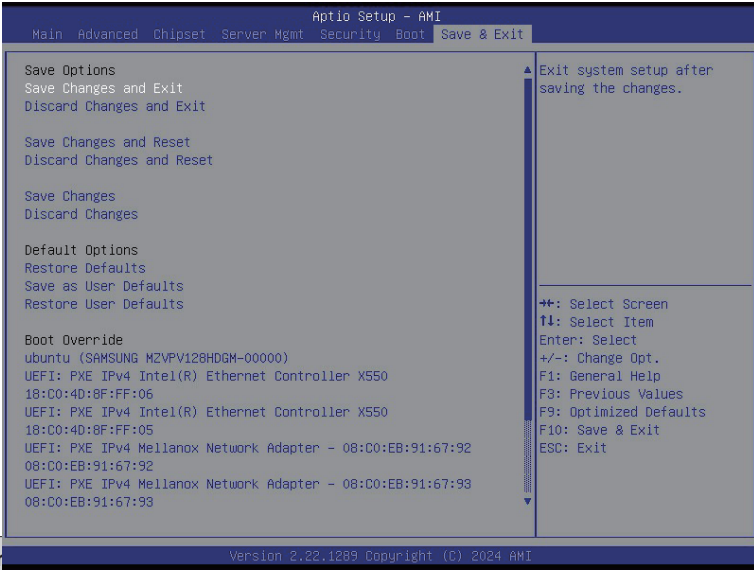
Parameter	Description
Boot Configuration	
Setup Prompt Timeout	Number of seconds to wait for setup activation key. 65535 (0xFFFF) means indefinite waiting. Press the numeric keys to input the desired values.
Bootup NumLock State	Enable/Disable the Bootup NumLock function. Options available: On, Off. Default setting is <b>On</b> .
Quiet Boot	Enable/Disable showing the logo during POST. Options available: Enabled, Disabled. Default setting is <b>Enabled</b> .
Endless Retry Boot	Options available: Disable, Enable. Default setting is <b>Disable</b> .
Fast Boot	Enables or disables Fast Boot to shorten the OS boot process.

Parameter	Description
FIXED BOOT ORDER Priorities	
Boot Option #1 / #2 / #3 / #4 / #5	<p data-bbox="399 200 949 285">Press [Enter] to configure the boot order priority. By default, the server searches for boot devices in the following sequence:</p> <ol data-bbox="436 290 949 431" style="list-style-type: none"> <li data-bbox="436 290 949 313">1. Hard drive.</li> <li data-bbox="436 316 949 340">2. CD-COM/DVD drive.</li> <li data-bbox="436 343 949 366">3. USB device.</li> <li data-bbox="436 370 949 393">4. Network.</li> <li data-bbox="436 396 949 420">5. UEFI.</li> </ol>
UEFI Application Boot Priorities	Press [Enter] to configure the boot priority.



## 5-7 Save & Exit Menu

The Save & Exit menu displays the various options to quit from the BIOS setup. Highlight any of the exit options then press <Enter>.



Save Options	
Save Changes and Exit	Saves changes made and closes the BIOS setup. Options available: Yes, No.
Discard changes and exit	Discards changes made and exits the BIOS setup. Options available: Yes, No.
Save Changes and Reset	Restarts the system after saving the changes made. Options available: Yes, No.
Discard Changes and Reset	Restarts the system without saving any changes. Options available: Yes, No.
Save Changes	Saves changes done so far to any of the setup options. Options available: Yes, No.
Discard Changes	Discards changes made and closes the BIOS setup. Options available: Yes, No.

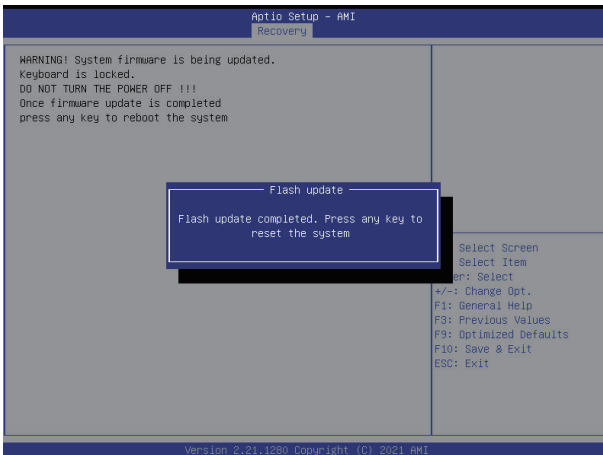
Parameter	Description
Default Options	
Restore Defaults	Loads the default settings for all BIOS setup parameters. Setup Defaults are quite demanding in terms of resources consumption. If you are using low-speed memory chips or other kinds of low-performance components and you choose to load these settings, the system might not function properly. Options available: Yes, No.
Save the User Default Values	Saves the changes made as the user default settings. Options available: Yes, No.
Restore the User Default Values	Loads the user default settings for all BIOS setup parameters. Options available: Yes, No.
Boot Device Priority	Press [Enter] to configure the device as the boot-up drive.
Launch EFI Shell	Attempts to Launch EFI Shell application (Shell.efi) from one of the available file system devices.

# 5-8 BIOS Recovery

The system has an embedded recovery technique. In the event that the BIOS becomes corrupt the boot block can be used to restore the BIOS to a working state. To restore your BIOS, please follow the instructions listed below:

Recovery Instruction:

1. Copy the XXX.rom to USB diskette.
2. Setting BIOS Recovery jump to enabled status.
3. Boot into BIOS recovery.
4. Run Proceed with flash update.
5. BIOS updated.



## 5-9 BIOS POST Beep code (AMI standard)

### 5-9-1 PEI Beep Codes

# of Beeps	Description
1	Memory not Installed.
1	Memory was installed twice (InstallPeiMemory routine in PEI Core called twice)
2	Recovery started
3	DXE IPL was not found
3	DXE Core Firmware Volume was not found
4	Recovery failed
4	S3 Resume failed
7	Reset PPI is not available

### 5-9-2 DXE Beep Codes

# of Beeps	Description
1	Invalid password
4	Some of the Architectural Protocols are not available
5	No Console Output Devices are found
5	No Console Input Devices are found
6	Flash update is failed
7	Reset protocol is not available
8	Platform PCI resource requirements cannot be met