GIGABYTE™

G482-Z54

HPC Server - 4U DP 8 x Gen4 GPU Server

User Manual

Rev. 1.0
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Documentation Classifications
In order to assist in the use of this product, GIGABYTE provides the following types of documentation:
- User Manual: detailed information & steps about the installation, configuration and use of this product (e.g. motherboard, server barebones), covering hardware and BIOS.
- User Guide: detailed information about the installation & use of an add-on hardware or software component (e.g. BMC firmware, rail-kit) compatible with this product.
- Quick Installation Guide: a short guide with visual diagrams that you can reference easily for installation purposes of this product (e.g. motherboard, server barebones).

Please see the support section of the online product page to check the current availability of these documents.

For More Information
For related product specifications, the latest firmware and software, and other information please visit our website at http://www.gigabyte.com

For GIGABYTE distributors and resellers, additional sales & marketing materials are available from our reseller portal: http://reseller.b2b.gigabyte.com

For further technical assistance, please contact your GIGABYTE representative or visit https://esupport.gigabyte.com/ to create a new support ticket

For any general sales or marketing enquiries, you may also message GIGABYTE server directly by email: server.grp@gigabyte.com
Conventions
The following conventions are used in this user's guide:

<table>
<thead>
<tr>
<th>WARNING!</th>
<th>Alerts you to any damage that might result from doing or not doing specific actions.</th>
</tr>
</thead>
<tbody>
<tr>
<td>CAUTION!</td>
<td>Gives precautionary measures to avoid possible hardware or software problems.</td>
</tr>
<tr>
<td>NOTE!</td>
<td>Gives bits and pieces of additional information related to the current topic.</td>
</tr>
</tbody>
</table>
Server Warnings and Cautions
Before installing a server, be sure that you understand the following warnings and cautions.

⚠️ WARNING!
To reduce the risk of electric shock or damage to the equipment:
• Do not disable the power cord grounding plug. The grounding plug is an important safety feature.
• Plug the power cord into a grounded (earthed) electrical outlet that is easily accessible at all times.
• Unplug all the power cords from the power supplies to disconnect power to the equipment.

⚠️ WARNING!
• Unplug the power cord from the power supply to disconnect power to the equipment.
• Do not route the power cord where it can be walked on or pinched by items placed against it. Pay particular attention to the plug, electrical outlet, and the point where the cord extends from the server.

⚠️ WARNING!
To reduce the risk of personal injury from hot surfaces, allow the drives and the internal system components to cool before touching them.

⚠️ WARNING!
This server is equipped with high speed fans. Keep away from hazardous moving fan blades during servicing.

⚠️ WARNING!
This equipment is intended to be used in Restrict Access Location. The access can only be gained by Skilled person. Only authorized by well trained professional person can access the restrict access location.

⚠️ CAUTION!
• Do not operate the server for long periods with the access panel open or removed. Operating the server in this manner results in improper airflow and improper cooling that can lead to thermal damage.
• Danger of explosion if battery is incorrectly replaced.
• Replace only with the same or equivalent type recommended by the manufacturer.
• Dispose of used batteries according to the manufacturer’s instructions.
Electrostatic Discharge (ESD)

CAUTION!

ESD CAN DAMAGE DRIVES, BOARDS, AND OTHER PARTS. WE RECOMMEND THAT YOU PERFORM ALL PROCEDURES AT AN ESD WORKSTATION. IF ONE IS NOT AVAILABLE, PROVIDE SOME ESD PROTECTION BY WEARING AN ANTI-STATIC WRIST STRAP ATTACHED TO CHASSIS GROUND -- ANY UNPAINTED METAL SURFACE -- ON YOUR SERVER WHEN HANDLING PARTS.

Always handle boards carefully. They can be extremely sensitive to ESD. Hold boards only by their edges without any component and pin touching. After removing a board from its protective wrapper or from the system, place the board component side up on a grounded, static free surface. Use a conductive foam pad if available but not the board wrapper. Do not slide board over any surface.

System power on/off: To remove power from system, you must remove the system from rack. Make sure the system is removed from the rack before opening the chassis, adding, or removing any non hot-plug components.

Hazardous conditions, devices and cables: Hazardous electrical conditions may be present on power, telephone, and communication cables. Turn off the system and disconnect the cables attached to the system before servicing it. Otherwise, personal injury or equipment damage can result.

Electrostatic discharge (ESD) and ESD protection: ESD can damage drives, boards, and other parts. We recommend that you perform all procedures in this chapter only at an ESD workstation. If one is not available, provide some ESD protection by wearing an antistatic wrist strap attached to chassis ground (any unpainted metal surface on the server) when handling parts.

ESD and handling boards: Always handle boards carefully. They can be extremely sensitive to electrostatic discharge (ESD). Hold boards only by their edges. After removing a board from its protective wrapper or from the system, place the board component side up on a grounded, static free surface. Use a conductive foam pad if available but not the board wrapper. Do not slide board over any surface.

Installing or removing jumpers: A jumper is a small plastic encased conductor that slips over two jumper pins. Some jumpers have a small tab on top that can be gripped with fingertips or with a pair of fine needle nosed pliers. If the jumpers do not have such a tab, take care when using needle nosed pliers to remove or install a jumper; grip the narrow sides of the jumper with the pliers, never the wide sides. Gripping the wide sides can damage the contacts inside the jumper, causing intermittent problems with the function controlled by that jumper. Take care to grip with, but not squeeze, the pliers or other tool used to remove a jumper, or the pins on the board may bend or break.
CAUTION!

Risk of explosion if battery is replaced incorrectly or with an incorrect type. Replace the battery only with the same or equivalent type recommended by the manufacturer. Dispose of used batteries according to the manufacturer’s instructions.
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Chapter 1  Hardware Installation

1-1  Installation Precautions

The motherboard/system contain numerous delicate electronic circuits and components which can become damaged as a result of electrostatic discharge (ESD). Prior to installation, carefully read the user manual and follow these procedures:

- Prior to installation, do not remove or break motherboard S/N (Serial Number) sticker or warranty sticker provided by your dealer. These stickers are required for warranty validation.
- Always remove the AC power by unplugging the power cord from the power outlet before installing or removing the motherboard or other hardware components.
- When connecting hardware components to the internal connectors on the motherboard, make sure they are connected tightly and securely.
- When handling the motherboard, avoid touching any metal leads or connectors.
- It is best to wear an electrostatic discharge (ESD) wrist strap when handling electronic components such as a motherboard, CPU or memory. If you do not have an ESD wrist strap, keep your hands dry and first touch a metal object to eliminate static electricity.
- Prior to installing the motherboard, please have it on top of an antistatic pad or within an electrostatic shielding container.
- Before unplugging the power supply cable from the motherboard, make sure the power supply has been turned off.
- Before turning on the power, make sure the power supply voltage has been set according to the local voltage standard.
- Before using the product, please verify that all cables and power connectors of your hardware components are connected.
- To prevent damage to the motherboard, do not allow screws to come in contact with the motherboard circuit or its components.
- Make sure there are no leftover screws or metal components placed on the motherboard or within the computer casing.
- Do not place the computer system on an uneven surface.
- Do not place the computer system in a high-temperature environment.
- Turning on the computer power during the installation process can lead to damage to system components as well as physical harm to the user.
- If you are uncertain about any installation steps or have a problem related to the use of the product, please consult a certified computer technician.
### 1-2 Product Specifications

**NOTE:**
We reserve the right to make any changes to the product specifications and product-related information without prior notice.

<table>
<thead>
<tr>
<th>Feature</th>
<th>Specification</th>
</tr>
</thead>
<tbody>
<tr>
<td><strong>CPU</strong></td>
<td>- AMD EPYC™ 7002 series processor family&lt;br&gt;- Dual processors, 7nm&lt;br&gt;- Up to 64-core, 128 threads per processor&lt;br&gt;- TDP up to 225W, cTDP up to 280W</td>
</tr>
<tr>
<td><strong>Socket</strong></td>
<td>- SP3</td>
</tr>
<tr>
<td><strong>Chipset</strong></td>
<td>- System on Chip</td>
</tr>
<tr>
<td><strong>Memory</strong></td>
<td>- 32 x DIMM slots&lt;br&gt;- DDR4 memory supported only&lt;br&gt;- 8-Channel memory per processor architecture&lt;br&gt;- RDIMM modules up to 128GB supported&lt;br&gt;- LRDIMM modules up to 128GB supported&lt;br&gt;- Memory speed: Up to 3200*/ 2933 MHz</td>
</tr>
<tr>
<td><strong>LAN</strong></td>
<td>Front side:&lt;br&gt;- 2 x 1Gb/s BASE-T LAN ports (Intel® I350-AM2)&lt;br&gt;- 1 x 10/100/1000 management LAN</td>
</tr>
<tr>
<td><strong>Expansion Slot</strong></td>
<td>8 x PCIe x16 slots (Gen4 x16 bus) for GPUs&lt;br&gt;- 1 x FHFL PCIe Gen4 x16 expansion slot in rear side#</td>
</tr>
<tr>
<td></td>
<td># Non-supported SAS Card with internal cable due to cable routing limitation</td>
</tr>
<tr>
<td><strong>Video</strong></td>
<td>- Integrated in Aspeed® AST2500&lt;br&gt;- 2D Video Graphic Adapter with PCIe bus interface&lt;br&gt;- 1920x1200@60Hz 32bpp, DDR4 SDRAM</td>
</tr>
<tr>
<td><strong>Storage</strong></td>
<td>2 x 2.5&quot; NVMe, 8 x 2.5&quot; SATA hot-swappable HDD/SSD bays*&lt;br&gt;NOTE: non-supported SAS devices</td>
</tr>
<tr>
<td><strong>SATA</strong></td>
<td>- Supported</td>
</tr>
</tbody>
</table>
Hardware Installation

Internal I/O
- 1 x TPM header
- 1 x Front panel header

Front I/O
- 2 x USB 3.0
- 1 x VGA
- 2 x RJ45
- 1 x MLAN
- 1 x Power button with LED
- 1 x ID button with LED
- 1 x Reset button
- 1 x NMI button
- 1 x System status LED
- 1 x HDD access LED

Backplane I/O
- 2 x NVMe and 8 x SATA ports

TPM
- 1 x TPM header with SPI interface
- Optional TPM2.0 kit: CTM010

System Management
- Aspeed® AST2500 management controller
- GIGABYTE Management Console (AMI MegaRAC SP-X) web interface
  - Dashboard
  - HTML5 KVM
  - Sensor Monitor (Voltage, RPM, Temperature, CPU Status …etc.)
  - Sensor Reading History Data
  - FRU Information
  - SEL Log in Linear Storage / Circular Storage Policy
  - Hardware Inventory
  - Fan Profile
  - System Firewall
  - Power Consumption
  - Power Control
  - LDAP / AD / RADIUS Support
  - Backup & Restore Configuration
  - Remote BIOS/BMC/CPLD Update
  - Event Log Filter
  - User Management
  - Media Redirection Settings
  - PAM Order Settings
  - SSL Settings
  - SMTP Settings
  - OS Compatibility
| Power Supply | • 3 x 2200W redundant PSUs  
• 80 PLUS Platinum  

• AC Input:  
  100 - 127V / 14A, 47 - 63Hz  
  200 - 240V / 12.6A, 47 - 63Hz  

• DC Output:  
  Max 1200W/ 100-127V~  
  +12.12V/ 95.6A  
  +12Vsb/ 3.5A  
  Max 2200W/ 200-240V  
  +12.12V/ 178.1A  
  +12Vsb/ 3.5A  

NOTE: The system power supply requires C19 type power cord. |
| Operating Properties | • Operating temperature: 10°C to 35°C  
• Non-operating temperature: -40°C to 60°C  
• Operating humidity: 8 - 80% (non-condensing)  
• Non-operating humidity: 20% - 95% (non-condensing) |
| System Dimension | • 4U  
• 448 (W) x 176 (H) x 880 (D) (mm)  
• 17.64 (W) x 6.93 (H) x 34.65 (D) (inch) |
1-3 System Block Diagram
Chapter 2  System Appearance

2-1  Front View

- Go to the section 2-3 Front Panel Buttons and LEDs for detail description of function LEDs.

<table>
<thead>
<tr>
<th>No.</th>
<th>Description</th>
<th>No.</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>1.</td>
<td>Front Panel LEDs and Buttons</td>
<td>4.</td>
<td>USB 3.0 Port x 2</td>
</tr>
<tr>
<td>2.</td>
<td>GbE LAN Port x 2</td>
<td>5.</td>
<td>VGA Port</td>
</tr>
<tr>
<td>3.</td>
<td>10/100/1000 Server Management LAN Port</td>
<td>6.</td>
<td>2.5-inch HDD Bay x 10 (#0 to #9)</td>
</tr>
</tbody>
</table>
## 2-2 Rear View

<table>
<thead>
<tr>
<th>No.</th>
<th>Description</th>
<th>No.</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>1.</td>
<td>PCIe x16 Slot x 8</td>
<td>2.</td>
<td>PCIe x16 Slot x 1</td>
</tr>
</tbody>
</table>
2-3  Front Panel Buttons and LEDs

<table>
<thead>
<tr>
<th>No.</th>
<th>Name</th>
<th>Color</th>
<th>Status</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>1.</td>
<td>Reset Button</td>
<td>--</td>
<td>--</td>
<td>Press this button to reset the system.</td>
</tr>
<tr>
<td>2.</td>
<td>HDD Status LED</td>
<td>Green</td>
<td>On</td>
<td>Indicates locating the HDD.</td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td>Blink</td>
<td>Indicates accessing the HDD.</td>
</tr>
<tr>
<td></td>
<td></td>
<td>Amber</td>
<td>On</td>
<td>Indicates HDD error.</td>
</tr>
<tr>
<td></td>
<td></td>
<td>Green / Amber</td>
<td>Blink</td>
<td>Indicates HDD rebuilding.</td>
</tr>
<tr>
<td></td>
<td></td>
<td>N/A</td>
<td>Off</td>
<td>Indicates no HDD access or no HDD error.</td>
</tr>
<tr>
<td>3.</td>
<td>System Status LED</td>
<td>Green</td>
<td>Solid On</td>
<td>Indicates system is operating normally.</td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td>Solid On</td>
<td>Indicates a critical condition, may include:</td>
</tr>
<tr>
<td></td>
<td></td>
<td>Amber</td>
<td>Blink</td>
<td>Indicates non-critical condition, may include:</td>
</tr>
<tr>
<td></td>
<td></td>
<td>N/A</td>
<td>Off</td>
<td>Indicates system is not ready, may include:</td>
</tr>
<tr>
<td>4.</td>
<td>NMI Button</td>
<td>--</td>
<td>--</td>
<td>Press this button for the server to generate a NMI to the processor.</td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td></td>
<td>If multiple-bit ECC errors occur, the server will effectively be handled.</td>
</tr>
<tr>
<td>5.</td>
<td>ID Button with LED</td>
<td>--</td>
<td>--</td>
<td>Press this button to activate system identification.</td>
</tr>
<tr>
<td>6.</td>
<td>Power Button with LED</td>
<td>Green</td>
<td>On</td>
<td>Indicates the system is powered on.</td>
</tr>
<tr>
<td></td>
<td></td>
<td>Green</td>
<td>Blink</td>
<td>System is in ACPI S1 slate (sleep mode).</td>
</tr>
<tr>
<td></td>
<td></td>
<td>N/A</td>
<td>Off</td>
<td>Indicates system is not powered on or in ACPI S5 slate (power off) or system is in ACPI S4 slate (hibernation mode).</td>
</tr>
</tbody>
</table>
## 2-4 Front System LAN LEDs

<table>
<thead>
<tr>
<th>No.</th>
<th>Name</th>
<th>Color</th>
<th>Status</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>1.</td>
<td>GbE Speed LED</td>
<td>Yellow</td>
<td>On</td>
<td>1 Gbps data rate</td>
</tr>
<tr>
<td></td>
<td></td>
<td>Green</td>
<td>On</td>
<td>100 Mbps data rate</td>
</tr>
<tr>
<td></td>
<td></td>
<td>N/A</td>
<td>Off</td>
<td>10 Mbps data rate</td>
</tr>
<tr>
<td>2.</td>
<td>GbE Link / Activity LED</td>
<td>Green</td>
<td>On</td>
<td>Link between system and network or no access</td>
</tr>
<tr>
<td></td>
<td></td>
<td>Blink</td>
<td>Data transmission or reception is occurring.</td>
<td></td>
</tr>
<tr>
<td></td>
<td></td>
<td>N/A</td>
<td>Off</td>
<td>No data transmission or reception is occurring.</td>
</tr>
</tbody>
</table>
## 2-5 Power Supply Unit (PSU) LED

![PSU LED](image)

<table>
<thead>
<tr>
<th>Color</th>
<th>Status</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>Off</td>
<td>--</td>
<td>No AC power to all power supplies</td>
</tr>
<tr>
<td>Green</td>
<td>On</td>
<td>+12V output ON and OK</td>
</tr>
<tr>
<td>Green</td>
<td>Blinking 0.5Hz</td>
<td>AC present / Only +12VSB on (PS off) or PSU in Smart Standby Mode</td>
</tr>
<tr>
<td>Green</td>
<td>Blinking 2Hz</td>
<td>Power supply firmware update</td>
</tr>
<tr>
<td>Amber</td>
<td>On</td>
<td>AC cord unplugged / AC power lost but a second power supply in parallel still having AC input power</td>
</tr>
<tr>
<td>Amber</td>
<td>Blinking 0.5Hz</td>
<td>Power supply warning events where the power supply continues to operate, such as: high temperature, high power, high current, slot fan</td>
</tr>
</tbody>
</table>
## 2-6 Hard Disk Drive LEDs

<table>
<thead>
<tr>
<th>RAID SKU</th>
<th>Disk LED (LED on Back Panel)</th>
<th>Disk LED</th>
<th>Removed HDD Slot (LED on Back Panel)</th>
<th>Removed HDD Slot</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td>No RAID configuration (via HBA)</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>Green ON(*)</td>
<td>OFF</td>
<td>BLINK (*2)</td>
<td>OFF</td>
</tr>
<tr>
<td></td>
<td>Amber OFF</td>
<td>OFF</td>
<td>OFF</td>
<td>OFF</td>
</tr>
<tr>
<td></td>
<td>Green ON(*)</td>
<td>OFF</td>
<td>--</td>
<td>--</td>
</tr>
<tr>
<td></td>
<td>Amber OFF</td>
<td>OFF</td>
<td>--</td>
<td>--</td>
</tr>
<tr>
<td></td>
<td>RAID configuration (via HW RAID Card or SW RAID Card)</td>
<td>Disk LED</td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>Green ON</td>
<td>OFF</td>
<td>BLINK (*2)</td>
<td>OFF</td>
</tr>
<tr>
<td></td>
<td>Amber OFF</td>
<td>ON (Low Speed: 2 Hz)</td>
<td>OFF</td>
<td>OFF</td>
</tr>
<tr>
<td></td>
<td>Green ON(*)</td>
<td>OFF</td>
<td>(*3)</td>
<td>--</td>
</tr>
<tr>
<td></td>
<td>Amber OFF</td>
<td>ON</td>
<td>(*3)</td>
<td>--</td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>LED 2</th>
<th>HDD Present</th>
<th>No HDD</th>
</tr>
</thead>
<tbody>
<tr>
<td>Green</td>
<td>ON</td>
<td>OFF</td>
</tr>
</tbody>
</table>

**NOTE:**

*1: Depends on HBA/Utility Spec.

*2: Blink cycle depends on HDD's activity signal.

*3: If HDD is pulled out during rebuilding, the disk status of this HDD is regarded as faulty.
Chapter 3  System Hardware Installation

Pre-installation Instructions

System components and electronic circuit boards can be damaged by discharges of static electricity. Working on systems that are still connected to a power supply can be extremely dangerous. Follow the simple guidelines below to avoid damage to your system or injury to yourself.

• Always disconnect the system from the power outlet whenever you are working inside the system case.

• If possible, wear a grounded wrist strap when you are working inside the system case. Alternatively, discharge any static electricity by touching the bare metal system of the system case, or the bare metal body of any other grounded appliance.

• Hold electronic circuit boards by the edges only. Do not touch the components on the board unless it is necessary to do so. Do not flex or stress the circuit board.

• Leave all components inside the static-proof packaging until you are ready to use the component for the installation.
3-1 Removing and Installing the Top Rear Cover

Before you remove the top rear cover:
- Make sure the system is not turned on or connected to AC power.

Follow these instructions to remove the top rear cover:

1. Remove the single screw securing the cover.
2. Loosen the two thumbnail screws securing the top rear cover in place.
3. Slide the cover towards the rear and remove the cover in the direction indicated.
4. Follow steps 1-4 in reverse order to re-install the top rear cover.
3-2 Removing and Installing the Air Duct

Before you remove the top rear cover:
- Make sure the system is not turned on or connected to AC power.

Follow these instructions to remove and install the air duct:

1. Lift up to remove the air duct.
2. To install the air duct, align the rear edge of air duct with the GPU module brackets ensuring that the arrows on the air duct face the rear of the system as shown in the image below, and then push down the air duct into chassis until it firmly seats.
3-3 Installing the GPU Card (Optional)

Before you install the GPU card:

- Voltages can be present within the server whenever an AC power source is connected. This voltage is present even when the main power switch is in the off position. Ensure that the system is powered down and all power sources have been disconnected from the server prior to installing a GPU card. Make sure the system is not turned on or connected to AC power.
- Failure to observe these warnings could result in personal injury or damage to the equipment.

Follow these instructions to install the GPU card:

1. Remove the three screws securing the GPU card bracket in place.
2. Slightly lift the GPU card bracket up in the direction indicated as shown in the image below.
3. Remove the two screws securing the GPU card slot covers and remove the GPU slot covers.
4. Insert the GPU card into the selected slot. Make sure the GPU card is properly seated.
5. Install the two screws to secure the GPU card in place.
6. Install the three screws to secure the GPU card bracket in place.
3-4 Installing the PCIe Card

Read the following guidelines before you begin to install the PCIe Card:

- Voltages can be present within the server whenever an AC power source is connected. This voltage is present even when the main power switch is in the off position. Ensure that the system is powered down and all power sources have been disconnected from the server prior to installing a PCIe card. Make sure the system is not turned on or connected to AC power.
- Failure to observe these warnings could result in personal injury or damage to the equipment.

Follow these instructions to install the PCIe card:

1. Pull out the thumbnail screw securing the GPU card cage in place.
2. Flip over the GPU card cage in the direction indicated.
3. Remove the two screws securing the PCIe card slot covers in place and remove the PCIe card slot covers.
4. Insert the PCIe card into the selected slot. Make sure the PCIe card is properly seated.
5. Install the two screws to secure the PCIe card in place.
3-5 Removing and Installing the Heat Sink

Read the following guidelines before you begin to install the heat sink:

- Always turn off the computer and unplug the power cord from the power outlet before installing the heat sink to prevent hardware damage.
- Unplug all cables from the power outlets.
- Disconnect all telecommunication cables from their ports.
- Place the system unit on a flat and stable surface.
- Open the system according to the instructions.

**WARNING!**

Failure to turn off the server before you start installing components may cause serious damage. Do not attempt the procedures described in the following sections unless you are a qualified service technician.

**Follow these instructions to install the heatsink:**

1. Loosen the four captive screws securing the heat sink in place in reverse order (4→3→2→1).
2. Lift and remove the heatsink from the system.
3. To install the heatsink, reverse steps 1-2 while ensuring that you tighten the captive screws in sequential order (1→2→3→4) as seen in the image below.
3-6 Removing and Installing the CPU

Read the following guidelines before you begin to install the CPU:
• Make sure that the motherboard supports the CPU.
• Always turn off the system and unplug the power cord from the power outlet before installing the CPU to prevent hardware damage.
• Unplug all cables from the power outlets.
• Disconnect all telecommunication cables from their ports.
• Place the system unit on a flat and stable surface.
• Open the system according to the instructions.

WARNING!
Failure to properly turn off the server before you start installing components may cause serious damage. Do not attempt the procedures described in the following sections unless you are a qualified service technician.

Follow these instructions to install the CPU:

1. Loosen the three captive screws securing the CPU cover in sequential order (1→2→3).
2. Flip open the CPU cover.
3. Remove the CPU carrier from the CPU frame using the handle on the CPU carrier.
4. Using the handle on the CPU carrier insert the new CPU carrier with CPU installed into the CPU frame.
   NOTE: Ensure the CPU is installed in the CPU carrier in the correct orientation, with the triangle on the CPU aligned to the top left corner of the CPU carrier.
5. Flip the CPU frame with CPU installed into place in the CPU socket.
6. Flip the CPU cover into place over the CPU socket.
7. Tighten the CPU cover screws in sequential order (1→2→3) to secure the CPU cover in place.
8. Repeat steps 1-7 for the second CPU.
9. To remove the CPUs, follow steps 1-7 in reverse order.
• When installing the heat sink over the CPU, use T20-Lobe driver to tighten the 4 captive nuts in sequential order (1→2→3→4).
• The screw tightening torque: 16.1 ± 1.2 kgf-cm (14.0± 1.0 lbf-in)
### 3-7 Removing and Installing the Memory

Read the following guidelines before you begin to install the memory:

- Make sure that the motherboard supports the memory. It is recommended that memory of the same capacity, brand, speed, and chips be used.
- Always turn off the system and unplug the power cord from the power outlet before installing the memory to prevent hardware damage.
- Memory modules have a foolproof design. A memory module can be installed in only one direction. If you are unable to insert the memory, switch the direction.

### 3-7-1 Eight Channel Memory Configuration

This motherboard provides 32 DDR4 memory slots and supports Eight Channel Technology. After the memory is installed, the BIOS will automatically detect the specifications and capacity of the memory. Enabling Four Channel memory mode will be four times of the original memory bandwidth.
3-7-2 Installing the Memory

- Before installing a memory module, make sure to turn off the system and unplug the power cord from the power outlet to prevent damage to the memory module.
- Be sure to install DDR4 DIMMs on this motherboard.

Follow these instructions to install the Memory:

1. Insert the DIMM memory module vertically into the DIMM slot, and push it down.
2. Close the plastic clip at both edges of the DIMM slots to lock the DIMM module.
3. Reverse the installation steps when you want to remove the DIMM module.

3-7-3 Processor and Memory Module Matrix Table

<table>
<thead>
<tr>
<th>Processor and Memory Module Matrix Table</th>
</tr>
</thead>
<tbody>
<tr>
<td>CPU#</td>
</tr>
<tr>
<td>------</td>
</tr>
<tr>
<td>8 DIMMs</td>
</tr>
<tr>
<td>CPU0</td>
</tr>
<tr>
<td>16 DIMMs</td>
</tr>
<tr>
<td>CPU0</td>
</tr>
<tr>
<td>CPU1</td>
</tr>
<tr>
<td>32 DIMMs</td>
</tr>
<tr>
<td>CPU0</td>
</tr>
<tr>
<td>CPU1</td>
</tr>
</tbody>
</table>

System Hardware Installation - 35 -
• When only one DIMM is used, it must be populated in memory slot DIMM1.

### EPYC Memory Speed based on DIMM Population (One DIMM per Channel)

<table>
<thead>
<tr>
<th>DIMM Type</th>
<th>DIMM Population</th>
<th>Max EPYC 7003 DDR Frequency (MHz)</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td>DIMM 0</td>
<td></td>
</tr>
<tr>
<td>RDIMM</td>
<td>1R (1 Rank)</td>
<td>3200</td>
</tr>
<tr>
<td></td>
<td>2R or 2DR (2 Ranks)</td>
<td>3200</td>
</tr>
<tr>
<td>LRDIMM</td>
<td>4DR (4 Ranks)</td>
<td>3200</td>
</tr>
<tr>
<td></td>
<td>2S2R (4 Ranks)</td>
<td>3200</td>
</tr>
<tr>
<td></td>
<td>2S4R (8 Ranks)</td>
<td>3200</td>
</tr>
</tbody>
</table>

### EPYC Memory Speed based on DIMM Population (Two DIMM per Channel)

<table>
<thead>
<tr>
<th>DIMM Type</th>
<th>DIMM Population</th>
<th>Max EPYC 7003 DDR Frequency (MHz)</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td>DIMM 0</td>
<td>DIMM 1</td>
</tr>
<tr>
<td>RDIMM</td>
<td>--</td>
<td>1R</td>
</tr>
<tr>
<td></td>
<td>1R</td>
<td>1R</td>
</tr>
<tr>
<td></td>
<td>--</td>
<td>2R or 2DR</td>
</tr>
<tr>
<td></td>
<td>1R</td>
<td>2R or 2DR</td>
</tr>
<tr>
<td></td>
<td>2R or 2DR</td>
<td>2R or 2DR</td>
</tr>
<tr>
<td>LRDIMM</td>
<td>--</td>
<td>4DR</td>
</tr>
<tr>
<td></td>
<td>4DR</td>
<td>4DR</td>
</tr>
<tr>
<td></td>
<td>--</td>
<td>2S2R (4 Ranks)</td>
</tr>
<tr>
<td></td>
<td>--</td>
<td>2S4R (8 Ranks)</td>
</tr>
<tr>
<td></td>
<td>2S2R (4 Ranks)</td>
<td>2S2R (4 Ranks)</td>
</tr>
</tbody>
</table>
3-8 Removing and Installing the 2.5" Hard Disk Drive

Read the following guidelines before you begin to install the hard disk drive:

- Take note of the drive tray orientation before sliding it out.
- The tray will not fit back into the bay if inserted incorrectly.

Follow these instructions to install a 2.5" hard disk drive:

1. Press the release button.
2. Extend the locking lever.
3. Pull the locking lever to remove the HDD tray.
4. Slide the hard disk drive into the blank HDD tray.
5. Secure the hard drive to the tray with the four screws as shown. Do not over tighten the screws. Slide the hard drive tray into the bay until it locks in place.

CAUTION!

We strongly recommend using enterprise level hard disk drives in the Gigabyte server system. For more information of recommended HDDs, please visit the Gigabyte website: https://www.gigabyte.com and search for the specific product QVL from Support & Downloads.
3-9 Replacing the System FAN Module

CAUTION!
Before you remove or install the system fans follow these steps:
• Make sure the system is not turned on or connected to the AC power.
• Disconnect all necessary cable connections. Failure to observe these warnings could result in personal injury or damage to the equipment.

Follow these instructions to replace the system fan module:
[For all system fan modules]
1. Grasp the finger slots of the fan module and pull up to remove the fan module.
2. Reverse the previous steps to install the replacement fan module.
3-10 Removing and Installing the Top Front Cover

Skip this section if you are not going to replace the PCIe RAID card.

Follow these instructions to remove the top front cover:

1. Remove the three screws securing the cover.
2. Slide the cover towards the rear and remove the cover in the direction indicated.
3. Reverse the previous steps to install the top front cover.
3-11 Removing and Installing the Power Supply

CAUTION!

• In order to reduce the risk of injury from electric shock, disconnect AC power from the power supply before removing the power supply from the system.
• Please see Section 2-2 "Rear View" for installation sequence.

Follow these instructions to replace the power supply:

1. Flip up and then grasp the power supply handle.
2. Press the retaining clip on the right side of the power supply in the direction indicated.
3. Pull out the power supply using the handle.
4. Insert the replacement power supply firmly into the chassis. Connect the AC power cord to the replacement power supply.
3-12 Cable Connections
Motherboard to PCIe Board and Front IO Board and HDD Backplane Board
Motherboard to 2.5" HDD Backplane Board
## Chapter 4  Motherboard Components

### 4-1  Motherboard Components

<table>
<thead>
<tr>
<th>Item</th>
<th>Description</th>
<th>Item</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>1</td>
<td>2 x 15 Pin HDD Back Plane Board Connector</td>
<td>2</td>
<td>SlimLine Connectors (PCIe Gen4 Signal)</td>
</tr>
<tr>
<td>3</td>
<td>SlimLine Connectors (PCIe Gen4 Signal)</td>
<td>4</td>
<td>SlimLine Connectors (PCIe Gen4 Signal)</td>
</tr>
<tr>
<td>5</td>
<td>SlimLine Connectors (PCIe Gen4 Signal)</td>
<td>6</td>
<td>SlimLine Connectors (PCIe Gen4 Signal)</td>
</tr>
<tr>
<td>7</td>
<td>SlimLine Connectors (PCIe Gen4 Signal)</td>
<td>8</td>
<td>SlimLine Connectors (PCIe Gen4 Signal)</td>
</tr>
<tr>
<td>9</td>
<td>TPM Module Connector</td>
<td>10</td>
<td>USB 3.0 Header</td>
</tr>
<tr>
<td>11</td>
<td>SlimLine Connector (PCIe Gen4 Signal)</td>
<td>12</td>
<td>SlimLine Connector (PCIe Gen4 Signal)</td>
</tr>
<tr>
<td>13</td>
<td>SlimLine Connector (PCIe Gen4 Signal)</td>
<td>14</td>
<td>2 x 7 Pin HDD Back Plane Board Power Connector</td>
</tr>
<tr>
<td>15</td>
<td>2 x9 Pin System FAN Power Connector</td>
<td>16</td>
<td>2 x 4 Pin Front Panel Power Connector</td>
</tr>
<tr>
<td>17</td>
<td>SlimLine Connector (PCIe Gen4 Signal)</td>
<td>18</td>
<td>SlimLine Connector (SATA Signal)</td>
</tr>
<tr>
<td>19</td>
<td>SlimLine Connector (NVMe Signal)</td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td></td>
<td>19</td>
<td></td>
</tr>
</tbody>
</table>
4-2  Jumper Settings

Clear CMOS Jumper

<table>
<thead>
<tr>
<th>J1</th>
<th>ON DIP</th>
<th>OFF</th>
</tr>
</thead>
<tbody>
<tr>
<td>1</td>
<td>HSMB_SEL</td>
<td>BIOS Defined</td>
</tr>
<tr>
<td>2</td>
<td>PMBUS_SEL</td>
<td>BIOS Defined</td>
</tr>
<tr>
<td>3</td>
<td>BIOS_PWDO</td>
<td>Clear Supervisor Password Normal [Default]</td>
</tr>
<tr>
<td>4</td>
<td>BIOS_RCVR</td>
<td>BIOS Recovery Mode Normal [Default]</td>
</tr>
</tbody>
</table>

Clear CMOS Jumper

- J1
- J2
Chapter 5  BIOS Setup

BIOS (Basic Input and Output System) records hardware parameters of the system in the EFI on the motherboard. Its major functions include conducting the Power-On Self-Test (POST) during system startup, saving system parameters, loading the operating system etc. The BIOS includes a BIOS Setup program that allows the user to modify basic system configuration settings or to activate certain system features. When the power is turned off, the battery on the motherboard supplies the necessary power to the CMOS to keep the configuration values in the CMOS.

To access the BIOS Setup program, press the <DEL> key during the POST when the power is turned on.

- BIOS flashing is potentially risky, if you do not encounter any problems when using the current BIOS version, it is recommended that you don't flash the BIOS. To flash the BIOS, do it with caution. Inadequate BIOS flashing may result in system malfunction.
- It is recommended that you not alter the default settings (unless you need to) to prevent system instability or other unexpected results. Inadequately altering the settings may result in system's failure to boot. If this occurs, try to clear the CMOS values and reset the board to default values. (Refer to the Exit section in this chapter or introductions of the battery/clearing CMOS jumper in Chapter 4 for how to clear the CMOS values.)

**BIOS Setup Program Function Keys**

<table>
<thead>
<tr>
<th>Function Key</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>&lt;←&gt;&lt;→&gt;</td>
<td>Move the selection bar to select the screen</td>
</tr>
<tr>
<td>↑↓</td>
<td>Move the selection bar to select an item</td>
</tr>
<tr>
<td>++</td>
<td>Increase the numeric value or make changes</td>
</tr>
<tr>
<td>-</td>
<td>Decrease the numeric value or make changes</td>
</tr>
<tr>
<td>ENTER</td>
<td>Execute command or enter the submenu</td>
</tr>
<tr>
<td>ESC</td>
<td>Main Menu: Exit the BIOS Setup program</td>
</tr>
<tr>
<td></td>
<td>Submenus: Exit current submenu</td>
</tr>
<tr>
<td>F1</td>
<td>Show descriptions of general help</td>
</tr>
<tr>
<td>F3</td>
<td>Restore the previous BIOS settings for the current submenus</td>
</tr>
<tr>
<td>F9</td>
<td>Load the Optimized BIOS default settings for the current submenus</td>
</tr>
<tr>
<td>F10</td>
<td>Save all the changes and exit the BIOS Setup program</td>
</tr>
</tbody>
</table>
- Main
  This setup page includes all the items of the standard compatible BIOS.

- Advanced
  This setup page includes all the items of AMI BIOS special enhanced features.
  (ex: Auto detect fan and temperature status, automatically configure hard disk parameters.)

- AMD CBS
  This setup page includes the common items for configuration of AMD motherboard-related information.

- AMD PBS Option
  This setup page includes the common items for configuration of AMD CPM RAS related settings.

- Chipset
  This setup page includes all the submenu options for configuring the functions of the North Bridge.

- Server Management
  Server additional features enabled/disabled setup menus.

- Security
  Change, set, or disable supervisor and user password. Configuration supervisor password allows you to
  restrict access to the system and BIOS Setup.
  A supervisor password allows you to make changes in BIOS Setup.
  A user password only allows you to view the BIOS settings but not to make changes.

- Boot
  This setup page provides items for configuration of the boot sequence.

- Save & Exit
  Save all the changes made in the BIOS Setup program to the CMOS and exit BIOS Setup. (Pressing
  <F10> can also carry out this task.)
  Abandon all changes and the previous settings remain in effect. Pressing <Y> to the confirmation
  message will exit BIOS Setup. (Pressing <Esc> can also carry out this task.)
5-1 The Main Menu

Once you enter the BIOS Setup program, the Main Menu (as shown below) appears on the screen. Use arrow keys to move among the items and press <Enter> to accept or enter other sub-menu.

Main Menu Help
The on-screen description of a highlighted setup option is displayed on the bottom line of the Main Menu.

Submenu Help
While in a submenu, press <F1> to display a help screen (General Help) of function keys available for the menu. Press <Esc> to exit the help screen. Help for each item is in the Item Help block on the right side of the submenu.

- When the system is not stable as usual, select the Restore Defaults item to set your system to its defaults.
- The BIOS Setup menus described in this chapter are for reference only and may differ by BIOS version.
<table>
<thead>
<tr>
<th>Parameter</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>BIOS Information</td>
<td></td>
</tr>
<tr>
<td>Project Name</td>
<td>Displays the project name information.</td>
</tr>
<tr>
<td>Project Version</td>
<td>Displays version number of the BIOS setup utility.</td>
</tr>
<tr>
<td>Build Date and Time</td>
<td>Displays the date and time when the BIOS setup utility was created.</td>
</tr>
<tr>
<td>BMC Information</td>
<td></td>
</tr>
<tr>
<td>BMC Firmware Version</td>
<td>Displays BMC firmware version information.</td>
</tr>
<tr>
<td>CPU Brand String / CPU Speed / Processor Core / Microcode Patch</td>
<td>Displays the technical information for the installed processor(s).</td>
</tr>
<tr>
<td>Total Memory <em>(Note3)</em></td>
<td>Displays the total memory size of the installed memory.</td>
</tr>
<tr>
<td>Memory Frequency <em>(Note3)</em></td>
<td>Displays the frequency information of the installed memory.</td>
</tr>
<tr>
<td>VR Information Version</td>
<td>Displays VR version information.</td>
</tr>
<tr>
<td>AGESA PI Version PI Version</td>
<td>Displays AGESA PI version information.</td>
</tr>
<tr>
<td>Onboard LAN Information</td>
<td></td>
</tr>
<tr>
<td>LAN1 MAC Address <em>(Note1)</em></td>
<td>Displays LAN MAC address information.</td>
</tr>
<tr>
<td>LAN2 MAC Address <em>(Note2)</em></td>
<td>Displays LAN MAC address information.</td>
</tr>
<tr>
<td>System Date</td>
<td>Sets the date following the weekday-month-day-year format.</td>
</tr>
<tr>
<td>System Time</td>
<td>Sets the system time following the hour-minute-second format.</td>
</tr>
</tbody>
</table>

*(Note1)* The number of LAN ports listed will depend on the motherboard / system model.  
*(Note2)* This section will display capacity and frequency information of the memory that the customer has installed.
5-2 Advanced Menu

The Advanced Menu displays submenu options for configuring the function of various hardware components. Select a submenu item, then press <Enter> to access the related submenu screen.

When Boot Mode Select is set to UEFI (Default)
When "Boot Mode Select" is set to Legacy in the Boot > Boot Mode Select section
## 5-2-1 Trusted Computing

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td><strong>Configuration</strong></td>
<td></td>
</tr>
<tr>
<td>Security Device Support</td>
<td>Enable/Disable BIOS support for security device. OS will not show security device. TCG EFI protocol and INT1A interface will not be available. Options available: Enable/Disable. Default setting is <strong>Enable</strong>.</td>
</tr>
<tr>
<td>SPI TPM Support</td>
<td>Select Enable to activate TPM support feature. Options available: Enabled/Disabled. Default setting is <strong>Disabled</strong>.</td>
</tr>
</tbody>
</table>
### 5-2-2 PSP Firmware Versions

The PSP Firmware Versions page displays the basic PSP firmware version information. Items on this window are non-configurable.

<table>
<thead>
<tr>
<th>PSP Directory Level 1 (Fixed)</th>
<th>FF.19.0.40</th>
</tr>
</thead>
<tbody>
<tr>
<td>PSP Recovery BL Ver</td>
<td>0.45.59.100</td>
</tr>
<tr>
<td>SMU FH Version</td>
<td>10005011</td>
</tr>
<tr>
<td>ABL Version</td>
<td></td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>PSP Directory Level 2 (Updateable)</th>
<th>FF.19.0.40</th>
</tr>
</thead>
<tbody>
<tr>
<td>PSP Bootloader Version</td>
<td>0.45.59.100</td>
</tr>
<tr>
<td>SMU FH Version</td>
<td>10005011</td>
</tr>
<tr>
<td>ABL Version</td>
<td></td>
</tr>
</tbody>
</table>

---

**Keys:**
- `F4`: Select Screen
- `F1`: Select Item
- `Enter`: Select
- `+/-`: Change Opt.
- `F1`: General Help
- `F3`: Previous Values
- `F5`: Default Settings
- `F10`: Save & Exit
- `ESC`: Exit
## 5-2-3 Legacy Video Select

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>OnBrd/Ext VGA Select(Nota)</td>
<td>Selects between onboard or external VGA support. Options available: Auto, Onboard, External. Default setting is <strong>Onboard</strong>.</td>
</tr>
</tbody>
</table>

(Note) This configurable option will be displayed when "Boot Mode Select" is set to **Legacy** in the Boot > Boot Mode Select section.
## AST2500 Super IO Configuration

### Parameter Description

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>AST2500 Super IO Configuration</td>
<td>Displays the super IO chip information</td>
</tr>
<tr>
<td>Super IO Chip</td>
<td>Press [Enter] for configuration of advanced items.</td>
</tr>
</tbody>
</table>

---

**BIOS Setup**  
- 54 -
## 5-2-4-1 Serial Port 1/2 Configuration

<table>
<thead>
<tr>
<th>Serial Port 1 Configuration</th>
</tr>
</thead>
<tbody>
<tr>
<td>Serial Port</td>
</tr>
<tr>
<td>Device Settings</td>
</tr>
<tr>
<td>Change Settings</td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>Serial Port 2 Configuration</th>
</tr>
</thead>
<tbody>
<tr>
<td>Serial Port</td>
</tr>
<tr>
<td>Device Settings</td>
</tr>
<tr>
<td>Change Settings</td>
</tr>
<tr>
<td>Parameter</td>
</tr>
<tr>
<td>-----------</td>
</tr>
<tr>
<td>Serial Port Configuration</td>
</tr>
<tr>
<td>Serial Port (Note1)</td>
</tr>
<tr>
<td>Devices Settings (Note2)</td>
</tr>
<tr>
<td>Change Settings (Note2)</td>
</tr>
</tbody>
</table>

(Note1) Advanced items prompt when this item is defined.

(Note2) This item appears when Serial Port is set to Enabled.
5-2-5  S5 RTC Wake Settings

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>Wake System from S5 (Note1)</td>
<td>Enable/Disable system wake on alarm event. Options available: Disabled/Fixed Time. When Fixed Time enabled, system will wake on the hr:mm:ss specified. Default setting is <strong>Disabled</strong>.</td>
</tr>
</tbody>
</table>

(Note1) Advanced items prompt when this item is defined.
## 5-2-6 Serial Port Console Redirection

<table>
<thead>
<tr>
<th>COM1/Serial Over LAN (Note)</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>Select whether to enable console redirection for specified device. Console redirection enables the users to manage the system from a remote location. Options available: Enabled/Disabled. Default setting is <strong>Disabled</strong>.</td>
<td></td>
</tr>
</tbody>
</table>

**COM1/Serial Over LAN Console Redirection Settings**

Press [Enter] to configure advanced items. **Please note that this item is configurable when COM1/Serial Over LAN & COM2 Console Redirection is set to Enabled.**

- **Terminal Type**
  - Selects a terminal type to be used for console redirection.
  - Options available: VT100, VT100+, ANSI, VT-UTF8. Default setting is **ANSI**.

- **Bits per second**
  - Selects the transfer rate for console redirection.
  - Options available: 9600, 19200, 38400, 57600, 115200. Default setting is **115200**.

- **Data Bits**
  - Selects the number of data bits used for console redirection.
  - Options available: 7/8. Default setting is **8**.

(Note) Advanced items prompt when this item is defined.

BIOS Setup - AMI
### COM1/Serial Over LAN Console Redirection Settings

(continued)

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>Parity</td>
<td>A parity bit can be sent with the data bits to detect some transmission errors.</td>
</tr>
<tr>
<td></td>
<td>• Even: parity bit is 0 if the number of 1’s in the data bits is even.</td>
</tr>
<tr>
<td></td>
<td>• Odd: parity bit is 0 if the number of 1’s in the data bits is odd.</td>
</tr>
<tr>
<td></td>
<td>• Mark: parity bit is always 1. Space: Parity bit is always 0.</td>
</tr>
<tr>
<td></td>
<td>• Mark and Space Parity do not allow for error detection.</td>
</tr>
<tr>
<td></td>
<td>• Options available: None, Even, Odd, Mark, Space. Default setting is <strong>None</strong>.</td>
</tr>
<tr>
<td>Stop Bits</td>
<td>Stop bits indicate the end of a serial data packet. (A start bit indicates the beginning). The standard setting is 1 stop bit. Communication with slow devices may require more than 1 stop bit.</td>
</tr>
<tr>
<td></td>
<td>• Options available: 1/2. Default setting is <strong>1</strong>.</td>
</tr>
<tr>
<td>Flow Control</td>
<td>Flow control can prevent data loss from buffer overflow. When sending data, if the receiving buffers are full, a ‘stop’ signal can be sent to stop the data flow. Once the buffers are empty, a ‘start’ signal can be sent to re-start the flow. Hardware flow control uses two wires to send start/stop signals.</td>
</tr>
<tr>
<td></td>
<td>• Options available: None, Hardware RTS/CTS. Default setting is <strong>None</strong>.</td>
</tr>
<tr>
<td>VT-UTF8 Combo Key Support</td>
<td>Enable/Disable the VT-UTF8 Combo Key Support.</td>
</tr>
<tr>
<td></td>
<td>• Options available: Enabled/Disabled. Default setting is <strong>Enabled</strong>.</td>
</tr>
<tr>
<td>Recorder Mode(Note)</td>
<td>When this mode enabled, only texts will be send. This is to capture Terminal data.</td>
</tr>
<tr>
<td></td>
<td>• Options available: Enabled/Disabled. Default setting is <strong>Disabled</strong>.</td>
</tr>
<tr>
<td>Resolution 100x31(Note)</td>
<td>Enable/Disable extended terminal resolution.</td>
</tr>
<tr>
<td></td>
<td>• Options available: Enabled/Disabled. Default setting is <strong>Enabled</strong>.</td>
</tr>
<tr>
<td>Putty KeyPad(Note)</td>
<td>Selects FunctionKey and KeyPad on Putty.</td>
</tr>
<tr>
<td></td>
<td>• Options available: VT100, LINUX, XTERMR6, SC0, ESCN, VT400. Default setting is <strong>VT100</strong>.</td>
</tr>
</tbody>
</table>

(Note) Advanced items prompt when this item is defined.
<table>
<thead>
<tr>
<th>Parameter</th>
<th>Description</th>
</tr>
</thead>
</table>
| Legacy Console Redirection | Press [Enter] to configure advanced items.  
  - Redirection COM Port  
    - Selects a COM port for Legacy serial redirection.  
    - Options available: COM1/Serial Over LAN, COM2. Default setting is **COM1/Serial Over LAN**.  
  - Resolution  
    - Selects the number of rows and columns used in Console Redirection for legacy OS support.  
    - Options available: 80x24, 80x25. Default setting is **80x24**.  
  - Redirect After POST  
    - When Bootloader is selected, then Legacy Console Redirection is disabled before booting to legacy OS. When Always Enable is selected, then Legacy Console Redirection is enabled for legacy OS.  
    - Options available: Always Enable, BootLoader. Default setting is **Always Enable**. |
| Serial Port for Out-of-Band Management / Windows Emergency Management Services (EMS) Console Redirection | EMS console redirection allows the user to configure Console Redirection Settings to support Out-of-Band Serial Port management.  
  Options available: Enabled/Disabled. Default setting is **Disabled**. |
| Serial Port for Out-of-Band EMS Console Redirection Settings | Press [Enter] to configure advanced items.  
  **Please note that this item is configurable when Serial Port for Out-of-Band Management EMS Console Redirection is set to Enabled.**  
  - Out-of-Band Mgmt Port  
    - Microsoft Windows Emerency Management Service (EMS) allows for remote management of a Windows Server OS through a serial port.  
    - Options available: COM1/Serial Over LAN, COM2. Default setting is **COM1/Serial Over LAN**.  
  - Terminal Type  
    - Selects a terminal type to be used for console redirection.  
    - Options available: VT100, VT100+, ANSI, VT-UTF8. Default setting is **ANSI**.  
  - Bits per second  
    - Selects the transfer rate for console redirection.  
    - Options available: 9600, 19200, 38400, 57600, 115200. Default setting is **115200**. |

(Note) Advanced items prompt when this item is defined.
<table>
<thead>
<tr>
<th>Parameter</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>Flow Control</td>
<td>Flow control can prevent data loss from buffer overflow. When sending data, if the receiving buffers are full, a 'stop' signal can be sent to stop the data flow. Once the buffers are empty, a 'start' signal can be sent to re-start the flow. Hardware flow control uses two wires to send start/stop signals.</td>
</tr>
<tr>
<td>Options available: None, Hardware RTS/CTS, Software Xon/Xoff. Default setting is None.</td>
<td></td>
</tr>
</tbody>
</table>
## 5-2-7 CPU Configuration

### Parameter Description

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>SVM Mode</td>
<td>Enable/Disable the CPU Virtualization. Options available: Enabled/Disabled. Default setting is <strong>Enabled</strong>.</td>
</tr>
<tr>
<td>SMEE</td>
<td>Controls the Secure Memory Encryption Enable (SMEE) function. Options available: Enabled/Disabled. Default setting is <strong>Enabled</strong>.</td>
</tr>
<tr>
<td>CPU 0/1 Information</td>
<td>Press [Enter] to view more information related to CPU.</td>
</tr>
</tbody>
</table>
### 5-2-8 PCI Subsystem Settings

<table>
<thead>
<tr>
<th>PCI Bus Driver Version</th>
<th>5.01.29</th>
</tr>
</thead>
</table>

**PCI Express Lanes**

- **PCIE_1 Lanes**
  - Lanes: [Auto]
  - I/O ROM: Enabled
  - Link speed: [Auto]

- **PCIE_2 Lanes**
  - Lanes: [Auto]
  - I/O ROM: Enabled
  - Link speed: [Auto]

- **PCIE_3 Lanes**
  - Lanes: [Auto]
  - I/O ROM: Enabled
  - Link speed: [Auto]

- **PCIE_4 Lanes**
  - Lanes: [Auto]
  - I/O ROM: Enabled
  - Link speed: [Auto]

- **GENZ_B Lanes**
  - Lanes: [Auto]
  - I/O ROM: Enabled

- **GENZ_B Link speed**

---

**Advanced Options**

- **PCIE_3 I/O ROM**
  - [Enabled]

- **PCIE_3 Link speed**
  - [Auto]

- **PCIE_4 Lanes**
  - Lanes: [Auto]
  - I/O ROM: Enabled
  - Link speed: [Auto]

- **GENZ_B Lanes**
  - Lanes: [Auto]
  - I/O ROM: Enabled

- **GENZ_B Link speed**

- **GENZ_F Lanes**
  - Lanes: [Auto]
  - I/O ROM: Enabled

- **GENZ_F Link speed**

- **Onboard LAN Controller**
  - [Enabled]

- **Onboard LPM1 I/O ROM**
  - [Enabled]

- **Onboard LPM2 I/O ROM**
  - [Enabled]

- **PCI Device Common Settings**
  - [Enabled]

- **SR-IOV Decoding**
  - [Enabled]

- **SR-IOV Support**
  - [Enabled]
<table>
<thead>
<tr>
<th>Parameter</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td><strong>PCI Bus Driver Version</strong></td>
<td>Displays the PCI Bus Driver version information.</td>
</tr>
<tr>
<td><strong>PCIE_# Lanes Configuration</strong></td>
<td>Change the PCIe lanes.</td>
</tr>
<tr>
<td><strong>GENZ_B# Lanes Configuration</strong></td>
<td>Options available: Disabled, Auto, x16, x8x8, x8x4x4, x4x4x8, x4x4x4x4. Default setting is <strong>Auto</strong>.</td>
</tr>
<tr>
<td><strong>GENZ_F# Lanes Configuration</strong></td>
<td></td>
</tr>
<tr>
<td><strong>PCIE_# I/O ROM</strong></td>
<td>When enabled, this setting will initialize the device expansion ROM for the related PCI-E slot.</td>
</tr>
<tr>
<td><strong>GENZ_B# I/O ROM</strong></td>
<td>Options available: Enabled/Disabled. Default setting is <strong>Enabled</strong>.</td>
</tr>
<tr>
<td><strong>GENZ_F# I/O ROM</strong></td>
<td></td>
</tr>
<tr>
<td><strong>PCIE_# Link Speed</strong></td>
<td>Configure mezzanine PCIe max link speed.</td>
</tr>
<tr>
<td><strong>GENZ_B# Link Speed</strong></td>
<td>Options available: Auto/Maximum/Gen1/Gen2/Gen3. Default setting is <strong>Auto</strong>.</td>
</tr>
<tr>
<td><strong>GENZ_F# Link Speed</strong></td>
<td></td>
</tr>
<tr>
<td><strong>Onboard LAN Controller</strong></td>
<td>Enable/Disable the onboard LAN devices.</td>
</tr>
<tr>
<td><strong>Onboard LAN I/O ROM</strong></td>
<td>Enable/Disable the onboard LAN devices, and initializes device expansion ROM.</td>
</tr>
<tr>
<td><strong>PCI Devices Common Settings</strong></td>
<td></td>
</tr>
<tr>
<td><strong>Above 4G Decoding</strong></td>
<td>Enable/Disable memory mapped I/O to 4GB or greater address space (Above 4G Decoding).</td>
</tr>
<tr>
<td><strong>SR-IOV Support</strong></td>
<td>If the system has SR-IOV capable PCIe devices, this item Enable/Disable Single Root IO Virtualization Support.</td>
</tr>
</tbody>
</table>

(Note1) This section is dependent on the available PCIe Slot.
(Note2) This section is dependent on the available LAN controller.
### USB Configuration

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>USB Module Version</td>
<td>Displays the USB version.</td>
</tr>
<tr>
<td>USB Controllers</td>
<td>Displays the supported USB controllers.</td>
</tr>
<tr>
<td>USB Devices</td>
<td>Displays the USB devices connected to the system.</td>
</tr>
<tr>
<td>Legacy USB Support</td>
<td>Enable/disable the Legacy USB support function. AUTO option disables legacy support if no USB devices are connected. DISABLE option will keep USB devices available only for EFI applications. Options available: Auto/Enabled/Disabled. Default setting is <strong>Enabled</strong>.</td>
</tr>
<tr>
<td>XHCI Hand-off</td>
<td>Enable/Disable the XHCI (USB 3.0) Hand-off support. Options available: Enabled/Disabled. Default setting is <strong>Enabled</strong>.</td>
</tr>
<tr>
<td>USB Mass Storage Driver Support (Note)</td>
<td>Enable/Disable the USB Mass Storage Driver Support. Options available: Enabled/Disabled. Default setting is <strong>Enabled</strong>.</td>
</tr>
<tr>
<td>USB hardware delays and time-outs</td>
<td></td>
</tr>
<tr>
<td>USB transfer time-out</td>
<td>The time-out value for Control, Bulk, and Interrupt transfers. Options available: 1 sec/5 sec/10 sec/20 sec. Default setting is <strong>20 sec</strong>.</td>
</tr>
<tr>
<td>USB resetr time-out</td>
<td>Options available: 1 sec/5 sec/10 sec/20 sec. Default setting is <strong>20 sec</strong>.</td>
</tr>
</tbody>
</table>

(Note) This item is present only if you attach USB devices.
<table>
<thead>
<tr>
<th>Parameter</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>Device reset time-out</td>
<td>USB mass storage device Start Unit command time-out. Options available: 10 sec/20 sec/30 sec/40 sec. Default setting is <strong>20 sec</strong>.</td>
</tr>
<tr>
<td>Device power-up delay</td>
<td>Maximum time the device will take before it properly reports itself to the Host Controller. &quot;Auto&quot; uses default value: for a Root port it is 100 ms, for a Hub port the delay is taken from Hub descriptor. Options available: Auto/Manual. Default setting is <strong>Auto</strong>.</td>
</tr>
<tr>
<td>Mass Storage Devices</td>
<td>Mass storage device emulation type. AUTO enumerates devices according to their media format. Optical drives are emulated as CDROM, drives with no media will be emulated according to a drive type. Options available: Auto/Floppy/Forced FDD/Hard Disk/CD-ROM. Default setting is <strong>Auto</strong>.</td>
</tr>
</tbody>
</table>

AMI Virtual CDROM0 1.00 / AMI HDisk0 1.00 / Generic Flash Disk 8.07 / ADATA USB Flash Drive 1100
## Network Stack Configuration

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>Network Stack</td>
<td>Enable/Disable the UEFI network stack. Options available: Enabled/Disabled. Default setting is <strong>Enabled</strong>.</td>
</tr>
<tr>
<td>Ipv4 PXE Support</td>
<td>Enable/Disable the Ipv4 PXE feature. Options available: Enabled/Disabled. Default setting is <strong>Enabled</strong>.</td>
</tr>
<tr>
<td>Ipv4 HTTP Support</td>
<td>Enable/Disable the Ipv4 HTTP feature. Options available: Enabled/Disabled. Default setting is <strong>Enabled</strong>.</td>
</tr>
<tr>
<td>Ipv6 PXE Support</td>
<td>Enable/Disable the Ipv6 PXE feature. Options available: Enabled/Disabled. Default setting is <strong>Enabled</strong>.</td>
</tr>
<tr>
<td>Ipv6 HTTP Support</td>
<td>Enable/Disable the Ipv6 HTTP feature. Options available: Enabled/Disabled. Default setting is <strong>Enabled</strong>.</td>
</tr>
<tr>
<td>IPSEC Certificate</td>
<td>Enable/Disable the IPSEC Certificate feature. Options available: Enabled/Disabled. Default setting is <strong>Enabled</strong>.</td>
</tr>
<tr>
<td>PXE boot wait time</td>
<td>Wait time in seconds to press ESC key to abort the PXE boot. Press the &lt;-&gt; keys to increase or decrease the desired values.</td>
</tr>
<tr>
<td>Media detect count</td>
<td>Number of times the presence of media will be checked. Press the &lt;-&gt; keys to increase or decrease the desired values.</td>
</tr>
</tbody>
</table>
5-2-11 NVMe Configuration

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>NVMe Configuration</td>
<td>Displays the NVMe devices connected to the system</td>
</tr>
</tbody>
</table>
## 5-2-12 SATA Configuration

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>SATA Configuration</td>
<td>Displays the installed HDD devices information. System will automatically detect HDD type.</td>
</tr>
</tbody>
</table>
## 5-2-13 UEFI POST LOGO Configuration

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>UEFI Configuration</td>
<td>Select output device. Options available: First loaded Device, Onboard Device, External Device, Specific Device. Default setting is Onboard Device.</td>
</tr>
</tbody>
</table>
## 5-2-14 AMI Graphic Output Protocol Policy

### Parameter Description

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>CPU0/1</td>
<td>Press [Enter] to view the memory configuration status related to CPU 0/1.</td>
</tr>
</tbody>
</table>

**Parameter List**:

- **CPU 0**
  - Mbist Test Enable: Disabled, 0xC000
  - Mbist Aggressor Enable: Disabled, 0xC000
  - Mbist Per Bit Slave Die Report: 0x0000, 0x0000
  - Dram Temp Controlled Refresh: Disabled, 0xC000
  - Enable User Timing Mode: Disabled, 0xC018
  - User Timing Value: Disabled, 0xC018
  - Mem Bus Freq Limit: Disabled, 0xC018
  - Enable Power Down: Disabled, 0xC000
  - Dram Double Refresh Rate: Disabled, 0xC000
  - Fmu Train Mode: 0xC002, 0xC000
  - Ecc Symbol Size: 0xC002, 0xC000
  - Uncorrectable Ecc Retry: Enabled, 0xC000
  - Ignore Spd Checksum: Enabled, 0xC000
  - Enable Bank Group Swap Alt: Enabled, 0xC000
  - Enable Bank Group Swap: Disabled, 0xC01A
  - Ddr Route Balanced Tee: Disabled, 0xC000
  - Nudimm Power Source: 0x0001, 0xC000
  - Ddts Cmd Throt Enable: Disabled, 0xC004
  - Ddts Cmd Throt Cycle: Disabled, 0xC004

**Socket-specific memory configuration status**

- [F1]: General Help
- [F3]: Previous Values
- [F4]: Optimized Defaults
- [ESC]: Exit

- [Enter]: Select
- [↑/↓]: Select Item
### 5-2-15 TLS Auth Configuration

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>Server CA Configuration</td>
<td>Press [Enter] for configuration of advanced items.</td>
</tr>
<tr>
<td></td>
<td>• Enroll Cert</td>
</tr>
<tr>
<td></td>
<td>– Press [Enter] to enroll a certificate</td>
</tr>
<tr>
<td></td>
<td>• Enroll Cert Using File</td>
</tr>
<tr>
<td></td>
<td>• Cert GUID</td>
</tr>
<tr>
<td></td>
<td>Input digit character in 1111111-2222-3333-4444-1234567890ab format.</td>
</tr>
<tr>
<td></td>
<td>– Commit Changes and Exit</td>
</tr>
<tr>
<td></td>
<td>– Discard Changes and Exit</td>
</tr>
<tr>
<td></td>
<td>• Delete Cert</td>
</tr>
<tr>
<td>Client Cert Configuration</td>
<td>Press [Enter] for configuration of advanced items.</td>
</tr>
</tbody>
</table>
5-2-16 iSCSI Configuration

**Parameter** | **Description**
--- | ---
iSCSI Initiator Name | Press [Enter] and name iSCSI Initiator. Only IQN format is accepted. Range: from 4 to 223
Add an Attempt | Press [Enter] to configure advanced items.
Delete Attempts | Press [Enter] to configure advanced items.
Change Attempt Order | Press [Enter] to configure advanced items.
5-2-17 Intel(R) Ethernet Controller X550

- BIOS Setup
- 74 -

---

**Advanced**

**Firmware Image Properties**

- **Blink LEDs**: 0
- **UEFI Driver**: Intel(R) 10GbE Driver 7.8.13 x64
- **Adapter PBA**: 0000000=000
- **Device Name**: Intel(R) Ethernet Controller X550
- **Chip Type**: Intel X550
- **PCI Device ID**: 1563
- **PCI Address**: 21:00:00
- **Link Status**: [Disconnected]
- **MAC Address**: 15:10:00:00:000:00000
- **Virtual MAC Address**: 00:00:00:00:00:00

**View device firmware version information.**

---

**Advanced**

**Aptio Setup - AMI**

- **Link Speed**: [Auto Negotiated]
- **Wake On LAN**: [Enabling]

**Enables power on of the system via LAN. Note that configuring Wake on LAN in the operating system does not change the value of this setting, but does override the behavior of Wake on LAN in OS controlled power states.**

---

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<table>
<thead>
<tr>
<th>Parameter</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>Firmware Image Properties</td>
<td>Press [Enter] to configure advanced items.</td>
</tr>
<tr>
<td></td>
<td></td>
</tr>
<tr>
<td>NIC Configuration</td>
<td>Press [Enter] to configure advanced items.</td>
</tr>
<tr>
<td></td>
<td>- Link Speed</td>
</tr>
<tr>
<td></td>
<td>– Allows for automatic link speed adjustment.</td>
</tr>
<tr>
<td></td>
<td>– Options available: Auto Negotiated, 10 Mbps Half, 10 Mbps Full,</td>
</tr>
<tr>
<td></td>
<td>100 Mbps Half, 100 Mbps Full. Default setting is <strong>Auto Negotiated</strong>.</td>
</tr>
<tr>
<td></td>
<td>- Wake On LAN</td>
</tr>
<tr>
<td></td>
<td>– Enables power on of the system via LAN. Note that configuring</td>
</tr>
<tr>
<td></td>
<td>Wake on LAN in the operating system does not change the value of</td>
</tr>
<tr>
<td></td>
<td>this setting, but does override the behavior of Wake on LAN in OS</td>
</tr>
<tr>
<td></td>
<td>controlled power states.</td>
</tr>
<tr>
<td></td>
<td>– Options available: Enabled/Disabled. Default setting is <strong>Enabled</strong>.</td>
</tr>
<tr>
<td>Blink LEDs</td>
<td>Identifies the physical network port by blinking the associated LED.</td>
</tr>
<tr>
<td></td>
<td>Press the numeric keys to adjust desired values.</td>
</tr>
<tr>
<td>UEFI Driver</td>
<td>Displays the technical specifications for the Network Interface Controller.</td>
</tr>
<tr>
<td>Adapter PBA</td>
<td>Displays the technical specifications for the Network Interface Controller.</td>
</tr>
<tr>
<td>Device Name</td>
<td>Displays the technical specifications for the Network Interface Controller.</td>
</tr>
<tr>
<td>Chip Type</td>
<td>Displays the technical specifications for the Network Interface Controller.</td>
</tr>
<tr>
<td>PCI Device ID</td>
<td>Displays the technical specifications for the Network Interface Controller.</td>
</tr>
<tr>
<td>PCI Address</td>
<td>Displays the technical specifications for the Network Interface Controller.</td>
</tr>
<tr>
<td>Link Status</td>
<td>Displays the technical specifications for the Network Interface Controller.</td>
</tr>
<tr>
<td>MAC Address</td>
<td>Displays the technical specifications for the Network Interface Controller.</td>
</tr>
<tr>
<td>Virtual MAC Address</td>
<td>Displays the technical specifications for the Network Interface Controller.</td>
</tr>
</tbody>
</table>
5-2-18 VLAN Configuration

Parameter | Description
--- | ---
Enter Configuration Menu | Press [Enter] to configure advanced items.
- Create new VLAN
- VLAN ID
  - Sets VLAN ID for a new VLAN or an existing VLAN.
  - Press the <--> / <-> keys to increase or decrease the desired values.
  - The valid range is from 0 to 4094.
- Priority
  - Sets 802.1Q Priority for a new VLAN or an existing VLAN.
  - Press the <--> / <-> keys to increase or decrease the desired values.
  - The valid range is from 0 to 7.
- Add VLAN
  - Press [Enter] to create a new VLAN or update an existing VLAN.
- Configured VLAN List
- Remove VLAN
  - Press [Enter] to remove an existing VLAN.
**5-2-19 MAC IPv4 Network Configuration**

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>Configured</td>
<td>Indicates whether network address is configured successfully or not. Options available: Enabled/Disabled. Default setting is <strong>Disabled</strong>.</td>
</tr>
<tr>
<td><strong>Enable DHCP(Note)</strong></td>
<td>Options available: Enabled/Disabled. Default setting is <strong>Enabled</strong>.</td>
</tr>
<tr>
<td>Local IP Address(Note)</td>
<td>Press [Enter] to configure local IP address.</td>
</tr>
<tr>
<td>Local NetMask(Note)</td>
<td>Press [Enter] to configure local NetMask.</td>
</tr>
<tr>
<td>Local Gateway(Note)</td>
<td>Press [Enter] to configure local Gateway</td>
</tr>
<tr>
<td>Local DNS Servers(Note)</td>
<td>Press [Enter] to configure local DNS servers</td>
</tr>
<tr>
<td>Save Changes and Exit</td>
<td>Press [Enter] to save all configurations.</td>
</tr>
</tbody>
</table>

(Note) This item appears when **Configured** is set to **Enabled**.
### 5-2-20 MAC IPv6 Network Configuration

**Advanced**

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>Interface Name :</td>
<td>eth0</td>
</tr>
<tr>
<td>Interface Type :</td>
<td>Ethernet</td>
</tr>
<tr>
<td>MAC address :</td>
<td>18-0D-40-05-3B-C7</td>
</tr>
<tr>
<td>Host addresses :</td>
<td></td>
</tr>
<tr>
<td>Route Table :</td>
<td>FEB0:1/64 &gt;&gt;1</td>
</tr>
<tr>
<td>Gateway addresses :</td>
<td></td>
</tr>
<tr>
<td>DNS addresses :</td>
<td></td>
</tr>
<tr>
<td>Interface ID</td>
<td>16:00:4D:FF:FE:53B:C7</td>
</tr>
<tr>
<td>DAD Transmit Count</td>
<td>1</td>
</tr>
<tr>
<td>Policy</td>
<td>automatic</td>
</tr>
</tbody>
</table>

**Enter Configuration Menu**

Press [Enter] to configure advanced items.

- Displays the MAC Address information.
- Interface ID
  - The 64 bit alternative interface ID for the device. The string is colon separated. e.g. ff:dd:88:66:cc:1:2:3.
- DAD Transmit Count
  - The number of consecutive Neighbor solicitation messages sent while performing Duplicate Address Detection on a tentative address. A value of zero indicates that Duplicate Address Detection is not performed.
- Policy
  - Options available: automatic/manual. Default setting is **automatic**.
- Save Changes and Exit
  - Press [Enter] to save all configurations.
5-3 AMD CBS Menu

AMD CBS menu displays submenu options for configuring the CPU-related information that the BIOS automatically sets. Select a submenu item, then press [Enter] to access the related submenu screen.

<table>
<thead>
<tr>
<th>AMD CBS</th>
<th>CPU Common Options</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td></td>
</tr>
</tbody>
</table>

++: Select Screen
++: Select Item
Enter: Select
+-/: Change Out.
F1: General Help
F3: Previous Values
F9: Optimized Defaults
F10: Save & Exit
ESC: Exit

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## 5-3-1 CPU Common Options

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>Performance</td>
<td>Press [Enter] for configuration of advanced items.</td>
</tr>
<tr>
<td>Prefetcher settings</td>
<td>Press [Enter] for configuration of advanced items.</td>
</tr>
<tr>
<td>Core Watchdog</td>
<td>Press [Enter] for configuration of advanced items.</td>
</tr>
<tr>
<td>RedirectForReturnDis</td>
<td>From a workaround for GCC/C000005 issue for XV Core on CZ A0, setting MSRC001_1029 Decode Configuration (DE_CFG) bit 14 [DecfgNoRdrctForReturns] to 1. Options available: Auto, 1, 0. Default setting is Auto.</td>
</tr>
<tr>
<td>Platform First Error Handling</td>
<td>Enable/Disable PFEH, cloak individual banks, and mask deferred error interrupts from each bank. Options available: Auto, Enabled, Disabled. Default setting is Auto.</td>
</tr>
<tr>
<td>Core Performance Boost</td>
<td>Enable/Disable the Core Performance Boost function. Options available: Auto/Disabled. Default setting is Auto.</td>
</tr>
<tr>
<td>Global C-State Control</td>
<td>Controls the IO based C-state generation and DF C-states. Options available: Auto, Enabled, Disabled. Default setting is Auto.</td>
</tr>
<tr>
<td>SEV ASID Count</td>
<td>Specifies the maximum valid ASID, which affects the maximum system physical address space. Options available: Auto, 253 ASIDs, 509 ASIDs. Default setting is Auto.</td>
</tr>
<tr>
<td>Parameter</td>
<td>Description</td>
</tr>
<tr>
<td>---------------------------</td>
<td>-----------------------------------------------------------------------------</td>
</tr>
<tr>
<td>SEV-ES ASID Space Limit</td>
<td>Space limit control for SEV-ES ASIDs. Options available: Auto/Manual. Default setting is <strong>Auto</strong>.</td>
</tr>
<tr>
<td>Control</td>
<td></td>
</tr>
<tr>
<td>Streaming Stores Control</td>
<td>Enable/Disable the Streaming Stores functionality. Options available: Auto, Enabled, Disabled. Default setting is <strong>Auto</strong>.</td>
</tr>
<tr>
<td>Local APIC Mode</td>
<td>Sets the Local APIC Mode. Options available: Auto, xAPIC, x2APIC. Default setting is <strong>Auto</strong>.</td>
</tr>
<tr>
<td>ACPI_CST C1 Decaration</td>
<td>Determines whether or not to declare the C1 state to the OS. Options available: Auto, Enabled, Disabled. Default setting is <strong>Auto</strong>.</td>
</tr>
<tr>
<td>MCA error thresh enable</td>
<td>Enable MCA error thresholding. Options available: Auto, False, True. Default setting is <strong>Auto</strong>.</td>
</tr>
<tr>
<td>SMU and PSP Debug Mode</td>
<td>When this option is enabled, specific uncorrected errors detected by the PSP FW or SMU FW will hand and not reset the system. Options available: Auto, Enabled, Disabled. Default setting is <strong>Auto</strong>.</td>
</tr>
<tr>
<td>Xtrig7 Workaround</td>
<td>Options available: Auto, No Workaround, Bronze Workaround, Sliver Workaround. Default setting is <strong>Auto</strong>.</td>
</tr>
<tr>
<td>PPIN Opt-in</td>
<td>Enable/Disable the PPIN feature. Options available: Auto, Enabled, Disabled. Default setting is <strong>Auto</strong>.</td>
</tr>
<tr>
<td>SNP Memory (RMP Table) Coverage</td>
<td>Enabled: Enter system memory is covered. Options available: Auto, Enabled, Disabled, Custom. Default setting is <strong>Auto</strong>.</td>
</tr>
<tr>
<td>SMEE</td>
<td>Control secure memory encryption enable. Options available: Auto, Enabled, Disabled. Default setting is <strong>Auto</strong>.</td>
</tr>
<tr>
<td>Action on BIST Failure</td>
<td>Action to take when a CCD BIST failure is detected. Options available: Auto, Do nothing, Down-CCD. Default setting is <strong>Auto</strong>.</td>
</tr>
<tr>
<td>Fast Short REP MOVSB</td>
<td>Default is 1, cab be set to zero for analysis purpose as long as OS supports it. Options available: Enabled, Disabled. Default setting is <strong>Enabled</strong>.</td>
</tr>
<tr>
<td>Enhanced REP MOVSB/STOSB</td>
<td>Default is 1, cab be set to zero for analysis purpose as long as OS supports it. Options available: Enabled, Disabled. Default setting is <strong>Enabled</strong>.</td>
</tr>
<tr>
<td>REP-MOV/STOS Steaming</td>
<td>Allows REP-MOV/STOS to use non-caching streaming stores for large sizes. Options available: Enabled, Disabled. Default setting is <strong>Enabled</strong>.</td>
</tr>
<tr>
<td>X3D</td>
<td>Override of X3D technology. Options available: Auto, Disable, 1 stack, 2 stack, 4 stack. Default setting is <strong>Auto</strong>.</td>
</tr>
<tr>
<td>IBS hardware work around</td>
<td>Set if using IBS execution sampling without software workaround for erratum 1,285. May impac performance. Options available: Enabled, Auto. Default setting is <strong>Enabled</strong>.</td>
</tr>
</tbody>
</table>
## 5-3-1-1 Performance

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>Performance</td>
<td>Option Available: Normal Operation, Customized</td>
</tr>
<tr>
<td>OC Mode (Note1)</td>
<td>Default setting is <strong>Normal Operation</strong>.</td>
</tr>
<tr>
<td>Custom Core Pstates</td>
<td>Allows you to accept or decline enabling Custom Core Pstates. When accepted, you can disable or customize core pstates.</td>
</tr>
<tr>
<td>CCD/Core/Thread Enablement</td>
<td>Allows you to accept or decline enabling CCDs, processor cores and threads. When accepted, you can control the number of CCDs to be used, the number of cores to be used, and whether to enable or disable Simultaneous Multithreading Technology (SMT) support.</td>
</tr>
<tr>
<td>SMT Control</td>
<td>Can be used to disable symmetric multithreading. To re-enable SMT, a POwer CYCLE is needed after select the 'Enable' option. Select 'Auto' base on BIOS PCD. (PcdAmdSmtMode) default setting. Option Available: Enable, Disable. Default setting is <strong>Disable</strong>.</td>
</tr>
</tbody>
</table>

(Note1) Advanced items are configurable when this item is defined.
## Prefetcher Settings

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td><strong>Prefetcher settings</strong></td>
<td></td>
</tr>
<tr>
<td>L1 Stream HW Prefetcher</td>
<td>Enable/Disable L1 Stream HW Prefetcher. Options available: Auto, Enable, Disabled. Default setting is <strong>Enable</strong>.</td>
</tr>
<tr>
<td>L1 Stride Prefetcher</td>
<td>Use memory access history of individual instruction to fetch additional lines Enable/Disable L1 Stream HW Prefetcher. Options available: Auto, Enable, Disable. Default setting is <strong>Auto</strong>.</td>
</tr>
<tr>
<td>L1 Region Prefetcher</td>
<td>Use memory access history to fetch additional lines when the data access for a given instruction tends to be followed by other data accesses. Options available: Auto, Enable, Disable. Default setting is <strong>Enable</strong>.</td>
</tr>
<tr>
<td>L2 Stream HW Prefetcher</td>
<td>Enable/Disable L2 Stream HW Prefetcher. Options available: Auto, Enable, Disabled. Default setting is <strong>Enable</strong>.</td>
</tr>
<tr>
<td>L2 Up/Down Prefetcher</td>
<td>Use memory access history to determine whether to fetch the next or previous line for all memory accesses. Options available: Auto, Enable, Disable. Default setting is <strong>Auto</strong>.</td>
</tr>
</tbody>
</table>
## 5-3-1-3 Core Watchdog

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>Core Watchdog</td>
<td></td>
</tr>
<tr>
<td>Core Watchdog Timer Enable</td>
<td>Enable/Disable CPU Watchdog Timer. Options available: Auto, Enabled, Disabled. Default setting is <strong>Auto</strong>.</td>
</tr>
</tbody>
</table>

**Parameter Description**

- **Core Watchdog Timer Enable**: Enable or disable CPU Watchdog Timer.
### 5-3-2 DF Common Options

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td><strong>Scrubber</strong></td>
<td>Press [Enter] for configuration of advanced items.</td>
</tr>
<tr>
<td><strong>Memory Addressing</strong></td>
<td>Press [Enter] for configuration of advanced items.</td>
</tr>
<tr>
<td><strong>ACPI</strong></td>
<td>Press [Enter] for configuration of advanced items.</td>
</tr>
<tr>
<td><strong>Link</strong></td>
<td>Press [Enter] for configuration of advanced items.</td>
</tr>
<tr>
<td><strong>Disable DF to external IP sync flood propagation</strong></td>
<td>Enable/Disable SyncFlood to UMC &amp; downstream slaves. Options available: Auto, Sync flood disabled, Sync flood enabled. Default setting is <strong>Auto</strong>.</td>
</tr>
<tr>
<td><strong>Disable DF sync flood propagation</strong></td>
<td>Enable/Disable DF Sync Flood propagation. Options available: Auto, Sync flood disabled, Sync flood enabled. Default setting is <strong>Auto</strong>.</td>
</tr>
<tr>
<td><strong>Freeze DF module queues on error</strong></td>
<td>Options available: Auto, Enabled, Disabled. Default setting is <strong>Auto</strong>.</td>
</tr>
<tr>
<td><strong>CC6 memory region encryption</strong></td>
<td>Controls whether or not the CC6 save/restor memory is encrypted. Options available: Auto, Enabled, Disabled. Default setting is <strong>Auto</strong>.</td>
</tr>
<tr>
<td><strong>System probe filter</strong></td>
<td>Enable/Disable System probe filter. Options available: Auto, Enabled, Disabled. Default setting is <strong>Auto</strong>.</td>
</tr>
<tr>
<td><strong>Memory Clear</strong></td>
<td>Enable/Disable the Memory Clear feature. Options available: Auto, Enabled, Disabled. Default setting is <strong>Auto</strong>.</td>
</tr>
<tr>
<td><strong>PSP error injection support</strong></td>
<td>Enable/Disable PSP error injection support. Options available: False/True. Default setting is <strong>False</strong>.</td>
</tr>
</tbody>
</table>
### 5-3-2-1 Scrubber

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>Scrubber</td>
<td>Provide a value that is the number of hours to scrub memory.</td>
</tr>
<tr>
<td>DRAM scrub time</td>
<td>Options available: Auto, Disabled, 1 hour, 4 hours, 8 hours, 16 hours, 24 hours, 48 hours. Default setting is Auto.</td>
</tr>
<tr>
<td>Poison scrubber control</td>
<td>Enable/Disable the Poison scrubber control feature. Options available: Auto, Enabled, Disabled. Default setting is Auto.</td>
</tr>
<tr>
<td>Redirect scrubber control</td>
<td>Enable/Disable the Redirect scrubber control feature. Options available: Auto, Enabled, Disabled. Default setting is Auto.</td>
</tr>
<tr>
<td>Redirect scrubber limit</td>
<td>Sets the redirect scrubber limit. Options available: Auto, 2, 4, 8, Infinite. Default setting is Auto.</td>
</tr>
</tbody>
</table>
## 5-3-2-2 Memory Addressing

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>NUMA nodes per socket</td>
<td>Specifies the number of desired NUMA nodes per socket. Options available: Auto, NPS0, NPS1, NPS2, NPS4. Default setting is <strong>NPS4</strong>.</td>
</tr>
<tr>
<td>Memory interleaving</td>
<td>Enable/Disable the Memory interleaving feature. Options available: Auto/Disabled. Default setting is <strong>Auto</strong>.</td>
</tr>
<tr>
<td>Memory interleaving size</td>
<td>Controls the memory interleaving size. This determines the starting address of the interleave (bit 8, 9, 10 or 11). Options available: Auto, 256Bytes, 512Bytes, 1KB, 2KB. Default setting is <strong>Auto</strong>.</td>
</tr>
<tr>
<td>1TB remap</td>
<td>Enable/Disable to remap DRAM out of the space just below the 1TB boundary. The ability to remap depends on DRAM configuration, NPS, and interleaving selection, and may not always be possible. Options available: Auto, Do not remap, Attempt to remap. Default setting is <strong>Auto</strong>.</td>
</tr>
<tr>
<td>DRAM map inversion</td>
<td>Enable/Disable the DRAM map inversion function. Options available: Auto, Enabled, Disabled. Default setting is <strong>Auto</strong>.</td>
</tr>
<tr>
<td>Location of private memory regions</td>
<td>Control whether or not the private memory regions (PSP, SMU, and CC6) are at the top of DRAM or distributed. Note that distributed requires memory on all dies. Note that it will always be at the top of DRAM if some dies don't have memory regardless of this. Options available: Auto, Distributed, Consolidated. Default setting is <strong>Auto</strong>.</td>
</tr>
</tbody>
</table>
### 5-3-2-3 ACPI

#### Parameter Description

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>ACPI</td>
<td></td>
</tr>
<tr>
<td>ACPI SRAT L3 Cache As NUMA Domain</td>
<td>Enable/Disable the ACPI SRAT L3 Cache As NUMA Domain function. Options available: Auto, Enabled, Disabled. Default setting is <strong>Auto</strong>.</td>
</tr>
<tr>
<td>ACPI SLIT Distance Control</td>
<td>Determines how the SLIT distances are declared. Options available: Auto/Manual. Default setting is <strong>Auto</strong>.</td>
</tr>
<tr>
<td>ACPI SLIT remote relative distance</td>
<td>Sets the remote socket distance for 2P systems as near (2.8) or far (3.2). Options available: Auto, Near, Far. Default setting is <strong>Auto</strong>.</td>
</tr>
</tbody>
</table>
### 5-3-2-4 Link

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>GMI encryption control</td>
<td>Enable/Disable GMI link encryption. Options available: Auto, Enabled, Disabled. Default setting is <strong>Auto</strong>.</td>
</tr>
<tr>
<td>xGMI encryption control</td>
<td>Enable/Disable xGMI link encryption. Options available: Auto, Enabled, Disabled. Default setting is <strong>Auto</strong>.</td>
</tr>
<tr>
<td>CAKE CRC perf bounds Control</td>
<td>Options available: Auto/Manual. Default setting is <strong>Auto</strong>.</td>
</tr>
<tr>
<td>4-link xGMI max speed</td>
<td>Specifies the max speed of 4-link xGMI. Options available: Auto, 10.667Gbps, 13Gbps, 16Gbps, 18Gbps. Default setting is <strong>10.667Gbps</strong>.</td>
</tr>
<tr>
<td>3-link xGMI max speed</td>
<td>Specifies the max speed of 3-link xGMI. Options available: Auto, 10.667Gbps, 13Gbps, 16Gbps, 18Gbps. Default setting is <strong>10.667Gbps</strong>.</td>
</tr>
<tr>
<td>xGMI TXEQ Mode</td>
<td>Configures xGMI TXEQ/RX vetting Mode. Options available: Auto, TXEQ_Disabled, TXEQ_Lane, TXEQ_Link, TXEQ_RX_Vet. Default setting is <strong>10.667Gbps</strong>.</td>
</tr>
<tr>
<td>xGMI 18GACOFC</td>
<td>Configures xGMI 18GACOFC. Options available: Auto, Enable, Disable. Default setting is <strong>Auto</strong>.</td>
</tr>
</tbody>
</table>
## 5-3-3 UMC Common Options

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>UMC Common Options</td>
<td></td>
</tr>
<tr>
<td>DDR4 Common Options</td>
<td>Press [Enter] for configuration of advanced items.</td>
</tr>
<tr>
<td>DRAM Memory Mapping</td>
<td>Press [Enter] for configuration of advanced items.</td>
</tr>
<tr>
<td>NVDIMM</td>
<td>Press [Enter] for configuration of advanced items.</td>
</tr>
<tr>
<td>Memory MBIST</td>
<td>Press [Enter] for configuration of advanced items.</td>
</tr>
</tbody>
</table>
### 5-3-3-1 DDR4 Common Options

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>DDR4 Common Options</td>
<td>Press [Enter] to configure the Plan of Record (POR) to enable / disable restrictions for DDR4 frequency and voltage programming. Memory speeds will be capped at AMD guidelines. <strong>Note:</strong> To enable 2 DIMMs per Channel at 3200MHz function, select [Accept] at warning message, change Overclock from [Auto] to [Enabled], and then set memory speed to 3200MHz.</td>
</tr>
<tr>
<td>Enforce POR</td>
<td>Press [Enter] to configure the Plan of Record (POR) to enable / disable restrictions for DDR4 frequency and voltage programming. Memory speeds will be capped at AMD guidelines. <strong>Note:</strong> To enable 2 DIMMs per Channel at 3200MHz function, select [Accept] at warning message, change Overclock from [Auto] to [Enabled], and then set memory speed to 3200MHz.</td>
</tr>
<tr>
<td>DRAM Controller Configuration</td>
<td>Press [Enter] to configure DRAM Controller Configuration.</td>
</tr>
<tr>
<td>CAD Bus Configuration</td>
<td>Press [Enter] to configure CAD Bus Configuration.</td>
</tr>
<tr>
<td>Data Bus Configuration</td>
<td>Press [Enter] to configure Data Bus Configuration.</td>
</tr>
<tr>
<td>Common RAS</td>
<td>Press [Enter] to configure Common RAS.</td>
</tr>
</tbody>
</table>
### 5-3-3-1-1 DRAM Controller Configuration

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Description</th>
</tr>
</thead>
</table>
| **DRAM Controller Configuration** | Press [Enter] to configure DRAM Power Options Ma.  
- Power Down Enable  
  - Enable/Disable DDR power down mode.  
  - Options available: Auto, Enabled, Disabled. Default setting is **Auto**.  
- Power Down Entry Delay  
- SubUrgRefLowerBound  
- UrgRefLimit  
- DRAM Maximum Activate Count  
  - Options available: Auto, Unlimited MC, 200K, 300K, 400K, 500K, 600K, 700K. Default setting is **Auto**.  
- DRAM Refresh Rate  
  - Options available: 7.8 usec, 3.9 usec. Default setting is **7.8 usec**.  
- Self-Refresh Exit Staggering  
  - Options available: Disabled, Trfc/3, Trfc/4. Default setting is **Disabled**. |
| **DRAM Power Options**    | **Cmd2T**  
Selects the Cmd2T mode on ADDR/CMD.  
Options available: Auto, 1T, 2T. Default setting is **Auto**. |
|                          | **Gear Down Mode**  
Enable/Disable the Gear Down Mode function.  
Options available: Auto, Enabled, Disabled. Default setting is **Auto**. |
## 5-3-3-1-2 CAD Bus Configuration

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td><strong>CAD Bus Configuration</strong></td>
<td></td>
</tr>
<tr>
<td><strong>CAD Bus Timing User Controls</strong></td>
<td>Setup time on CAD bus signals to Auto or Manual. Options available: Auto/Manual. Default setting is <strong>Auto</strong>.</td>
</tr>
<tr>
<td><strong>CAD Bus Drive Strength User Controls</strong></td>
<td>Drive Strength on CAD bus signals to Auto or Manual. Options available: Auto/Manual. Default setting is <strong>Auto</strong>.</td>
</tr>
</tbody>
</table>
## 5-3-3-1-3 Data Bus Configuration

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>Data Bus Configuration User</td>
<td>Specifies the mode for drive strength to Auto or Manual. Options available: Auto/Manual. Default setting is <strong>Auto</strong>.</td>
</tr>
</tbody>
</table>

**Data Bus Configuration User Controls**: [Table of options]
## 5-3-3-1-4 Common RAS

### BIOS Setup

#### Parameter Description

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td><strong>Common RAS</strong></td>
<td>Enable/Disable the Data Poisoning function. Options available: Auto, Enabled, Disabled. Default setting is <strong>Auto</strong>.</td>
</tr>
<tr>
<td><strong>Data Poisoning</strong></td>
<td>Enable/Disable the Data Poisoning function. Options available: Auto, Enabled, Disabled. Default setting is <strong>Auto</strong>.</td>
</tr>
<tr>
<td><strong>DRAM Post Package Repair</strong></td>
<td>Enable/Disable the DRAM Post Package Repair function. Options available: Enabled/Disabled. Default setting is <strong>Auto</strong>.</td>
</tr>
<tr>
<td><strong>RCD Parity</strong></td>
<td>Enable/Disable the RCD Parity function. Options available: Auto, Enabled, Disabled. Default setting is <strong>Auto</strong>.</td>
</tr>
<tr>
<td><strong>DRAM Address Command Parity Retry</strong></td>
<td>Enable/Disable the DRAM Address Command Parity Retry function. Options available: Auto, Enabled, Disabled. Default setting is <strong>Auto</strong>.</td>
</tr>
<tr>
<td><strong>Max Parity Error Replay</strong></td>
<td>Configures the Max Parity Error Replay. (0~0x3f) Default setting is <strong>8</strong>. Please note that this item is configurable when DRAM Address Command Parity Retry is set to Enabled.</td>
</tr>
<tr>
<td><strong>Write CRC Enable</strong></td>
<td>Enable/Disable the Write CRC function. Options available: Auto, Enabled, Disabled. Default setting is <strong>Auto</strong>.</td>
</tr>
<tr>
<td><strong>DRAM Write CRC Enable and Retry Limit</strong></td>
<td>Enable/Disable DRAM Write CRC Enable and Retry Limit. Options available: Auto, Enabled, Disabled. Default setting is <strong>Auto</strong>.</td>
</tr>
<tr>
<td><strong>Max Write CRC Error Replay</strong></td>
<td>Configures the Max Write CRC Error Replay. (0~0x3f) Default setting is <strong>8</strong>. Please note that this item is configurable when DRAM Write CRC Enable and Retry Limit is set to Enabled.</td>
</tr>
</tbody>
</table>

**Notes:**
- **Auto**:
  - UMC_CH:0:EccCtrl[0xF=0x1F]
  - UMC_CH:1:EccCtrl[1x=F=0x11]
  - Should be enabled/disabled together.

**Keys:**
- **Q**: Select Screen
- **T**: Select Item
- **Enter**: Select Item
- **+/--**: Change Opt.
- **F1**: General Help
- **F3**: Previous Values
- **F9**: Optimized Defaults
- **F10**: Save & Exit
- **ESC**: Exit
<table>
<thead>
<tr>
<th>Parameter</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>Disable Memory Error Injection</td>
<td>Options available: False/True. Default setting is <strong>True</strong>.</td>
</tr>
<tr>
<td></td>
<td>Press [Enter] to configure advanced items.</td>
</tr>
<tr>
<td></td>
<td>- DRAM ECC Symbol Size</td>
</tr>
<tr>
<td></td>
<td>- Configures the DRAM ECC Symbol Size.</td>
</tr>
<tr>
<td></td>
<td>- Options available: Auto, x4, x8, x16. Default setting is <strong>Auto</strong>.</td>
</tr>
<tr>
<td></td>
<td>- DRAM ECC Enable</td>
</tr>
<tr>
<td></td>
<td>- Enable/Disable DRAM ECC. When set to Auto, it will set ECC to enable.</td>
</tr>
<tr>
<td></td>
<td>- Options available: Auto, Enabled, Disabled. Default setting is <strong>Auto</strong>.</td>
</tr>
<tr>
<td></td>
<td>- DRAM UECC Retry</td>
</tr>
<tr>
<td></td>
<td>- Enable/Disable DRAM UECC Retry.</td>
</tr>
<tr>
<td></td>
<td>- Options available: Auto, Enabled, Disabled. Default setting is <strong>Auto</strong>.</td>
</tr>
</tbody>
</table>
## 5-3-3-1-5 Security

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>Security</td>
<td></td>
</tr>
<tr>
<td>TSME</td>
<td>Enable/Disable Transparent SME. Options available: Auto, Enabled, Disabled. Default setting is <strong>Auto</strong>.</td>
</tr>
<tr>
<td>Data Scramble</td>
<td>Enable/Disable Data Scrambling. Options available: Auto, Enabled, Disabled. Default setting is <strong>Auto</strong>.</td>
</tr>
</tbody>
</table>
## 5-3-3-1-6 Phy Configuration

### PMU Training

Press [Enter] to configure PMU Training.
- **DFE Training**
  - Enable/Disable DDR power down mode.
  - Options available: Auto, Enabled, Disabled. Default setting is **Auto**.
- **FFE Write Training**
  - Auto, Enabled, Disabled. Default setting is **Auto**.
- **PMU Pattern Bits Controls**
  - Auto, Manual. Default setting is **Auto**.
### 5-3-3-2 DRAM Memory Mapping

#### DRAM Memory Mapping

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>Chipselect Interleaving</td>
<td>Interleave memory blocks across the DRAM chip selects for CPU 0. Options available: Auto/Disabled. Default setting is <strong>Auto</strong>.</td>
</tr>
<tr>
<td>BankGroupSwap</td>
<td>Configures the BankGroupSwap. BankGroupSwap (BGS) is a new memory mapping option in AGESA that alters how applications get assigned to physical locations within the memory modules. When this option sets to Auto, it is null: No help string. Options available: Auto, Enabled, Disabled. Default setting is <strong>Auto</strong>.</td>
</tr>
<tr>
<td>BankGroupSwapAlt</td>
<td>Configures the BankGroupSwapAlt. Options available: Auto, Enabled, Disabled. Default setting is <strong>Auto</strong>.</td>
</tr>
<tr>
<td>Address Hash Bank</td>
<td>Enable/Disable bank address hashing. Options available: Auto, Enabled, Disabled. Default setting is <strong>Auto</strong>.</td>
</tr>
<tr>
<td>Address Hash CS</td>
<td>Enable/Disable CS address hashing. Options available: Auto, Enabled, Disabled. Default setting is <strong>Auto</strong>.</td>
</tr>
<tr>
<td>Address Hash Rm</td>
<td>Enable/Disable RM address hashing. Options available: Auto, Enabled, Disabled. Default setting is <strong>Auto</strong>.</td>
</tr>
<tr>
<td>SPD Read Optimization</td>
<td>Enable/Disable SPD Read Optimization. Options available: Auto, Enabled, Disabled. Default setting is <strong>Auto</strong>.</td>
</tr>
</tbody>
</table>
## 5-3-3-3 NVDIMM

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>NVDIMM</td>
<td>Disable NVDIMM-N feature for memory margin tool. Options available: No, Yes. Default setting is <strong>No</strong>.</td>
</tr>
</tbody>
</table>
## 5-3-3-4 Memory MBIST

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td><strong>Memory MBIST</strong></td>
<td></td>
</tr>
<tr>
<td>MBIST Enable</td>
<td>Enable/Disable the Memory MBIST function. Options available: Enabled/Disabled. Default setting is <strong>Disabled</strong>.</td>
</tr>
<tr>
<td>MBIST Test Mode (Note)</td>
<td>Selects MBIST Test Mode. <strong>Interface Mode</strong>: Tests Single and Multiple CS transactions and Basic Connectivity.  <strong>Data Eye Mode</strong>: Measures Voltage vs. Timing. Options available: Auto, Both, Interface Mode, Data Eye Mode. Default setting is <strong>Auto</strong>.</td>
</tr>
<tr>
<td>MBIST Aggressors (Note)</td>
<td>Enable/Disable MBIST Aggressor test. Options available: Auto, Enabled, Disabled. Default setting is <strong>Auto</strong>.</td>
</tr>
<tr>
<td>MBIST Per Bit Slave Die Reporting (Note)</td>
<td>Enable/Disable to report 2D data eye results in ABL log for each DQ, Chipselect, and Channel. Options available: Auto, Enabled, Disabled. Default setting is <strong>Auto</strong>.</td>
</tr>
<tr>
<td>Data Eye</td>
<td>Press [Enter] to configure advanced items.</td>
</tr>
<tr>
<td>Memory Healing BIST</td>
<td>Enable/Disable memory healing BIST. Options available: Auto, Enabled, Disabled. Default setting is <strong>Disabled</strong>.</td>
</tr>
</tbody>
</table>

(Nota) This item appears when **MBIST Enable** is set to **Enabled**.
5-3-3-4-1 Data Eye

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td><strong>Pattern Select</strong></td>
<td>Options available: PRBS, SSO, Both. Default setting is <strong>PRBS</strong>.</td>
</tr>
<tr>
<td><strong>Pattern Length</strong></td>
<td>Determines the pattern length. The possible options are N=3....12.</td>
</tr>
<tr>
<td><strong>Aggressor Channel</strong></td>
<td>This item helps read the aggressors channels. Options available: Disabled, 1 Aggressor Channel, 3 Aggressor Channels, 7 Aggressor Channels. Default setting is <strong>1 Aggressor Channel</strong>.</td>
</tr>
<tr>
<td><strong>Aggressor Static Lane Control</strong></td>
<td>Enable/Disable the Aggressor Static Lane Control function. Options available: Enabled/Disabled. Default setting is <strong>Disabled</strong>.</td>
</tr>
<tr>
<td><strong>Aggressor Static Lane Select Upper 32 bits</strong></td>
<td>This item is configurable when <strong>Aggressor Static Lane Control</strong> is set to <strong>Enabled</strong>.</td>
</tr>
<tr>
<td><strong>Aggressor Static Lane Select Lower 32 bits</strong></td>
<td>This item is configurable when <strong>Aggressor Static Lane Control</strong> is set to <strong>Enabled</strong>.</td>
</tr>
<tr>
<td><strong>Aggressor Static Lane Select ECC</strong></td>
<td>This item is configurable when <strong>Aggressor Static Lane Control</strong> is set to <strong>Enabled</strong>.</td>
</tr>
<tr>
<td><strong>Aggressor Static Lane Value</strong></td>
<td>This item is configurable when <strong>Aggressor Static Lane Control</strong> is set to <strong>Enabled</strong>.</td>
</tr>
<tr>
<td><strong>Target Static Lane Control</strong></td>
<td>Enable/Disable the Target Static Lane Control function. Options available: Enabled/Disabled. Default setting is <strong>Disabled</strong>.</td>
</tr>
<tr>
<td>Parameter</td>
<td>Description</td>
</tr>
<tr>
<td>--------------------------------</td>
<td>---------------------------------------------------------------------------------------------------------------------------------------------</td>
</tr>
<tr>
<td>Target Static Lane Select Upper 32 bits</td>
<td>This item is configurable when Target Static Lane Control is set to Enabled.</td>
</tr>
<tr>
<td>Target Static Lane Select Lower 32 bits</td>
<td>This item is configurable when Target Static Lane Control is set to Enabled.</td>
</tr>
<tr>
<td>Target Static Lane Select ECC</td>
<td>This item is configurable when Target Static Lane Control is set to Enabled.</td>
</tr>
<tr>
<td>Target Static Lane Value</td>
<td>This item is configurable when Target Static Lane Control is set to Enabled.</td>
</tr>
<tr>
<td>Data Eye Type</td>
<td>This item determines which results are expected to be captured for Data Eye. Options available: 1D Voltage Sweep, 1D Timing Sweep, 2D Full Data Eye, Worst Case Margin Only. Default setting is Worst Case Margin Only.</td>
</tr>
<tr>
<td>Worst Case Margin Granularity</td>
<td>Configures Worst Case Margin Granularity. Options available: Per Chip Select, Per Nibble. Default setting is Worst Case Margin Only.</td>
</tr>
<tr>
<td>Read Voltage Sweep Step Size</td>
<td>Configures the step size for read Data Eye voltage sweep. Options available: 1, 2, 4. Default setting is 2.</td>
</tr>
<tr>
<td>Read Timing Sweep Step Size</td>
<td>Configures the step size for read Data Eye timing sweep. Options available: 1, 2, 4. Default setting is 1.</td>
</tr>
<tr>
<td>Write Voltage Sweep Step Size</td>
<td>Configures the step size for write Data Eye voltage sweep. Options available: 1, 2, 4. Default setting is 2.</td>
</tr>
<tr>
<td>Write Timing Sweep Step Size</td>
<td>Configures the step size for write Data Eye timing sweep. Options available: 1, 2, 4. Default setting is 1.</td>
</tr>
</tbody>
</table>
### NBIO Common Options

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>IOMMU</td>
<td>Enable/Disable the IOMMU function. Options available: Enabled/Disabled. Default setting is <strong>Disabled</strong>.</td>
</tr>
<tr>
<td>DMAr Support</td>
<td>Enable DMAr system protection during POST. Options available: Auto, Enabled, Disabled. Default setting is <strong>Auto</strong>.</td>
</tr>
<tr>
<td>PCIe ARI Support</td>
<td>Enable/Disable Alternative Routng-ID Interpretation. Options available: Auto, Enabled, Disabled. Default setting is <strong>Auto</strong>.</td>
</tr>
<tr>
<td>PCIe ARI Enumeration</td>
<td>ARI Forwarding Enable for each downstream port. Options available: Auto, Enabled, Disabled. Default setting is <strong>Auto</strong>.</td>
</tr>
<tr>
<td>PCIe Ten Bit Tag Support</td>
<td>Enable/Disable PCIe ten bit tags for supported devices. (Auto=Disabled) Options available: Auto, Enabled, Disabled. Default setting is <strong>Auto</strong>.</td>
</tr>
<tr>
<td>SMU Common Options</td>
<td>Press [Enter] for configuration of advanced items.</td>
</tr>
<tr>
<td>NBIO RAS Common Options</td>
<td>Press [Enter] for configuration of advanced items.</td>
</tr>
<tr>
<td>Enable AER Cap</td>
<td>Enable/Disable Advanced Error Reporting Capability. Options available: Auto, Enabled, Disabled. Default setting is <strong>Auto</strong>.</td>
</tr>
<tr>
<td>Early Link Speed</td>
<td>Configures Early Link Speed. Options available: Auto, Gen1, Gen2. Default setting is <strong>Auto</strong>.</td>
</tr>
<tr>
<td>Parameter</td>
<td>Description</td>
</tr>
<tr>
<td>---------------------------</td>
<td>---------------------------------------------------------------------------------------------------------------------------------------------</td>
</tr>
<tr>
<td>Hot Plug Handling mode</td>
<td>Controls the Hot Plug Handling mode. Options available: Auto, A0 Mode, OS First (No Error Handling), OS First (Error Handling-Not Implemented), Firmware First (Not Implemented). Default setting is <strong>Auto</strong>.</td>
</tr>
<tr>
<td>Presence Detect Select mode</td>
<td>Controls the Presence Detect Select mode. Options available: Auto, OR, AND. Default setting is <strong>Auto</strong>.</td>
</tr>
<tr>
<td>Preferred IO Device</td>
<td>Configures Preferred IO Device. Options available: Auto, Manual. Default setting is <strong>Auto</strong>.</td>
</tr>
<tr>
<td>Loopback Mode</td>
<td>Enabled/Disabled PCIe Loopback mode. Options available: Auto, Enabled, Disabled. Default setting is <strong>Auto</strong>.</td>
</tr>
<tr>
<td>CV test</td>
<td>Set this to Enabled to support running PCIECV tool. Auto: preserve hardware defaults. Options available: Auto, Enabled, Disabled. Default setting is <strong>Auto</strong>.</td>
</tr>
<tr>
<td>CAC Weight Adjustment</td>
<td>EDC Mode select. Options available: Auto, Enabled, Disabled. Default setting is <strong>Auto</strong>.</td>
</tr>
<tr>
<td>SEV-SNP Support</td>
<td>Options available: Enable, Disable. Default setting is <strong>Enable</strong>.</td>
</tr>
<tr>
<td>SRIS</td>
<td>Options available: Auto, Enable, Disable. Default setting is <strong>Auto</strong>.</td>
</tr>
</tbody>
</table>
### 5-3-4-1 SMU Common Options

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td><strong>SMU Common Options</strong></td>
<td></td>
</tr>
<tr>
<td>Power Policy Quick Setting</td>
<td>Options available: Standard, Best Performance, Energy Efficient. Default setting is <strong>Standard</strong>.</td>
</tr>
<tr>
<td>Determinism Control</td>
<td>Selects use the fused Determinism or set customized Determinism. Options available: Auto/Manual. Default setting is <strong>Auto</strong>.</td>
</tr>
<tr>
<td>Determine Slider</td>
<td>Options available: Auto/Power, Performance. Default setting is <strong>Power</strong>.</td>
</tr>
<tr>
<td>cTDP Control</td>
<td>Selects use the fused TDP or set customized TDP. <strong>TDP is used to define the RC thermal model only</strong> Options available: Auto/Manual. Default setting is <strong>Auto</strong>.</td>
</tr>
<tr>
<td>cTDP</td>
<td>Display cTDP information.</td>
</tr>
<tr>
<td>EfficiencyModeEn</td>
<td>Options available: Auto/Enabled. Default setting is <strong>Auto</strong>.</td>
</tr>
<tr>
<td>Package Power Limit Control</td>
<td>Selects use the fused PPT or set customized PPT. <strong>PPT will be used as the ASIC power limit</strong> Options available: Auto/Manual. Default setting is <strong>Auto</strong>.</td>
</tr>
<tr>
<td>Package Power Limit</td>
<td>Display Package Power Limit information</td>
</tr>
<tr>
<td>xGMI Link Width Control</td>
<td>Options available: Auto/Enabled. Default setting is <strong>Auto</strong>.</td>
</tr>
<tr>
<td>APBDIS</td>
<td>Options available: Auto, 0, 1. Default setting is <strong>Auto</strong>.</td>
</tr>
<tr>
<td>Parameter</td>
<td>Description</td>
</tr>
<tr>
<td>-----------------------</td>
<td>---------------------------------------------------------------------------------------------------------------------------------------------</td>
</tr>
<tr>
<td>DF Cstates</td>
<td>Enable/Disable DF C-states. Options available: Auto, Enabled, Disabled. Default setting is <strong>Auto</strong>.</td>
</tr>
<tr>
<td>CPPC</td>
<td>Enable/Disable the CPPC feature. Options available: Auto, Enabled, Disabled. Default setting is <strong>Auto</strong>.</td>
</tr>
<tr>
<td>HSMP Support</td>
<td>Select HSMP support enable or disable. Options available: Auto, Enabled, Disabled. Default setting is <strong>Auto</strong>.</td>
</tr>
<tr>
<td>DLMM Support</td>
<td>Select DLMM support enable or disable. Options available: Auto, Enabled, Disabled. Default setting is <strong>Auto</strong>.</td>
</tr>
<tr>
<td>BoostFmaxEn</td>
<td>Options available: Auto/Enabled. Default setting is <strong>Auto</strong>.</td>
</tr>
<tr>
<td>EDC Current</td>
<td>Options available: Enable, Disable. Default setting is <strong>Disable</strong>.</td>
</tr>
<tr>
<td>LCLK Frequency Control</td>
<td>Press [Enter] for advanced configuration.</td>
</tr>
<tr>
<td>DF PSTATE Mode Select</td>
<td>Option available: Normal, limit Highest, Limit All, Auto. Default setting is <strong>Auto</strong>.</td>
</tr>
</tbody>
</table>
## 5-3-4-2 NBIO RAS Common Options

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>NBIO RAS Control</td>
<td>Options available: Disabled, MCA, Legacy. Default setting is <strong>MCA</strong>.</td>
</tr>
<tr>
<td>Egress Poison Severity High</td>
<td>Configures the Egress Poison High Serverity. Each bit set to 1 enables High serverity on the associated IOHC egress port. A bit of 0 indicates LOW serverity.</td>
</tr>
<tr>
<td>Egress Poison Severity Low</td>
<td>Configures the Egress Poison Low Serverity. Each bit set to 1 enables High serverity on the associated IOHC egress port. A bit of 0 indicates LOW serverity.</td>
</tr>
<tr>
<td>NBIO SyncFlood Generation</td>
<td>The value may be used to mask SyncFlood caused by NBIO RAS options. Options available: Auto, Enabled, Disabled. Default setting is <strong>Auto</strong>.</td>
</tr>
<tr>
<td>NBIO SyncFlood Reporting</td>
<td>The value may be used to enable SyncFlood reporting to APML. Options available: Enabled/Disabled. Default setting is <strong>Disabled</strong>.</td>
</tr>
<tr>
<td>Egress Poison Mask High</td>
<td>Enables mask for masking of errors logged in EGRESS_POISON_STATUS. For each bit set to 1, errors are masked. For each bit set to 0, errors trigger response actions.</td>
</tr>
<tr>
<td>Egress Poison Mask Low</td>
<td>Enables mask for masking of errors logged in EGRESS_POISON_STATUS. For each bit set to 1, errors are masked. For each bit set to 0, errors trigger response actions.</td>
</tr>
<tr>
<td>Parameter</td>
<td>Description</td>
</tr>
<tr>
<td>-----------------------------------------------</td>
<td>---------------------------------------------------------------------------------------------------------------------------------------------</td>
</tr>
<tr>
<td>Uncorrected Converted to Poison Enable Mask High</td>
<td>Enables mask for masking of uncorrectable parity errors on internal arrays.</td>
</tr>
<tr>
<td>Uncorrected Converted to Poison Enable Mask Low</td>
<td>Enables mask for masking of uncorrectable parity errors on internal arrays.</td>
</tr>
<tr>
<td>System Hub Watchdog Timer</td>
<td>Specifies the timer interval of the SYSHUB Watchdog timer in milliseconds.</td>
</tr>
<tr>
<td>SLINK Read Response OK</td>
<td>This item specifies whether SLINK read response errors are converted to an Okay response. Options available: Enabled/Disabled. Default setting is <strong>Disabled</strong>.</td>
</tr>
<tr>
<td>SLINK Read Response Error Handling</td>
<td>Options available: Enabled, Trigger MCOMMIT Error, Log Errors in MCA. Default setting is <strong>Log Errors in MCA</strong>.</td>
</tr>
<tr>
<td>Log Poison Data from SLINK</td>
<td>Enable/Disable the Log Poison Data from SLINK feature. Options available: Enabled/Disabled. Default setting is <strong>Disabled</strong>.</td>
</tr>
<tr>
<td>PCIe Aer Reporting Mechanism</td>
<td>Selects the method of reporting AER errors from PCI Express. Options available: Auto, Firmware First, OS First, MCA. Default setting is <strong>Auto</strong>.</td>
</tr>
<tr>
<td>Edpc Control</td>
<td>Options available: Auto, Enabled, Disabled. Default setting is <strong>Disabled</strong>.</td>
</tr>
<tr>
<td>NBIO Poison Consumption</td>
<td>Options available: Auto, Enabled, Disabled. Default setting is <strong>Auto</strong>.</td>
</tr>
<tr>
<td>Sync Flood on PCIe Fatal Error</td>
<td>Options available: Auto, True, False. Default setting is <strong>Auto</strong>.</td>
</tr>
</tbody>
</table>
5-3-5 FCH Common Options

FCH Common Options
- AC Power Loss Options
- FCH RAS Options
- Miscellaneous Options

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>AC Power Loss Options</td>
<td>Press [Enter] for configuration of advanced items.</td>
</tr>
<tr>
<td>FCH RAS Options</td>
<td>Press [Enter] for configuration of advanced items.</td>
</tr>
<tr>
<td>Miscellaneous Options</td>
<td>Press [Enter] for configuration of advanced items.</td>
</tr>
</tbody>
</table>
### 5-3-5-1 AC Power Loss Options

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>AC Power Loss Options</td>
<td>Selects the AC Loss Control Method. Options available: Power Off, Power On, Last State. Default setting is <strong>Power off</strong>.</td>
</tr>
<tr>
<td>AC Loss Control [Power Off]</td>
<td>Selects the AC Loss Control Method. Options available: Power Off, Power On, Last State. Default setting is <strong>Power off</strong>.</td>
</tr>
</tbody>
</table>
### FCH RAS Options

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>ALink RAS Support</td>
<td>Enable/Disable the ALink RAS Support. Options available: Auto, Enabled, Disabled. Default setting is <strong>Auto</strong>.</td>
</tr>
<tr>
<td>Reset after sync flood</td>
<td>Enables AB to forward downstream sync-flood message to system controller. Options available: Auto, Enabled, Disabled. Default setting is <strong>Auto</strong>.</td>
</tr>
</tbody>
</table>
### 5-3-5-3 Miscellaneous Options

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>Miscellaneous Options</td>
<td>Options available: Auto, Enabled, Disabled. Default setting is <strong>Auto</strong>.</td>
</tr>
<tr>
<td>Boot Time Enable</td>
<td>Options available: Auto, Enabled, Disabled. Default setting is <strong>Auto</strong>.</td>
</tr>
</tbody>
</table>
## 5-3-6 NTB Common Options

### NTB Common Options

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>Socket-0 P0 NTB Enable</td>
<td>Options available: Auto/Enable. Default setting is Auto.</td>
</tr>
<tr>
<td>Socket-0 P1 NTB Enable</td>
<td>Options available: Auto/Enable. Default setting is Auto.</td>
</tr>
<tr>
<td>Socket-0 P2 NTB Enable</td>
<td>Options available: Auto/Enable. Default setting is Auto.</td>
</tr>
<tr>
<td>Socket-0 P3 NTB Enable</td>
<td>Options available: Auto/Enable. Default setting is Auto.</td>
</tr>
</tbody>
</table>
### SOCM Miscellaneous Control

**Parameter** | **Description**
--- | ---
ABL Console Out Control | Enable/Disable the ConsoleOut function for ABL. Options available: Auto, Enabled, Disabled. Default setting is **Auto**.
ABL PMU message Control (Note) | To Control the total number of PMU debug messages. Options available: Auto, Enabled, Disabled. Default setting is **Auto**.

**(Note)** This item appears when ABL Console Out Control is set to Enabled.
### 5-3-8 Workload Tuning

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>Workload Tuning</td>
<td>Select the profile for different workloads.</td>
</tr>
<tr>
<td>Workload Profile</td>
<td>Select the profile for different workloads. Default setting is <strong>Auto</strong>.</td>
</tr>
<tr>
<td>Performance Tracing</td>
<td>Enable to allow capturing performance traces. Options available: Auto, Enabled, Disabled. Default setting is <strong>Auto</strong>.</td>
</tr>
</tbody>
</table>
5-4 AMD PBS Menu

AMD PBS Option menu displays submenu options for configuring the function of AMD PBS. Select a submenu item, then press [Enter] to access the related submenu screen.

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>RAS</td>
<td>Press [Enter] for configuration of advanced items.</td>
</tr>
<tr>
<td>SPI Locking</td>
<td>Enable/Disable SPI Locking for protect ROM part. Options available: Enabled/Disabled. Default setting is <strong>Disabled</strong>.</td>
</tr>
<tr>
<td>iLA TraceMemoryEn</td>
<td>Reserved 1M bytes MMIO space on 1M boundary when iLA TraceMemoryEn disabled. Options available: Enabled/Disabled. Default setting is <strong>Disabled</strong>.</td>
</tr>
<tr>
<td>iLA TraceMemoryEn reserved MMIO</td>
<td>Reserved function.</td>
</tr>
<tr>
<td>SRIS mode debug</td>
<td>Control SRIS mode debug. Options available: Auto/Enabled/Disabled. Default setting is <strong>Auto</strong>.</td>
</tr>
<tr>
<td>SRIS Autodetect</td>
<td>Control SRIS Autodetect. Options available: Auto/Enabled/Disabled. Default setting is <strong>Auto</strong>.</td>
</tr>
</tbody>
</table>
### BIOS Setup

#### 5-4-1 RAS

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>RAS Periodic SMI Control</td>
<td>Enable/Disable the Periodic SMI for polling [MCA Threshold] error. Options available: Enabled/Disabled. Default setting is <strong>Enabled</strong>.</td>
</tr>
<tr>
<td>SMI Threshold</td>
<td>Configures the SMI Threshold value.</td>
</tr>
<tr>
<td>SMI Scale</td>
<td>Configures the SMI Scale value.</td>
</tr>
<tr>
<td>SMI Scale Unit</td>
<td>Defines the unit of time scale. Options available: millisecond, second, minute. Default setting is <strong>millisecond</strong>.</td>
</tr>
<tr>
<td>SMI Period</td>
<td>Configures the SMI Period.</td>
</tr>
<tr>
<td>GHES Notify Type</td>
<td>Selects the Notification type for deferred/ corrected errors. Options available: Polled/SCI. Default setting is <strong>Polled</strong>.</td>
</tr>
<tr>
<td>GHES UnCorrect Notify Type</td>
<td>Selects the Notification type for uncorrected errors. Options available: Polled/NMI. Default setting is <strong>NMI</strong>.</td>
</tr>
<tr>
<td>PCIe GHES Notify Type</td>
<td>Selects the Notification type for PCIe corrected errors. Options available: Polled/SCI. Default setting is <strong>Polled</strong>.</td>
</tr>
<tr>
<td>PCIe UnCorrect GHES Notify Type</td>
<td>Selects the Notification type for PCIe uncorrected errors. Options available: Polled/NMI. Default setting is <strong>NMI</strong>.</td>
</tr>
<tr>
<td>PCIe Root Port Corr Err Mask Reg</td>
<td>Initialize the PCIe AER Corrected Error Mask register of Root Port.</td>
</tr>
</tbody>
</table>

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<table>
<thead>
<tr>
<th>Parameter</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>PCIe Root Port Uncorr Err Mask Reg</td>
<td>Initialize the PCIe AER Uncorrected Error Mask register of Root Port.</td>
</tr>
<tr>
<td>PCIe Root Port Uncorr Err Sev Reg</td>
<td>Initialize the PCIe AER Uncorrected Error Serverity register of Root Port.</td>
</tr>
<tr>
<td>PCIe Device Corr Err Mask Reg</td>
<td>Initialize the PCIe AER Corrected Error Mask register of PCIe device.</td>
</tr>
<tr>
<td>PCIe Device Uncorr Err Mask Reg</td>
<td>Initialize the PCIe AER Uncorrected Error Mask register of PCIe device.</td>
</tr>
<tr>
<td>PCIe Device Uncorr Err Sev Reg</td>
<td>Initialize the PCIe AER Uncorrected Error Serverity register of PCIe device.</td>
</tr>
<tr>
<td>CCIX GHES Deferred ERR Notify Type</td>
<td>Selects the Notification type for CCIX deferred error. Options available: Polled/SCI. Default setting is <strong>Polled</strong>.</td>
</tr>
<tr>
<td>CCIX GHES Corrected Err Notify Type</td>
<td>Selects the Notification type for CCIX corrected error. Options available: Polled/SCI. Default setting is <strong>Polled</strong>.</td>
</tr>
<tr>
<td>DDR4 DRAM Hard Post Package Repair</td>
<td>This feature allows spare DRAM rows to replace malfunctioning rows via an in-field repair mechanism. Options available: Enabled/Disabled. Default setting is <strong>Disabled</strong>.</td>
</tr>
<tr>
<td>HEST DMC Structure Support</td>
<td>HEST DMC (Deferred Machine Check) Structure Support. Options available: Enabled/Disabled. Default setting is <strong>Disabled</strong>.</td>
</tr>
<tr>
<td>RAS EINJ Mode</td>
<td>BIOS: Send APEI EINJ actions to PSP via CPM EINJSMI callback; PSP: Send APEI EINJ actions to RSP via PSP Mailbox. Option available: BIOS, PSP. Default setting is <strong>PSP</strong>.</td>
</tr>
</tbody>
</table>
5-5  Chipset Setup Menu

Chipset Setup menu displays submenu options for configuring the function of the North Bridge. Select a submenu item, then press <Enter> to access the related submenu screen.

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>PCIe Compliance Mode</td>
<td>Options available: On/Off. Default setting is Off.</td>
</tr>
<tr>
<td>Program All VR</td>
<td>Enable/Disable program all VR on MB. Options available: Enabled/Disabled. Default setting is Enabled.</td>
</tr>
<tr>
<td>North Bridge</td>
<td>Press [Enter] for configuration of advanced items.</td>
</tr>
</tbody>
</table>
## 5-5-1 North Bridge

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>North Bridge Configuration</td>
<td></td>
</tr>
<tr>
<td>Memory Information</td>
<td></td>
</tr>
<tr>
<td>Total Memory</td>
<td>Displays the total memory information.</td>
</tr>
<tr>
<td>CPU0 Information</td>
<td>Press [Enter] to view information related to CPU 0.</td>
</tr>
<tr>
<td>CPU1 Information</td>
<td>Press [Enter] to view information related to CPU 1.</td>
</tr>
</tbody>
</table>
# 5-5-2 Fabric Resource

<table>
<thead>
<tr>
<th>Parameter Description</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td><strong>Socket 0/1 NBIO_# PCIe Bus Number</strong></td>
<td>Change Socket 0/1 NBIO_# PCIe Bus Number.</td>
</tr>
</tbody>
</table>
## Server Management Menu

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>FRB-2 Timer</td>
<td>Display the FRB-2 Timer status. This item is not configurable.</td>
</tr>
<tr>
<td>FRB-2 Timer timeout</td>
<td>Configures the FRB2 Timer timeout. Options available: 3 minutes, 4 minutes, 5 minutes, 6 minutes. Default setting is 6 minutes.</td>
</tr>
<tr>
<td>FRB-2 Timer Policy</td>
<td>Configures the FRB2 Timer policy. Options available: Do Nothing, Reset, Power Down, Power Cycle. Default setting is Do Nothing.</td>
</tr>
<tr>
<td>OS Wtd Timer Timeout</td>
<td>Configures OS Watchdog Timer. Options available: 5 minutes, 10 minutes, 15 minutes, 20 minutes. Default setting is 10 minutes. Please note that this item is configurable when OS Watchdog Timer is set to Enabled.</td>
</tr>
<tr>
<td>OS Wtd Timer Policy</td>
<td>Configure OS Watchdog Timer Policy. Options available: Reset, Do Nothing, Power Down. Default setting is Reset. Please note that this item is configurable when OS Watchdog Timer is set to Enabled.</td>
</tr>
<tr>
<td>Parameter</td>
<td>Description</td>
</tr>
<tr>
<td>-------------------------------</td>
<td>-----------------------------------------------------------------------------</td>
</tr>
<tr>
<td>Wait BMC Ready</td>
<td>POST wait BMC ready and reboot system. Options available: Disabled/2 minutes/4 minutes/6 minutes. Default setting is <strong>2 minutes</strong>.</td>
</tr>
<tr>
<td>System Event Log</td>
<td>Press [Enter] to configure advanced items.</td>
</tr>
<tr>
<td>View FRU Information</td>
<td>Press [Enter] to view the FRU information.</td>
</tr>
<tr>
<td>BMC network configuration</td>
<td>Press [Enter] to configure advanced items.</td>
</tr>
<tr>
<td>IPv6 BMC Network Configuration</td>
<td>Press [Enter] to configure advanced items.</td>
</tr>
</tbody>
</table>
## System Event Log

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td><strong>Enabling / Disabling Options</strong></td>
<td>Change this item to enable or disable all features of System Event Logging during boot. Options available: Enabled/Disabled. Default setting is <strong>Enabled</strong>.</td>
</tr>
<tr>
<td><strong>SEL Components</strong></td>
<td>Change this to enable or disable all features of System Event Logging during boot. Options available: Enabled/Disabled. Default setting is <strong>Enabled</strong>.</td>
</tr>
<tr>
<td><strong>Erasing Settings</strong></td>
<td>Choose options for erasing SEL. Options available: No/Yes, On next reset/Yes, On every reset. Default setting is <strong>No</strong>.</td>
</tr>
<tr>
<td><strong>Erase SEL</strong></td>
<td>Choose options for reactions to a full SEL. Options available: Do Nothing/Erase Immediately. Default setting is <strong>Do Nothing</strong>.</td>
</tr>
<tr>
<td><strong>When SEL is Full</strong></td>
<td></td>
</tr>
<tr>
<td><strong>Custom EFI Logging Options</strong></td>
<td>Enable/Disable the logging of EFI Status Codes (if not already converted to legacy). Options available: Disabled, Both, Error code and Progress code. Default setting is <strong>Error code</strong>.</td>
</tr>
</tbody>
</table>
5-6-2 View FRU Information

The FRU page is a simple display page for basic system ID information, as well as System product information. Items on this window are non-configurable.

![FRU Information](image)

(Note) The model name will vary depending on the product you purchased.
## BMC Network Configuration

### Parameter Description

- **BMC network configuration**

#### Lan Channel 1

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>Configuration Address source</td>
<td>Selects to configure LAN channel parameters statically or dynamically (DHCP). Do nothing option will not modify any BMC network parameters during BIOS phase. Options available: Unspecified, Static, DynamicBmcDhcp. Default setting is DynamicBmcDhcp.</td>
</tr>
<tr>
<td>Station IP address</td>
<td>Displays IP Address information.</td>
</tr>
<tr>
<td>Subnet mask</td>
<td>Displays Subnet Mask information. Please note that the IP address must be in three digits, for example, 192.168.000.001.</td>
</tr>
<tr>
<td>Router IP address</td>
<td>Displays the Router IP Address information.</td>
</tr>
<tr>
<td>Station MAC address</td>
<td>Displays the MAC Address information.</td>
</tr>
<tr>
<td>VLAN Support</td>
<td>Set to BMC enabled/disabled VLAN. Options available: Enabled, Disabled. Default setting is Disabled.</td>
</tr>
<tr>
<td>Real-time get BMC network address</td>
<td>Press [Enter] will set LAN mode and Address source and then get IP, Subnet, Gateway and MAC address.</td>
</tr>
</tbody>
</table>
### IPv6 BMC Network Configuration

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>IPv6 BMC network configuration</td>
<td></td>
</tr>
<tr>
<td>IPv6 BMC Lan Channel 1</td>
<td>Enable/Disable IPv6 BMC LAN channel function. When this item is disabled, the system will not modify any BMC network during BIOS phase.</td>
</tr>
<tr>
<td>IPv6 BMC Lan Option</td>
<td>Options available: Unspecified, Disable, Enable. Default setting is Enable.</td>
</tr>
<tr>
<td>IPv6 BMC Lan IP Address Source</td>
<td>Selects to configure LAN channel parameters statically or dynamically (by BIOS or BMC). Options available: Unspecified, Static, Dynamic-Obtained by BMC running DHCP. Default setting is Enable Dynamic-Obtained by BMC running DHCP.</td>
</tr>
<tr>
<td>IPv6 BMC Lan IP Address/Prefix Length</td>
<td>Check if the IPv6 BMC LAN IP address matches those displayed on the screen.</td>
</tr>
</tbody>
</table>
5-7 Security Menu

The Security menu allows you to safeguard and protect the system from unauthorized use by setting up access passwords.

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>Administrator Password</td>
<td>Press [Enter] to configure the administrator password.</td>
</tr>
<tr>
<td>User Password</td>
<td>Press [Enter] to configure the user password.</td>
</tr>
<tr>
<td>Secure Boot</td>
<td>Press [Enter] to configure advanced items.</td>
</tr>
</tbody>
</table>
## 5-7-1 Secure Boot

The Secure Boot submenu is applicable when your device is installed the Windows® 8 (or above) operating system.

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>System Mode</td>
<td>Displays if the system is in User mode or Setup mode.</td>
</tr>
<tr>
<td>Secure Boot Mode (Note)</td>
<td>Secure Boot requires all the applications that are running during the booting process to be pre-signed with valid digital certificates. This way, the system knows all files being loaded before Windows loads to the login screen have not been tampered with. When set to Standard, it will automatically load the Secure Boot keys form the BIOS databases. When set to Custom, you can customize the Secure Boot settings and manually load its keys from the BIOS database. Options available: Standard/Custom. Default setting is Standard.</td>
</tr>
<tr>
<td>Restore Factory Keys</td>
<td>Forces the system to user mode and installs factory default Secure Boot key database.</td>
</tr>
<tr>
<td>Reset to Setup Mode (Note)</td>
<td>Enter Audit Mode workflow. Transitions from User to Audit. Mode will result in erasing of PK variable.</td>
</tr>
<tr>
<td>Enter Audit Mode</td>
<td>Enter Audit Mode. Transitions from User to Audit. Mode will result in erasing of PK variable.</td>
</tr>
</tbody>
</table>

(Note) Advanced items prompt when this item is set to Custom.
<table>
<thead>
<tr>
<th>Parameter</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>Press [Enter] to configure advanced items.</td>
<td></td>
</tr>
</tbody>
</table>

Please note that this item is configurable when Secure Boot Mode is set to Custom.

- **Factory Key Provision**
  - Allows to provision factory default Secure Boot keys when system is in Setup Mode.
  - Options available: Enabled/Disabled. Default setting is **Disabled**.

- **Restore Factory Keys**
  - Installs all factory default keys. It will force the system in User Mode.
  - Options available: Yes/No.

- **Enroll Efi Image**
  - Press [Enter] to enroll SHA256 hash of the binary into Authorized Signature Database (db).

- **Restore DB defaults**
  - Restore DB variable to factory defaults.

- **Secure Boot variable**
  - Displays the current status of the variables used for secure boot.

- **Platform Key (PK)**
  - Displays the current status of the Platform Key (PK).
  - Press [Enter] to configure a new PK.
  - Options available: Set New.

- **Key Exchange Keys (KEK)**
  - Displays the current status of the Key Exchange Key Database (KEK).
  - Press [Enter] to configure a new KEK or load additional KEK from storage devices.
  - Options available: Set New/Append.

- **Authorized Signatures (DB)**
  - Displays the current status of the Authorized Signature Database.
  - Press [Enter] to configure a new DB or load additional DB from storage devices.
  - Options available: Set New/Append.

- **Forbidden Signatures (DBX)**
  - Displays the current status of the Forbidden Signature Database.
  - Press [Enter] to configure a new dbx or load additional dbx from storage devices.
  - Options available: Set New/Append.

- **Authorized TimeStamps (DBT)**
  - Displays the current status of the Authorized TimeStamps Database.
  - Press [Enter] to configure a new DBT or load additional DBT from storage devices.
  - Options available: Set New/Append.

- **OsRecovery Signatures**
  - Displays the current status of the OsRecovery Signature Database.
  - Press [Enter] to configure a new OsRecovery Signature or load additional OsRecovery Signature from storage devices.
  - Options available: Set New/Append.
5-8 Boot Menu

The Boot menu allows you to set the drive priority during system boot-up. BIOS setup will display an error message if the legacy drive(s) specified is not bootable.

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>Boot Configuration</td>
<td>Number of seconds to wait for setup activation key. 65535 (0xFFFF) means indefinite waiting. Press the numeric keys to input the desired values.</td>
</tr>
<tr>
<td>Setup Prompt Timeout</td>
<td></td>
</tr>
<tr>
<td>Bootup NumLock State</td>
<td>Enable/Disable the Bootup NumLock function. Options available: On/Off. Default setting is Off.</td>
</tr>
<tr>
<td>Quiet Boot</td>
<td>Enable/Disable showing the logo during POST. Options available: Enabled/Disabled. Default setting is Enabled.</td>
</tr>
<tr>
<td>Boot mode select</td>
<td>Selects the boot mode. Options available: LEGACY/UEFI. Default setting is UEFI.</td>
</tr>
</tbody>
</table>

**Parameter Description**

**Boot Configuration**
- Setup Prompt Timeout: Number of seconds to wait for setup activation key. 65535 (0xFFFF) means indefinite waiting. Press the numeric keys to input the desired values.
- Quiet Boot: Enable/Disable showing the logo during POST. Options available: Enabled/Disabled. Default setting is Enabled.
- Boot mode select: Selects the boot mode. Options available: LEGACY/UEFI. Default setting is UEFI.
<table>
<thead>
<tr>
<th>Parameter</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>FIXED BOOT ORDER Priorities</td>
<td>Press [Enter] to configure the boot priority.</td>
</tr>
<tr>
<td>Boot Option #1 / #2 / #3 / #4 / #5</td>
<td>By default, the server searches for boot devices in the following sequence:</td>
</tr>
<tr>
<td></td>
<td>1. Hard drive.</td>
</tr>
<tr>
<td></td>
<td>2. CD-ROM/DVD drive.</td>
</tr>
<tr>
<td></td>
<td>3. USB device.</td>
</tr>
<tr>
<td></td>
<td>4. Network.</td>
</tr>
<tr>
<td></td>
<td>5. UEFI.</td>
</tr>
<tr>
<td>UEFI Network Drive BBS Priorities</td>
<td>Press [Enter] to configure the boot priority.</td>
</tr>
<tr>
<td>UEFI Application Boot Priorities</td>
<td>Press [Enter] to configure the boot priority.</td>
</tr>
</tbody>
</table>
5-8-1 UEFI NETWORK Drive BBS Priorities

The UEFI network drive BBS priorities submenu allows you to specify the boot device priority from the available UEFI network drives during system boot-up. BIOS setup will display an error message if the legacy drive(s) specified is not bootable.

Boot Option #1
- PXE IP4 Intel(R)
- 1350 Gigabit Network
  Connection

Boot Option #2
- PXE IP4 Intel(R)
- 1350 Gigabit Network
  Connection

Boot Option #3
- PXE IP6 Intel(R)
- 1350 Gigabit Network
  Connection

Boot Option #4
- PXE IP6 Intel(R)
- 1350 Gigabit Network
  Connection

Sets the system boot order
++: Select Screen
Tt: Select Item
Enter: Select
+-/: Change Opt.
F1: General Help
F3: Previous Values
F9: Optimized Defaults
F10: Save & Exit
ESC: Exit
5-8-2  UEFI Application Boot Priorities

The UEFI application boot priorities submenu allows you to specify the boot device priority from the available UEFI applications during system boot-up. BIOS setup will display an error message if the legacy drive(s) specified is not bootable.
5-9  Save & Exit Menu

The Save & Exit menu displays the various options to quit from the BIOS setup. Highlight any of the exit options then press <Enter>.

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>Save Options</td>
<td>Saves changes made and closes the BIOS setup. Options available: Yes/No.</td>
</tr>
<tr>
<td>Save Changes and Exit</td>
<td>Discards changes made and exits the BIOS setup. Options available: Yes/No.</td>
</tr>
<tr>
<td>Discard Changes and Exit</td>
<td>Saves changes done so far to any of the setup options. Options available: Yes/No.</td>
</tr>
<tr>
<td>Default Options</td>
<td>Loads the default settings for all BIOS setup parameters. Setup Defaults are quite demanding in terms of resources consumption. If you are using low-speed memory chips or other kinds of low-performance components and you choose to load these settings, the system might not function properly. Options available: Yes/No.</td>
</tr>
<tr>
<td>Restore Defaults</td>
<td>Press [Enter] to configure the device as the boot-up drive.</td>
</tr>
<tr>
<td>Launch EFI Shell from filesystem device</td>
<td>Attempts to Launch EFI Shell application (Shell.efi) from one of the available filesystem devices.</td>
</tr>
</tbody>
</table>
## 5-10  BIOS POST Beep code (AMI standard)

### 5-10-1  PEI Beep Codes

<table>
<thead>
<tr>
<th># of Beeps</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>1</td>
<td>Memory not Installed.</td>
</tr>
<tr>
<td>1</td>
<td>Memory was installed twice (InstallPciMemory routine in PEI Core called twice)</td>
</tr>
<tr>
<td>2</td>
<td>Recovery started</td>
</tr>
<tr>
<td>3</td>
<td>DXEIPL was not found</td>
</tr>
<tr>
<td>3</td>
<td>DXE Core Firmware Volume was not found</td>
</tr>
<tr>
<td>4</td>
<td>Recovery failed</td>
</tr>
<tr>
<td>4</td>
<td>S3 Resume failed</td>
</tr>
<tr>
<td>7</td>
<td>Reset PPI is not available</td>
</tr>
</tbody>
</table>

### 5-10-2  DXE Beep Codes

<table>
<thead>
<tr>
<th># of Beeps</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>1</td>
<td>Invalid password</td>
</tr>
<tr>
<td>4</td>
<td>Some of the Architectural Protocols are not available</td>
</tr>
<tr>
<td>5</td>
<td>No Console Output Devices are found</td>
</tr>
<tr>
<td>5</td>
<td>No Console Input Devices are found</td>
</tr>
<tr>
<td>6</td>
<td>Flash update is failed</td>
</tr>
<tr>
<td>7</td>
<td>Reset protocol is not available</td>
</tr>
<tr>
<td>8</td>
<td>Platform PCI resource requirements cannot be met</td>
</tr>
</tbody>
</table>
This page intentionally left blank