GIGABYTE[™]

G363-SR0-AAX1 G363-SR0-AAX4

HPC/Al Server - 4th/5th Gen Intel® Xeon® Scalable 3U DP HGX™ H100 4-GPU (AAX1) 3U DP HGX™ H200 4-GPU (AAX4)

User Manual

Rev. 1.0

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Documentation Classifications

In order to assist in the use of this product, Giga Computing provides the following types of documentation:

- User Manual: detailed information & steps about the installation, configuration and use of this
 product (e.g. motherboard, server barebones), covering hardware and BIOS.
- User Guide: detailed information about the installation & use of an add-on hardware or software component (e.g. BMC firmware, rail-kit) compatible with this product.
- Quick Installation Guide: a short guide with visual diagrams that you can reference easily for installation purposes of this product (e.g. motherboard, server barebones).

Please see the support section of the online product page to check the current availability of these documents

For More Information

For related product specifications, the latest firmware and software, and other information please visit our website at http://www.giqabyte.com/Enterprise

For GIGABYTE distributors and resellers, additional sales & marketing materials are available from our reseller portal: http://reseller.b2b.gigabyte.com

For further technical assistance, please contact your GIGABYTE representative or visit https://esupport.gigabyte.com/ to create a new support ticket

For any general sales or marketing enquiries, you may also message GIGABYTE server directly by email: server.grp@gigabyte.com

Conventions

The following conventions are used in this user's guide:

	NOTE! Pieces of additional information related to the current topic.		
	CAUTION! Precautionary measures to avoid possible hardware or software problems.		
A	WARNING! Alerts to any damage that might result from doing or not doing specific actions.		

Server Warnings and Cautions

Before installing a server, be sure that you understand the following warnings and cautions.



WARNING!

To reduce the risk of electric shock or damage to the equipment:

- Do not disable the power cord grounding plug. The grounding plug is an important safety feature.
- Plug the power cord into a grounded (earthed) electrical outlet that is easily accessible at all times.
- Unplug the power cord from the power supply to disconnect power to the equipment.





- · Shock Hazard! Disconnect all power supply cords before servicing.
- Do not route the power cord where it can be walked on or pinched by items placed against it.
 Pay particular attention to the plug, electrical outlet, and the point where the cord extends from the server.
- Do not route the power cord where it can be walked on or pinched by items placed against it.
 Pay particular attention to the plug, electrical outlet, and the point where the cord extends from the server.



WARNING!

To reduce the risk of personal injury from hot surfaces, allow the drives and the internal system components to cool before touching them.



This server is equipped with high speed fans. Keep away from hazardous moving fan blades during servicing.



This equipment is intended to be used in Restrict Access Location. The access can only be gained by Skilled person. Only authorized by well trained professional person can access the restrict access location.



This equipment is not intended for use by children.



- Do not operate the server for long periods with the access panel open or removed. Operating the server in this manner results in improper airflow and improper cooling that can lead to thermal damage.
- · Danger of explosion if battery is incorrectly replaced.
- Replace only with the same or equivalent type recommended by the manufacturer.
- · Dispose of used batteries according to the manufacturer's instructions.



CAUTION!

Risk of explosion if battery is replaced incorrectly or with an incorrect type. Replace the battery only with the same or equivalent type recommended by the manufacturer. Dispose of used batteries according to the manufacturer's instructions.

Warning Stability hazard

The slide-rail may tip over causing serious personal injury

- Before extending the rack to its installation position, read the installation instructions.
- Do not put any load on the slide-rail mounted equipment in the installation position.
- Do not leave the slide-rail mounted equipment in the installation position.



CAUTION!

ESD CAN DAMAGE DRIVES, BOARDS, AND OTHER PARTS. WE RECOMMEND THAT YOU PERFORM ALL PROCEDURES AT AN ESD WORKSTATION. IF ONE IS NOT AVAILABLE, PROVIDE SOME ESD PROTECTION BY WEARING AN ANTI-STATIC WRIST STRAP ATTACHED TO CHASSIS GROUND -- ANY UNPAINTED METAL SURFACE -- ON YOUR SERVER WHEN HANDLING PARTS.

Always handle boards carefully, they can be extremely sensitive to ESD. Hold boards only by their edges without touching any components or connectors. After removing a board from its protective ESD bag or from the system, place the board component side up on a grounded, static free surface. Use a conductive foam pad if available but not the ESD bag. Do not slide the board over any surface.

System power on/off: To service components within the server, please ensure the power has been disconnected.

e.g. Remove the node from the server chassis (to disconnect power) or disconnect the power from the server chassis.

Make sure the system is removed from the rack before opening the chassis, adding, or removing any non hot-plug components.

Hazardous conditions, devices and cables: Hazardous electrical conditions may be present on power, telephone, and communication cables. Turn off the system chassis and disconnect the cables attached to the system before servicing the chassis. Otherwise, personal injury or equipment damage can result.

Electrostatic discharge (ESD) and ESD protection: ESD can damage drives, boards, and other parts. We recommend that you perform all procedures in this chapter only at an ESD workstation. If one is not available, provide some ESD protection by wearing an antistatic wrist strap attached to chassis ground (any unpainted metal surface on the server) when handling parts.

ESD and handling boards: Always handle boards carefully. They can be extremely sensi-tive to electrostatic discharge (ESD). Hold boards only by their edges. After removing a board from its protective wrapper or from the system, place the board component side up on a grounded, static free surface. Use a conductive foam pad if available but not the board wrapper. Do not slide board over any surface.

Installing or removing jumpers: A jumper is a small plastic encased conductor that slips over two jumper pins. Some jumpers have a small tab on top that can be gripped with fin-gertips or with a pair of fine needle nosed pliers. If the jumpers do not have such a tab, take care when using needle nosed pliers to remove or install a jumper; grip the narrow sides of the jumper with the pliers, never the wide sides. Gripping the wide sides can dam-age the contacts inside the jumper, causing intermittent problems with the function con-trolled by that jumper. Take care to grip with, but not squeeze, the pliers or other tool used to remove a jumper, or the pins on the board may bend or break.

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Chapter 1 Hardware Installation

1-1 Installation Precautions

The motherboard/system contain numerous delicate electronic circuits and components which can become damaged as a result of electrostatic discharge (ESD). Prior to installation, carefully read the service guide and follow these procedures:

- Prior to installation, do not remove or break motherboard S/N (Serial Number) sticker or warranty sticker provided by your dealer. These stickers are required for warranty validation.
- Always remove the AC power by unplugging the power cord from the power outlet before installing or removing the motherboard or other hardware components.
- When connecting hardware components to the internal connectors on the motherboard, make sure they are connected tightly and securely.
- When handling the motherboard, avoid touching any metal leads or connectors.
- It is best to wear an electrostatic discharge (ESD) wrist strap when handling electronic
 components such as a motherboard, CPU or memory. If you do not have an ESD wrist
 strap, keep your hands dry and first touch a metal object to eliminate static electricity.
- Prior to installing the motherboard, please have it on top of an antistatic pad or within an
 electrostatic shielding container.
- Before unplugging the power supply cable from the motherboard, make sure the power supply has been turned off.
- Before turning on the power, make sure the power supply voltage has been set according to the local voltage standard.
- Before using the product, please verify that all cables and power connectors of your hardware components are connected.
- To prevent damage to the motherboard, do not allow screws to come in contact with the motherboard circuit or its components.
- Make sure there are no leftover screws or metal components placed on the motherboard or within the computer casing.
- · Do not place the computer system on an uneven surface.
- Do not place the computer system in a high-temperature environment.
- Turning on the computer power during the installation process can lead to damage to system components as well as physical harm to the user.
- If you are uncertain about any installation steps or have a problem related to the use of the product, please consult a certified computer technician.

1-2 Product Specifications



NOTE

We reserve the right to make any changes to the product specifications and product-related information without prior notice.

System Dimension	3U 448 (W) x 130 (H) x 800 (D) mm
CPU CPU	5th Generation Intel® Xeon® Scalable Processors 4th Generation Intel® Xeon® Scalable Processors Intel® Xeon® CPU Max Series Dual processor, TDP up to 350W NOTE: If only 1 CPU is installed, some PCle or memory functions might be
	unavailable
Socket	 ◆ 2 x LGA 4677 ◆ Socket E
Chipset	◆ Intel® C741 Chipset
Memory	 16 x DIMM slots DDR5 memory supported 8-Channel memory per processor
	5th Gen Intel® Xeon®: ◆ RDIMM: Up to 5600 MT/s
	4th Gen Intel® Xeon®: ◆ RDIMM: Up to 4800 MT/s
	Intel® Xeon® Max Series: ◆ RDIMM: Up to 4800 MT/s
LAN	Front (I/O board - CLBH160): 2 x 1Gb/s LAN ports (1 x Intel® I350-AM2) Support NCSI function (by switch setting)
	Rear (MLAN board - CDB66):
	1 x 10/100/1000 management LAN
	[Note] When both MLAN ports are connected with cables, the front MLAN port will be set as the default.
Video	 ◆ Integrated in Aspeed® AST2600 ◆ 1 x Mini-DP

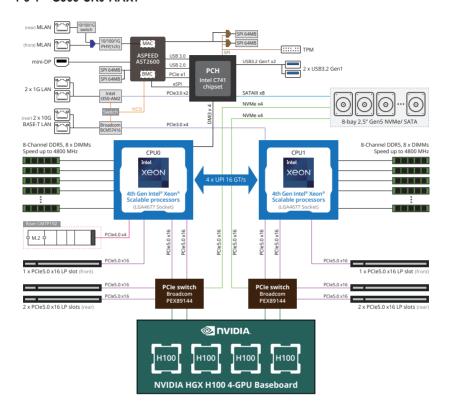
Storage	Front hot-swap:						
	8 x 2.5" Gen5 NVMe/SATA/SAS-4 [1] ANAL GOVERNMENT OF YOUR AND THE YOUR AND						
	(NVMe from PEX89144)						
	Internal M.2 (CMTP192):						
	• 1 x M.2 (2280/22110), PCIe Gen4 x4, from CPU_0						
	[1] SAS card is required to support SAS drives.						
SAS	Reguire SAS add-in cards						
RAID	Intel® SATA RAID 0/1/10/5						
	Support optional RAID add-in cards						
Modular GPU	G363-SR0-AAX1						
	NVIDIA HGX™ H100 with 4 x SXM5 GPUs						
	G363-SR0-AAX4						
	NVIDIA HGX™ H200 with 4 x SXM GPUs						
Expansion Slot	Riser Card CRSH01R:						
	 1 x LP x16 (Gen5 x16), from CPU_0 						
	, , , _						
	DCIa Calala.						
	PCle Cable:						
	• 1 x LP x16 (Gen5 x16), from CPU_1						
	1 x OCP NIC 3.0, from CPU_0, disabled						
	Rear:						
	 4 x LP x16 (Gen5 x16), from PEX89144, support RDMA 						
	(Equipped with full-height brackets)						
Front I/O	1 x Power button with LED						
	• 1 x ID button with LED						
	1 x Reset button						
	1 x Storage activity LED						
	1 x System status LED						
	I/O board - CLBH160:						
	2 x USB 3.2 Gen1 ports (Type-A)						
	• 1 x Mini-DP						
	• 2 x RJ45 ports (Intel I350)						
	1 x MLAN port (default)						
Rear I/O	2 x RJ45 ports (Broadcom BCM57416)						
	MLAN board - CDB66:						
	1 x MLAN port						
	·						

Backplane I/O	Speed and bandwidth:			
	PCle Gen5 x4 or SATA 6Gb/s or SAS 12Gb/s			
TPM	1 x TPM header with SPI interface			
	Optional TPM2.0 kit: CTM010			
Power Supply	• 2+1 3000W 80 PLUS Titanium redundant power supplies [1]			
	[1] The system power supply requires C19 power cord.			
	[Note] Please refer to GIGABYTE Website for detail power supply specification.			
System	Aspeed® AST2600 Baseboard Management Controller			
Management	GIGABYTE Management Console web interface			
	• • • • • • • • • • • • • • • • • • •			
	◆ Dashboard			
	◆ HTML5 KVM			
	 Sensor Monitor (Voltage, RPM, Temperature, CPU Statusetc.) 			
	Sensor Reading History Data			
	FRU Information			
	SEL Log in Linear Storage / Circular Storage Policy			
	Hardware Inventory			
	◆ Fan Profile			
	System Firewall			
	Power Consumption			
	Power Control			
	◆ LDAP / AD / RADIUS Support			
	Backup & Restore Configuration			
	◆ Remote BIOS/BMC/CPLD Update			
	Event Log Filter			
	User Management			
	Media Redirection Settings			
	PAM Order Settings			
	SSL Settings			
	SMTP Settings			
Operating	Operating temperature: 10°C to 35°C			
Properties	 Operating humidity: 8%-80% (non-condensing) 			
	 Non-operating temperature: -40°C to 60°C 			

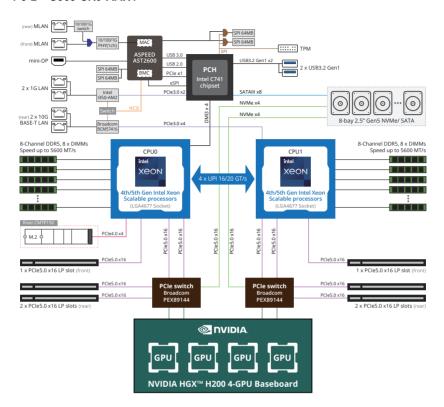
Non-operating humidity: 20%-95% (non-condensing)

1-3 System Block Diagram

1-3-1 G363-SR0-AAX1

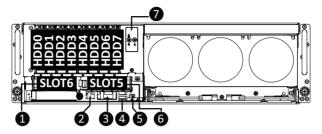


1-3-2 G363-SR0-AAX4



Chapter 2 System Appearance

2-1 Front View



No.	Description
1.	PCIe Card Slot
2.	10/100/1000 Server Management LAN Port*
3.	1 GbE LAN Port x2
4.	USB 3.2 Gen1 Port x 2
5.	Mini DP Port
6.	PCIe Card Slot
7.	Front Panel LEDs and Buttons

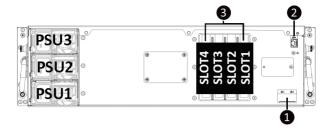
Note!

- 1. Drives with green latches support NVMe.
- 2. Only one Management LAN (front side or rear) can be used at a time.



 Refer to section 2-3 Front Panel LEDs and Buttons for a detailed description of the function of the LEDs.

2-3 Rear View

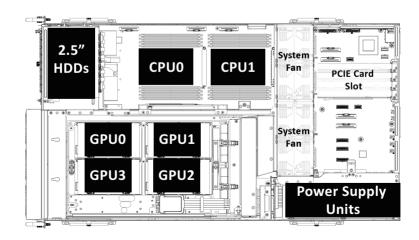


No.	Description		
1.	10 GbE LAN Port x 2		
2.	10/100/1000 Server Management LAN Port*		
3.	PCle Card Slot		
Note! Only one Management LAN (front side or rear) can be used at a time.			

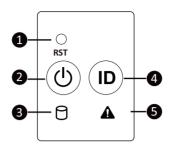


 Refer to section 2-5 Rear System LAN LEDs for a detailed description of the function of the LEDs.

2-2 Top View

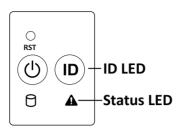


2-4 Front Panel LEDs and Buttons



No.	Name	Color	Status	Description	
1.	Reset Button			Press this button to reset the system.	
•	Power button	Green	On	Indicates the system is powered on.	
2.	with LED	N/A	Off	System is not powered on or in ACPI S5 state (power off)	
		Green	On	Indicates locating the HDD.	
		Green	Blink	Indicates accessing the HDD.	
3.	HDD Status	Amber	On	Indicates HDD error.	
	LED	Green/ Amber	Blink	Indicates HDD rebuilding.	
		N/A	Off	Indicates no HDD access or no HDD error.	
4.	ID Button with LED(Note)	Blue	On	System identification is active.	
4.		N/A	Off	System identification is disabled.	
		Green	Solid On	System is operating normally.	
5.	System Status LED ^(Note)	Amber	Solid On	Critical condition, may indicate: System fan failure System temperature	
			Blink	Non-critical condition, may indicate: Redundant power module failure Temperature and voltage issue Chassis intrusion	
		N/A	Off	System is not ready, may indicate: POST error NMI error Processor or terminator missing	

2-5 RoT LEDs



	LED on Front panel(Note5)	
	ID LED	Status LED
EC Firmware (FW) Authentication fail or not exit		
EC FW is broken or not exit (Note1)	OFF	OFF
Authenticating/Recovering BMC/BIOS Images		
Authenticating Images	OFF	OFF
Recovering BMC Active Flash	Blinks Blue 4 times per second	Blinks Green 4 times per second
Recovering BIOS Active Flash	Blinks Blue 4 times per second	Blinks Green 4 times per second
Authentication (AUTH) Pass		
Recovering BIOS Active Flash	OFF	OFF
BMC : AUTH pass after doing recovery BIOS : AUTH pass after doing recovery	OFF	OFF
BMC : AUTH pass after doing recovery BIOS : AUTH pass	OFF	OFF
BMC : AUTH pass BIOS : AUTH pass after doing recovery	OFF	OFF

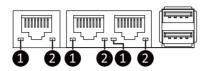
Active Flash Authentication (AUTH) Fail		
DMO - ALITH F - 11(Note2)	Blinks Blue	Blinks Green
BMC : AUTH Fail ^(Note2)	1 time per second	1 time per second
DIOC - ALITH 5-1/Note2)	Blinks Blue	Blinks Amber
BIOS : AUTH fail ^(Note2)	1 time per second	1 time per second
	Blinks Blue	Blinks Green
BMC : AUTH fail after doing recovery(Note3)	2 times per second	2 times per second
• •	[ON OFF OFF]	[ON OFF OFF]
	Blinks Blue	Blinks Amber
BIOS: AUTH fail after doing recovery(Note3)	2 times per second	2 times per second
,	[ON OFF OFF]	[ON OFF OFF]
Backup Flash Authentication Fail ^(Note4)		
	Blinks Blue	Blinks Green
BMC : AUTH fail	2 times per second	2 times per second
	[ON OFF ON OFF]	[ON OFF ON OFF]
	Blinks Blue	Blinks Amber
BIOS: AUTH fail	2 times per second	2 times per second
	[ON OFF ON OFF]	[ON OFF ON OFF]

NOTE!

- 1. EC FW is broken or not exited result in Microchip CEC1702 cannot load EC FW for authentication.
- (1) Authentication fail include below scenarios Configuration table is missing or modified Public key is missing or modified Protected area or signature is modified Flash empty
- if active flash is still authentication failed after recovery sequence, Microchip CEC1702 stop the process and showing LED behavior.
- 4. If backup flash authentication is failed cause by configuration table, public key or protected area is broken. Microchip CEC1702 stop the process and showing LED behavior.
- Front panel LED is controlled by BMC or Microchip CEC1702. Once Microchip CEC1702 is working(Auth or recovery), the front panel LED is controlled by Microchip CEC1702 and vice versa.

System LAN LEDs 2-6

Front System LAN LED



Rear System LAN LED

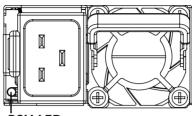


No.	Name	Color	Status	Description	
	1GbE Speed LED -	Yellow	On	1 Gbps data rate	
1.		Green	On	100 Mbps data rate	
		N/A	Off	10 Mbps data rate	
		Green	On	Link between system and network or no access	
2.	1GbE Link / Activity LED _		Blink	Data transmission or reception is occurring.	
		N/A	Off	No data transmission or reception is occurring.	
	10GbE -	Yellow	On	10 Gbps data rate	
3.		Green	On	1 Gbps data rate	
		N/A	Off	100 Mbps data rate	
	10GbE Link / Activity LED _	Green	On	Link between system and network or no access	
4.			Blink	Data transmission or reception is occurring.	
		N/A	Off	No data transmission or reception is occurring.	

Power Supply Unit LED 2-7



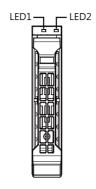
The power supply may be vary based on the system configuration.



PSU LED

State	Description		
Off	No AC power to all power supplies		
1Hz Green Blinking	AC present / only standby on / Cold redundant mode		
2Hz Green Blinking	Power supply firmware updating mode		
Amber	AC cord unplugged or AC power lost; with a second power supply in parallel still with AC input power		
Amber	Power supply critical event causing shut down: failure, OCP, OVP, fan failure and UVP		
1Hz Amber Blinking	Power supply warning events where the power supply continues to operate: high temp, high power, high current and slow fan		

2-8 Hard Disk Drive LEDs



RAID S	SKU	LED1	Locate	HDD Fault	Rebuilding	HDD Access	HDD Present (No Access)
	Disk LED (LED on Back Panel)	Green	ON(*1)	OFF		BLINK (*2)	OFF
No DAID configuration		Amber	OFF	OFF		OFF	OFF
No RAID configuration (via HBA)	Removed HDD Slot (LED on Back Panel)	Green	ON(*1)	OFF			
		Amber	OFF	OFF			-
	Disk LED	Green	ON	OFF		BLINK (*2)	OFF
RAID configuration (via HW RAID Card or		Amber	OFF	ON	(Low Speed: 2 Hz)	OFF	OFF
SW RAID Card)	Removed HDD Slot	Green	ON(*1)	OFF	(*3)		
		Amber	OFF	ON	(*3)		

LED 2	HDD Present	No HDD	
Green	ON	OFF	

NOTE:

^{*1:} Depends on HBA/Utility Spec.

^{*2:} Blink cycle depends on HDD's activity signal.

^{*3:} If HDD is pulled out during rebuilding, the disk status of this HDD is regarded as faulty.

Chapter 3 System Hardware Installation



Pre-installation Instructions

Computer components and electronic circuit boards can be damaged by discharges of static electricity. Working on computers that are still connected to a power supply can be extremely dangerous. Follow the simple guidelines below to avoid damage to your computer or injury to yourself.

- Always disconnect the computer from the power outlet whenever you are working inside the computer case.
- If possible, wear a grounded wrist strap when you are working inside the computer case.
 Alternatively, discharge any static electricity by touching the bare metal system of the computer case, or the bare metal body of any other grounded appliance.
- Hold electronic circuit boards by the edges only. Do not touch the components on the board unless it is necessary to do so. Do not flex or stress the circuit board.
- Leave all components inside the static-proof packaging until you are ready to use the component for the installation.

3-1 Removing and Installing the Top Cover

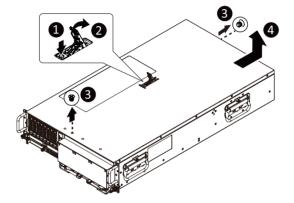


Before you remove the top cover:

Make sure the system is not turned on or connected to AC power.

Follow these instructions to remove the top cover:

- 1. Unlock the plastic handle.
- 2. Pull the grip handle.
- 3. Remove the two screw securing the top cover.
- Slide the cover to the rear of the system and then remove the cover in the direction indicated by the arrow
- 5. To reinstall the chassis cover reverse steps 1-3.



3-2 Removing and Installing the Hard Disk Drive

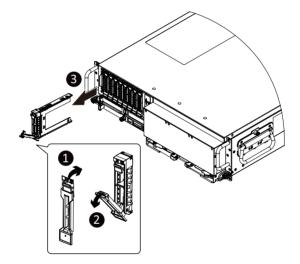


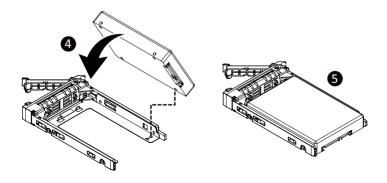
Read the following guidelines before you begin to install the hard disk drive:

- Take note of the HDD tray orientation before sliding it out.
- The tray will not fit back into the bay if it is inserted incorrectly.
- Make sure that the hard disk drive is connected to the connector on the backplane.

Follow these instructions to install a 2.5" hard disk drive:

- Press the release button.
- 2. Extend the locking lever.
- 3. Pull the locking lever in the direction indicated to remove the HDD tray.
- 4. Align the hard disk drive with the positioning stud on the HDD tray.
- 5. Slide the hard disk drive into the HDD tray.
- 6. Reinsert the HDD tray into the slot and close the locking lever.

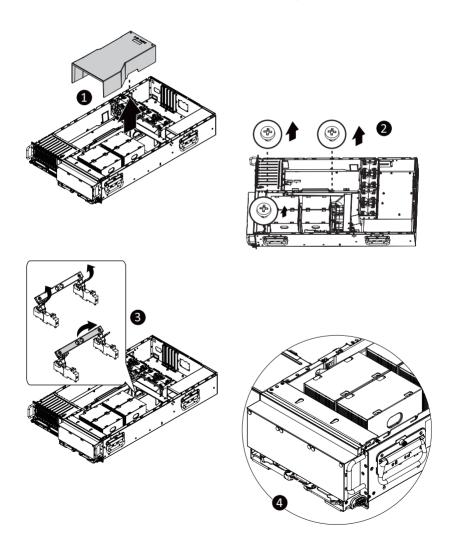


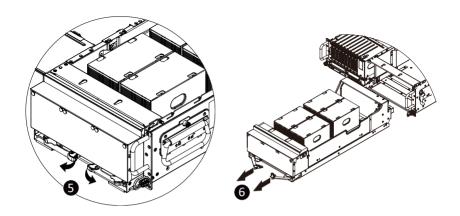


3-3 Removing and Installing the GPU Card Tray

Follow these instructions to remove the GPU card tray:

- 1. Remove the GPU fanduct out of the system.
- 2. Remove the three screws securing the GPU card board.
- 3. Filp over the handle.
- 4. Pull the grip handles on the both sides of the system
- 5. Slide the tray to the front of the system at the same time to remove the tray.
- 6. Follow steps 1-5 in reverse order to re-install the GPU card tray.





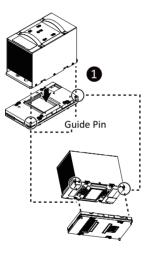
3-4 Installing and Removing the GPU Heat Sink

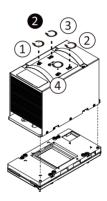


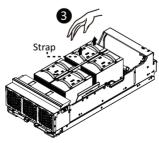
- Thoroughly unlock the four screws before lifting and removing the heatsink.
- When installing the heatsink to CPU,use T20-Lobe driver to tighten 4 captive nuts in sequence as 1-4.
- The screw tightening torque: 3.5 +/- 0.5 kgf-cm

Follow these instructions to remove the GPU card tray:

- To install the heatsink, align the heatsink to the GPU socket using the guide pins and make sure the gold triangle is in the correct orientation. Then place the heatsink onto the top of the GPU socket.
- 2. Secure the heatsink by tightening the screws in sequential order $(1 \rightarrow 2 \rightarrow 3 \rightarrow 4)$.
- 3. To remove the heatsink, gently pull up the heatsink strap.





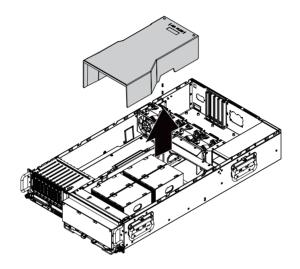


3-5 Removing and Installing the Fan Duct

Follow these instructions to remove the fan duct:

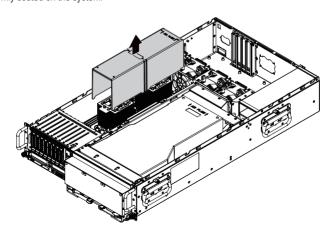
GPU Fan Duct:

- 1. Lift up to remove the fan duct.
- 2. To reinstall the fan duct, align the fan duct with the guiding groove. Push down the fan duct until it is firmly seated on the system.



CPU Fan Duct:

- 3. Lift up to remove the fan duct.
- 4. To reinstall the fan duct, align the fan duct with the guiding groove. Push down the fan duct until it is firmly seated on the system.



3-6 Removing and Installing the Heat Sink



Read the following guidelines before you begin to install the heat sink:

- Always turn off the computer and unplug the power cord from the power outlet before installing the heat sink to prevent hardware damage.
- Unplug all cables from the power outlets.
- Disconnect all telecommunication cables from their ports.
- Place the system unit on a flat and stable surface.
- · Open the system according to the instructions.

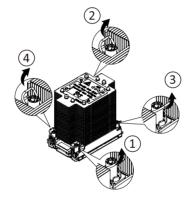


WARNING!

Failure to turn off the server before you start installing components may cause serious damage. Do not attempt the procedures described in the following sections unless you are a qualified service technician

Follow these instructions to install the heat sink:

- 1. Loosen the screws securing the heat sink in place in reverse order $(4 \rightarrow 3 \rightarrow 2 \rightarrow 1)$.
- 2. Lift and remove the heat sink from the system.
- 3. To install the heat sink, reverse steps 1-2 while ensuring that you tighten the captive screws in sequential order (1→2→3→4) as seen in the image below.



3-7 Installing the CPU and Heat Sink



Read the following guidelines before you begin to install the CPU:

- Make sure that the motherboard supports the CPU.
- Always turn off the computer and unplug the power cord from the power outlet before installing the CPU to prevent hardware damage.
- Unplug all cables from the power outlets.
- Disconnect all telecommunication cables from their ports.
- · Place the system unit on a flat and stable surface.
- Open the system according to the instructions.



WARNING!

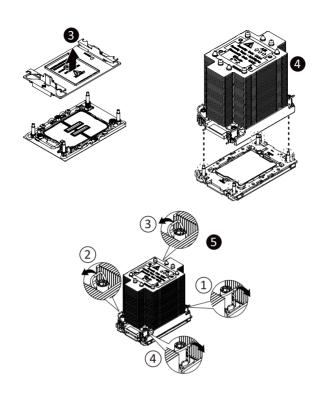
Failure to properly turn off the server before you start installing components may cause serious damage. Do not attempt the procedures described in the following sections unless you are a qualified service technician.

Follow these instructions to install the CPU:

- Align the processor to the carrier so that the gold triangle on the processor aligns with the triangle on the carrier, and then install the processor into the carrier.
 - NOTE: Apply thermal compound evenly on the top of the CPU.
- Carefully flip the heatsink over. Align the carrier assembly so that the triangle on the carrier aligns with the triangle on the heatsink, and then install the carrier assembly onto the bottom of the heatsink.
- 3. Remove the CPU socket cover.
 - NOTE: Save and replace the CPU socket cover if the processor is removed from its socket.
- Align the heatsink to the CPU socket using the guide pins and make sure the gold triangle is in the correct orientation. Then place the heatsink onto the top of the CPU socket.
- Secure the heatsink by tightening the screws in sequential order (1→2→3→4).
 NOTE: When removing the heatsink, loosen the screws in reverse order (4→3→2→1).







Carrier Types used for Package Types

Package Type	Xeon® SP XCC	Xeon® SP MCC	Xeon® SP+HBM	
Carrier Code	E1A	E1B	E1C	

NOTE!

- The carrier code is marked on each carrier and matches a code laser marked on to the IHS(Integrated Heat Spreader) to ensure the right parts are used together.
- When installing the heatsink to CPU, use T30-Lobe driver to tighten 4 captive nuts in sequence as 1-4.
- The screw tightening torque: 8 ± 0.5 kgf-cm.

3-8 Removing and Installing Memory

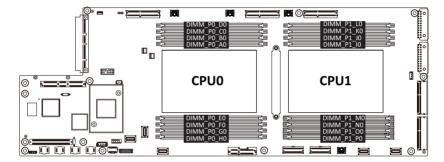


Read the following guidelines before you begin to install the memory:

- Make sure that the motherboard supports the memory. It is recommended that memory of the same capacity, brand, speed, and chips be used.
- Always turn off the computer and unplug the power cord from the power outlet before installing the memory to prevent hardware damage.
- Memory modules have a foolproof design. A memory module can be installed in only one direction. If you are unable to insert the memory, switch the direction.

3-8-1 Eight Channel Memory Configuration

This motherboard provides 16 DDR5 memory sockets and supports Eight Channel Technology. After the memory is installed, the BIOS will automatically detect the specifications and capacity of the memory.



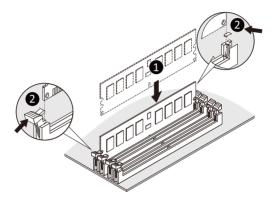
3-8-2 Removing and Installing a Memory Module



Before installing a memory module, make sure to turn off the computer and unplug the power cord from the power outlet to prevent damage to the memory module. Be sure to install DDR5 DIMMs on to this motherboard.

Follow these instructions to install a DIMM module:

- 1. Insert the DIMM memory module vertically into the DIMM slot and push it down.
- 2. Close the plastic clip at both edges of the DIMM slots to lock the DIMM module.
- 3. Reverse the installation steps when you want to remove the DIMM module.



3-8-3 DIMM Population Table

4thGen Intel Xeon Scalable Processors-SP Memory Support

Туре	Ranks Per DIMM and	DIMM Capacity (GB)		(GB)	Speed (MT/s); DIMM per Ch	• ()	
		16Gb	24Gb ²	36Gb	1.1	V	
	SRx8 (RC D)	16GB	24GB	NA	4000	4400	
	SRx4 (RC C)	32GB	48GB	NA			
RDIMM	SRx4 (RC F) 9x4	32GB	NA	NA			
KDIIVIIVI	DRx8 (RC E)	32GB	48GB	NA			
	DRx4 (RC A)	64GB	96GB	128GB	4800		
	DRx4 (RC B) 9x4	64GB	NA	NA			
RDIMM 3DS	(4R/8R)x4	2H-128GB	NA	NA			
KDIIVIIVI 3DS	(RC A)	4H-256GB	INA	INA			

NOTE:

- 1. 1DPC applies to 1SPC or 2SPC implementations (SPC Sockets Per Channel)
- 2. 24Gb XCC only w/ limited configs: 1DPC all DIMM types, 2DPC 96GB only. Only 8 and 16 DIMM configs, no fallbacks.

5th Gen Intel Xeon Scalable Processors-SP Memory Support

	Ranks Per DIMM and	DIM	M Capacity	(GB)	Speed (MT/s); DIMM per Ch	• ()	
Туре	Data Width				1DPC ¹ 2DPC		
		16Gb	24Gb ²	36Gb	1.1V		
	SRx8 (RC D)	16GB	24GB	NA			
	SRx4 (RC C)	32GB	48GB	NA			
RDIMM	SRx4 (RC F) 9x4	NA	NA	NA	5600 ³	4400³	
KUIIVIIVI	DRx8 (RC E)	32GB	48GB	NA	3000		
	DRx4 (RC A)	64GB	96GB	128GB		4400	
	DRx4 (RC B) 9x4	NA	NA	NA			
RDIMM 3DS	(4R/8R)x4	2H-128GB	NA	NA	5600 ⁴		
	(RC A)	4H-256GB	INA	NA	3000		

NOTE:

- 1. 1DPC applies to 1SPC or 2SPC implementations (SPC Sockets Per Channel)
- 2. 24Gb 2DPC not POR w/ 24GB and 48GB DIMMs.
- $3.\ DDR5-5600\ RDIMMs$ will be limited to $5600\ MT/s\ 1DPC$ and $4400\ MT/s\ 2DPC$. DDR5-4800 DIMMs will be limited to $4800\ MT/s\ 1DPC$ and $4400\ MT/s\ 2DPC$.
- 4. DDR5-5600 DIMMS are required for 5600 and 5200 1DPC speeds.

3-8-4 Processor and Memory Module Matrix Table

Memory Q'ty				CP	U0							CP	U1			
for each CPU	H0	G0	F0	E0	A0	В0	CO	D0	L0	K0	J0	10	MO	N0	00	P0
1 DIMM								v	v							
2 DIMM	v							v	v							v
4 DIMM	v	v					v	v	v	v					v	v
8 DIMM	v	v	v	v	v	v	v	v	v	v	v	v	v	v	v	v

3-9 Removing and Installing the PCle Card



- Voltages can be present within the server whenever an AC power source is connected. This
 voltage is present even when the main power switch is in the off position. Ensure that the system
 is powered off and all power sources have been disconnected from the server prior to installing a
 PCle card.
- Failure to observe these warnings could result in personal injury or damage to equipment.



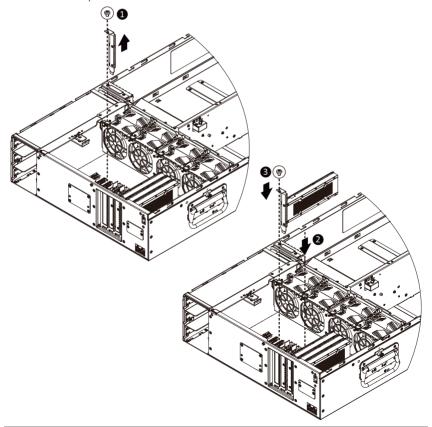
 The PCIe riser assembly does not include a riser card or any cabling as standard. To install a PCIe card, a riser card must be installed.

Follow these instructions to PCI Expansion card:

- 1. Remove the screw securing the riser bracket. Lift up the riser bracket out of system.
- Orient the PCle card with the riser guide slot and push in the direction of the arrow until the PCle card sits in the PCle card connector.

NOTE: Some riser brackets allow for single or multiple PCle cards. Repeat steps 4-5 as necessary.

- Secure the PCle card with the screw.
- 4. Reverse steps 1-3 to install the riser bracket.



3-10 Installing and Removing an M.2 Solid State Drive

Follow these instructions to install an optional M.2 solid state drive (SSD):



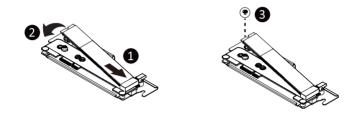
NOTE:

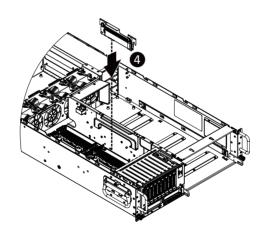
To install/remove the M.2 solid state drive $\,$ use a No. 1 Phillips-head screwdriver with a screw torque of 1.5 \pm 0.2 kgf*cm

- Place the solid state drive into the M.2 module.
- Push down the solid state drive.
- Secure the solid state drive to the module with a single screw.

NOTE: The position of the screw will depend on the size of the SSD. Refer to the second image below for proper placement.

4. Install the solid state drive module into the system.

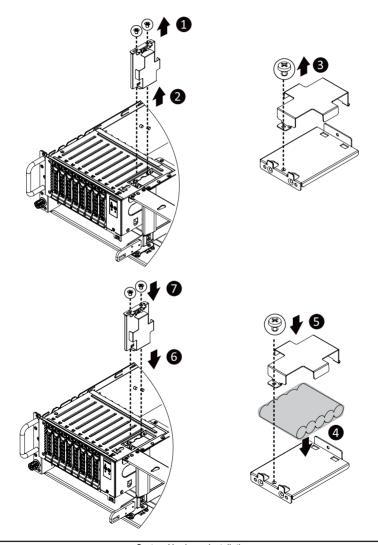




3-11 Removing and Installing the Backup Battery Unit

Follow these instructions to replace the backup battery unit:

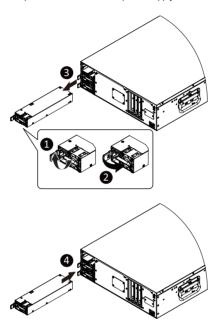
- 1. Remove the two screws securing the battery unit enclosure.
- 2. Remove the battery unit enclosure.
- 3. Remove the battery unit enclosure metal cover.
- 4. Insert the replacement batteries firmly into the enclosure.
- 5. Replace the metal cover and lock it with screw.
- 6. Install the battery unit enclosure into the system and lock it with two screws.



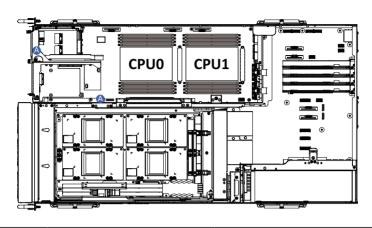
3-12 Removing and Installing the Power Supply

Follow these instructions to replace the power supply:

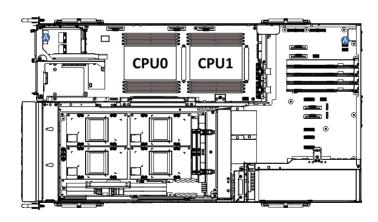
- 1. Flip up and then grasp the power supply handle.
- 2. Press the retaining clip on the right side of the power supply unit in the direction indicated.
- 3. Pull out the power supply unit using the handle.
- Insert the replacement power supply unit firmly into the chassis. Connect the AC power cord to the replacement power supply.
- 5. Repeat steps 1-4 for replacement of the second power supply.



3-13 Cable Routing

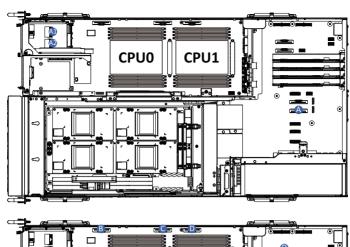


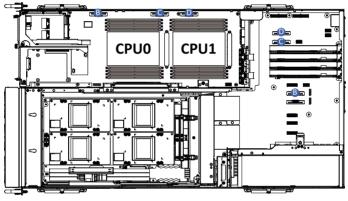
A Front Panel LEDs and Buttons Cable Motherboard: FBTN_IO
Front IO Board: FP_1



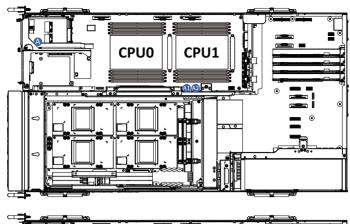
A OCP NCSI Signal Cable

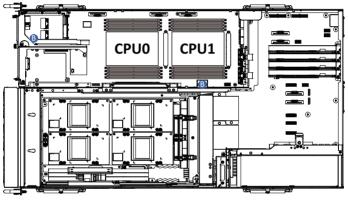
OCP Card: -
PCIe Baseboard: U2_NCSI1



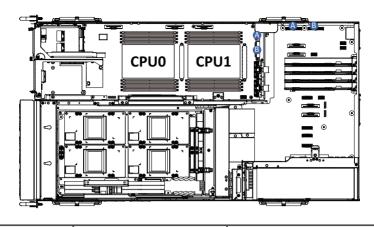


А		OCP Card: A1: MCIO_CD/ A2: MCIO_AB PCIe Baseboard: U2_PEX2
В	CPU0	Motherboard: U2_P01_PE0 PCle Baseboard: U2_PEX1
С	to Baseboard PCIe Signal Cable	Motherboard: U2_P1_PE1 PCle Baseboard: U2_PEX4
D		Motherboard: U2_P1_PE0 PCle Baseboard: U2_PEX3

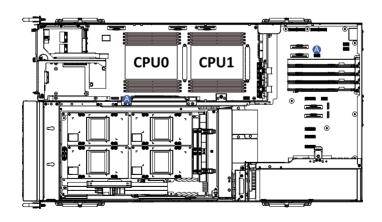




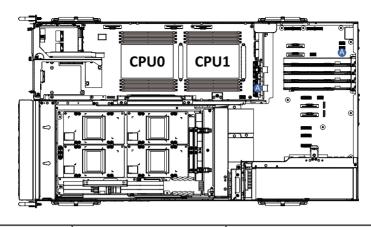
		PCle Riser:		
A	System Front PCle Signal Cable	Motherboard: A1: U2_P1_PE2A A2: U2_P1_PE2B		
В	System Front PCIe Power Cable	PCle Riser:		
	System From Fole Fower Cable	Motherboard: U2_PWR4		



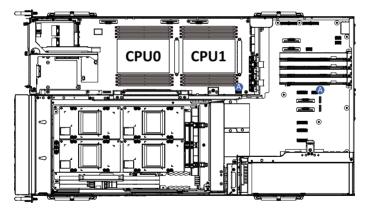
		Motherboard: ATX1	
	System Power Cable	PCIe Baseboard: ATX1	
В	System ower capie	Motherboard: ATX2	
		PCIe Baseboard: ATX1	



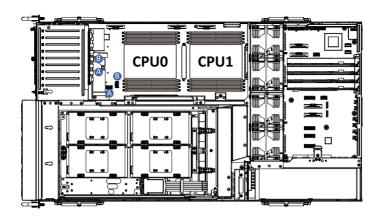
Motherboard: REAR_IO Management LAN Signal Cable PCIe Baseboard: REDSTONE



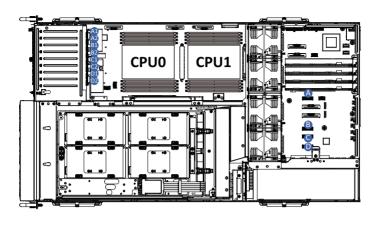
Motherboard: U2_P1_PE3 Motherboard to PCIe Baseboard 16i to 4i Cable PCle Baseboard: U2_1



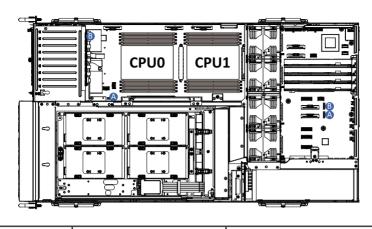
Motherboard: PDB_IO Power On Signal Cable PCIe Baseboard: PDB_IO



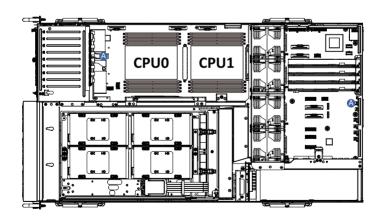
_	A SATA Cable	Motherboard : SL4_SATA0	
^		F/ HDD Board: SL_SAS0	
B SATA Cable		Motherboard : SL4_SATA1	
	SAIA Cable	F/ HDD Board: SL_SAS1	



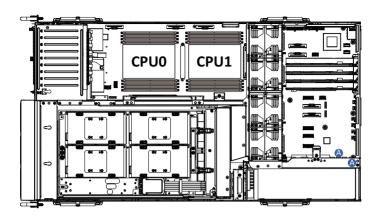
A	NVMe 0-1 Cable	F/ HDD Board: A1: U_2_0 A2: U_2_1	С	NVMe 4-5 Cable	F/ HDD Board: C1: U_2_4 C2: U_2_5	
	Cable	PCIe Baseboard: MCIO1_1		Cable	PCle Baseboard: MClO3_1	
B NVMe 2-3		F/ HDD Board: B1: U_2_2 B2: U_2_3	D	NVMe 6-7	F/ HDD Board: D1: U_2_6 D2: U_2_7	
	Cable	PCIe Baseboard: MCIO2_1		Cable	PCle Baseboard: MClO4_1	



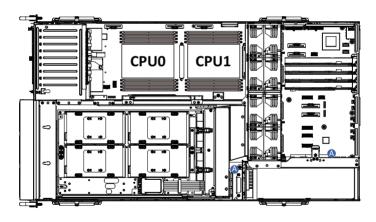
_		Motherboard: BP_1	
^	HDD Backplane Board Signal Cable	PCle Baseboard: BP_1	
В	TIDD Backplane Board Signal Cable	F/ HDD Board: BP_1	
		PCIe Baseboard: BP_SERIES1	



	HDD Backplane Board Power Cable	PCIe Baseboard: HDD_PWR2		
_ ^	TIDD Backplatte Board Fower Cable	F/ HDD Board: ATX1		



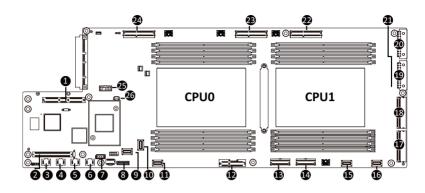
Α Fan Board Signal Cable PCle Baseboard: SMB_CON2



Α PDB Signal Cable PCIe Baseboard: SMB_CON1

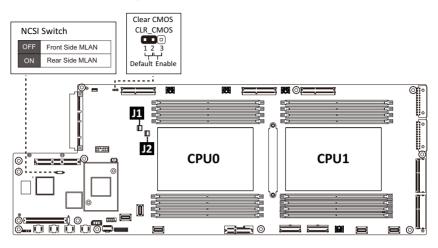
Chapter 4 Motherboard Components 4-1 Motherboard Components

Motherboard Components



1 Proprietary PCIe Slot (Gen 5/x16 slot/GENZ_2) 2 IO Card Slot for IO Board 3 BIOS Flash ROM #2 4 BIOS Flash ROM #1 5 BMC Flash ROM #1 6 BMC Flash ROM #2 7 IPMB Connector 8 HDD Backplane Board Signal Connector 9 SlimLine SAS Connector (SL4_SATA0/SATA#0-#3) 10 SlimLine SAS Connector (SL4_SATA1/SATA#4-#7) 11 SlimLine SAS Connector (REAR_IO/for HGX sideband signal) 12 Proprietary PCIe Slot (Gen 5/ x10 slot/ GENZ_3) 13 MCIO Connector (U2_P1_PE2A/PCIe Gen4) 14 MCIO Connector (U2_P1_PE2B/PCIe Gen4) 15 SlimLine SAS Connector 16 SlimLine SAS Connector (PDB_IO/for power on signal) 17 MCIO Connector (U2_P1_PE3/PCIe Gen4) 18 MCIO Connector (U2_P1_PE3/PCIe Gen4) 19 2 x 12 Pin Power Connector (ATX2) 20 2 x 12 Pin Power Connector (ATX1) 21 VROC Module Connector (U2_P1_PE0/PCIe Gen4) 23 MCIO Connector (U2_P1_PE0/PCIe Gen4) 24 MCIO Connector (U2_P1_PE1/PCIe Gen4) 25 TPM Connector (U2_P1_PE1/PCIe Gen4) 26 System Battery Cable Connector 27 TPM Connector (U2_P1_PE0/PCIe Gen4) 28 TPM Connector (U2_P1_PE0/PCIe Gen4) 29 TPM Connector (U2_P1_PE0/PCIe Gen4) 20 TPM Connector (U2_P1_PE0/PCIe Gen4) 21 FPM Connector (U2_P1_PE0/PCIe Gen4) 22 MCIO Connector (U2_P1_PE0/PCIe Gen4) 23 MCIO Sonnector (U2_P1_PE0/PCIe Gen4) 24 MCIO Connector (U2_P1_PE0/PCIe Gen4) 25 TPM Connector (U2_P1_PE0/PCIe Gen4) 26 System Battery Cable Connector	Item	Description					
3 BIOS Flash ROM #2 4 BIOS Flash ROM #1 5 BMC Flash ROM #1 6 BMC Flash ROM #2 7 IPMB Connector 8 HDD Backplane Board Signal Connector 9 SlimLine SAS Connector (SL4_SATA0/SATA#0-#3) 10 SlimLine SAS Connector (SL4_SATA1/SATA#4-#7) 11 SlimLine SAS Connector (REAR_IO/for HGX sideband signal) 12 Proprietary PCIe Slot (Gen 5/ x10 slot/ GENZ_3) 13 MCIO Connector (U2_P1_PE2A/PCIe Gen4) 14 MCIO Connector (U2_P1_PE2B/PCIe Gen4) 15 SlimLine SAS Connector (PDB_IO/for power on signal) 17 MCIO Connector (U2_P1_PE3/PCIe Gen4) 18 MCIO Connector (U2_P1_PE3/PCIe Gen4) 19 2 x 12 Pin Power Connector (ATX2) 20 2 x 12 Pin Power Connector (ATX1) 21 VROC Module Connector 22 MCIO Connector (U2_P1_PE0/PCIe Gen4) 23 MCIO Connector (U2_P1_PE0/PCIe Gen4) 24 MCIO Connector (U2_P1_PE0/PCIe Gen4) 25 TPM Connector 26 System Battery Cable Connector *NOTE! If UPI is setting at x4 link, the PCIe can link by Gen 4	1	Proprietary PCIe Slot (Gen 5/x16 slot/GENZ_2)					
4 BIOS Flash ROM #1 5 BMC Flash ROM #1 6 BMC Flash ROM #2 7 IPMB Connector 8 HDD Backplane Board Signal Connector 9 SlimLine SAS Connector (SL4_SATA0/SATA#0-#3) 10 SlimLine SAS Connector (SL4_SATA1/SATA#4-#7) 11 SlimLine SAS Connector (REAR_IO/for HGX sideband signal) 12 Proprietary PCle Slot (Gen 5/ x10 slot/ GENZ_3) 13 MCIO Connector (U2_P1_PE2A/PCle Gen4) 14 MCIO Connector (U2_P1_PE2B/PCle Gen4) 15 SlimLine SAS Connector 16 SlimLine SAS Connector (PDB_IO/for power on signal) 17 MCIO Connector (U2_P1_PE3/PCle Gen4)* 18 MCIO Connector (U2_P1_PE4PCle Gen4) 19 2 x 12 Pin Power Connector (ATX2) 20 2 x 12 Pin Power Connector (ATX2) 21 VROC Module Connector 22 MCIO Connector (U2_P1_PE0/PCle Gen4) 23 MCIO Connector (U2_P1_PE1/PCle Gen4) 24 MCIO Connector (U2_P1_PE0/PCle Gen4) 25 TPM Connector 26 System Battery Cable Connector *NOTE! If UPI is setting at x4 link, the PCle can link by Gen 4	2	IO Card Slot for IO Board					
5 BMC Flash ROM #1 6 BMC Flash ROM #2 7 IPMB Connector 8 HDD Backplane Board Signal Connector 9 SlimLine SAS Connector (SL4_SATA0/SATA#0-#3) 10 SlimLine SAS Connector (SL4_SATA1/SATA#4-#7) 11 SlimLine SAS Connector (REAR_IO/for HGX sideband signal) 12 Proprietary PCle Slot (Gen 5/ x10 slot/ GENZ_3) 13 MCIO Connector (U2_P1_PE2A/PCle Gen4) 14 MCIO Connector (U2_P1_PE2B/PCle Gen4) 15 SlimLine SAS Connector 16 SlimLine SAS Connector (PDB_IO/for power on signal) 17 MCIO Connector (U2_P1_PE3/PCle Gen4)* 18 MCIO Connector (U2_P1_PE4PCle Gen4) 19 2 x 12 Pin Power Connector (ATX2) 20 2 x 12 Pin Power Connector (ATX1) 21 VROC Module Connector 22 MCIO Connector (U2_P1_PE0/PCle Gen4) 23 MCIO Connector (U2_P1_PE0/PCle Gen4) 24 MCIO Connector (U2_P1_PE0/PCle Gen4) 25 TPM Connector 26 System Battery Cable Connector *NOTE! If UPI is setting at x4 link, the PCle can link by Gen 4	3	BIOS Flash ROM #2					
6 BMC Flash ROM #2 7 IPMB Connector 8 HDD Backplane Board Signal Connector 9 SlimLine SAS Connector (SL4_SATA0/SATA#0-#3) 10 SlimLine SAS Connector (SL4_SATA1/SATA#4-#7) 11 SlimLine SAS Connector (REAR_IO/for HGX sideband signal) 12 Proprietary PCle Slot (Gen 5/ x10 slot/ GENZ_3) 13 MCIO Connector (U2_P1_PE2A/PCle Gen4) 14 MCIO Connector (U2_P1_PE2B/PCle Gen4) 15 SlimLine SAS Connector 16 SlimLine SAS Connector 16 SlimLine SAS Connector (PDB_IO/for power on signal) 17 MCIO Connector (U2_P1_PE3/PCle Gen4)* 18 MCIO Connector (U2_P1_PE4PCle Gen4) 19 2 x 12 Pin Power Connector (ATX2) 20 2 x 12 Pin Power Connector (ATX1) 21 VROC Module Connector 22 MCIO Connector (U2_P1_PE0/PCle Gen4) 23 MCIO Connector (U2_P1_PE1/PCle Gen4) 24 MCIO Connector (U2_P1_PE0/PCle Gen4) 25 TPM Connector 26 System Battery Cable Connector *NOTE! If UPI is setting at x4 link, the PCle can link by Gen 4	4	BIOS Flash ROM #1					
7 IPMB Connector 8 HDD Backplane Board Signal Connector 9 SlimLine SAS Connector (SL4_SATA0/SATA#0-#3) 10 SlimLine SAS Connector (SL4_SATA1/SATA#4-#7) 11 SlimLine SAS Connector (REAR_IO/for HGX sideband signal) 12 Proprietary PCIe Slot (Gen 5/ x10 slot/ GENZ_3) 13 MCIO Connector (U2_P1_PE2A/PCIe Gen4) 14 MCIO Connector (U2_P1_PE2B/PCIe Gen4) 15 SlimLine SAS Connector 16 SlimLine SAS Connector (PDB_IO/for power on signal) 17 MCIO Connector (U2_P1_PE3/PCIe Gen4)* 18 MCIO Connector (U2_P1_PE4PCIe Gen4) 19 2 x 12 Pin Power Connector (ATX2) 20 2 x 12 Pin Power Connector (ATX1) 21 VROC Module Connector 22 MCIO Connector (U2_P1_PE0/PCIe Gen4) 23 MCIO Connector (U2_P1_PE1/PCIe Gen4) 24 MCIO Connector (U2_P1_PE0/PCIe Gen4) 25 TPM Connector 26 System Battery Cable Connector *NOTE! If UPI is setting at x4 link, the PCIe can link by Gen 4	5	BMC Flash ROM #1					
8 HDD Backplane Board Signal Connector 9 SlimLine SAS Connector (SL4_SATA0/SATA#0-#3) 10 SlimLine SAS Connector (SL4_SATA1/SATA#4-#7) 11 SlimLine SAS Connector (REAR_IO/for HGX sideband signal) 12 Proprietary PCle Slot (Gen 5/ x10 slot/ GENZ_3) 13 MCIO Connector (U2_P1_PE2A/PCle Gen4) 14 MCIO Connector (U2_P1_PE2B/PCle Gen4) 15 SlimLine SAS Connector 16 SlimLine SAS Connector (PDB_IO/for power on signal) 17 MCIO Connector (U2_P1_PE3/PCle Gen4)* 18 MCIO Connector (U2_P1_PE4PCle Gen4) 19 2 x 12 Pin Power Connector (ATX2) 20 2 x 12 Pin Power Connector (ATX1) 21 VROC Module Connector 22 MCIO Connector (U2_P1_PE0/PCle Gen4) 23 MCIO Connector (U2_P1_PE1/PCle Gen4) 24 MCIO Connector (U2_P1_PE0/PCle Gen4) 25 TPM Connector 26 System Battery Cable Connector *NOTE! If UPI is setting at x4 link, the PCle can link by Gen 4	6	BMC Flash ROM #2					
9 SlimLine SAS Connector (SL4_SATA0/SATA#0-#3) 10 SlimLine SAS Connector (SL4_SATA1/SATA#4-#7) 11 SlimLine SAS Connector (REAR_IO/for HGX sideband signal) 12 Proprietary PCle Slot (Gen 5/ x10 slot/ GENZ_3) 13 MCIO Connector (U2_P1_PE2A/PCle Gen4) 14 MCIO Connector (U2_P1_PE2B/PCle Gen4) 15 SlimLine SAS Connector 16 SlimLine SAS Connector (PDB_IO/for power on signal) 17 MCIO Connector (U2_P1_PE3/PCle Gen4)* 18 MCIO Connector (U2_P1_PE4/PCle Gen4) 19 2 x 12 Pin Power Connector (ATX2) 20 2 x 12 Pin Power Connector (ATX1) 21 VROC Module Connector 22 MCIO Connector (U2_P1_PE0/PCle Gen4) 23 MCIO Connector (U2_P1_PE1/PCle Gen4) 24 MCIO Connector (U2_P1_PE1/PCle Gen4) 25 TPM Connector 26 System Battery Cable Connector *NOTE! If UPI is setting at x4 link, the PCle can link by Gen 4	7	IPMB Connector					
10 SlimLine SAS Connector (SL4_SATA1/SATA#4-#7) 11 SlimLine SAS Connector (REAR_IO/for HGX sideband signal) 12 Proprietary PCle Slot (Gen 5/ x10 slot/ GENZ_3) 13 MCIO Connector (U2_P1_PE2A/PCle Gen4) 14 MCIO Connector (U2_P1_PE2B/PCle Gen4) 15 SlimLine SAS Connector 16 SlimLine SAS Connector (PDB_IO/for power on signal) 17 MCIO Connector (U2_P1_PE3/PCle Gen4)* 18 MCIO Connector (U2_P1_PE4PCle Gen4) 19 2 x 12 Pin Power Connector (ATX2) 20 2 x 12 Pin Power Connector (ATX1) 21 VROC Module Connector 22 MCIO Connector (U2_P1_PE0/PCle Gen4) 23 MCIO Connector (U2_P1_PE1/PCle Gen4) 24 MCIO Connector (U2_P1_PE1/PCle Gen4) 25 TPM Connector 26 System Battery Cable Connector *NOTE! If UPI is setting at x4 link, the PCle can link by Gen 4	8	HDD Backplane Board Signal Connector					
11 SlimLine SAS Connector (REAR_IO/for HGX sideband signal) 12 Proprietary PCle Slot (Gen 5/ x10 slot/ GENZ_3) 13 MCIO Connector (U2_P1_PE2A/PCle Gen4) 14 MCIO Connector (U2_P1_PE2B/PCle Gen4) 15 SlimLine SAS Connector 16 SlimLine SAS Connector (PDB_IO/for power on signal) 17 MCIO Connector (U2_P1_PE3/PCle Gen4)* 18 MCIO Connector (U2_P1_PE4PCle Gen4) 19 2 x 12 Pin Power Connector (ATX2) 20 2 x 12 Pin Power Connector (ATX1) 21 VROC Module Connector 22 MCIO Connector (U2_P1_PE0/PCle Gen4) 23 MCIO Connector (U2_P1_PE1/PCle Gen4) 24 MCIO Connector (U2_P1_PE1/PCle Gen4) 25 TPM Connector 26 System Battery Cable Connector *NOTE! If UPI is setting at x4 link, the PCle can link by Gen 4	9	SlimLine SAS Connector (SL4_SATA0/SATA#0-#3)					
12 Proprietary PCIe Slot (Gen 5/ x10 slot/ GENZ_3) 13 MCIO Connector (U2 P1 PE2A/PCIe Gen4) 14 MCIO Connector (U2 P1 PE2B/PCIe Gen4) 15 SlimLine SAS Connector 16 SlimLine SAS Connector (PDB_IO/for power on signal) 17 MCIO Connector (U2_P1_PE3/PCIe Gen4)* 18 MCIO Connector (U2_P1_PE4PCIe Gen4) 19 2 x 12 Pin Power Connector (ATX2) 20 2 x 12 Pin Power Connector (ATX1) 21 VROC Module Connector 22 MCIO Connector (U2_P1_PE0/PCIe Gen4) 23 MCIO Connector (U2_P1_PE1/PCIe Gen4) 24 MCIO Connector (U2_P1_PE0/PCIe Gen4) 25 TPM Connector 26 System Battery Cable Connector *NOTE! If UPI is setting at x4 link, the PCIe can link by Gen 4	10	SlimLine SAS Connector (SL4_SATA1/SATA#4-#7)					
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15 SlimLine SAS Connector 16 SlimLine SAS Connector (PDB_IO/for power on signal) 17 MCIO Connector (U2_P1_PE3/PCIe Gen4)* 18 MCIO Connector (U2_P1_PE4PCIe Gen4) 19 2 x 12 Pin Power Connector (ATX2) 20 2 x 12 Pin Power Connector (ATX1) 21 VROC Module Connector 22 MCIO Connector (U2_P1_PE0/PCIe Gen4) 23 MCIO Connector (U2_P1_PE1/PCIe Gen4) 24 MCIO Connector (U2_P1_PE0/PCIe Gen4) 25 TPM Connector 26 System Battery Cable Connector *NOTE! If UPI is setting at x4 link, the PCIe can link by Gen 4	13	MCIO Connector (U2_P1_PE2A/PCIe Gen4)					
16 SlimLine SAS Connector (PDB_IO/for power on signal) 17 MCIO Connector (U2_P1_PE3/PCIe Gen4)* 18 MCIO Connector (U2_P1_PE4PCIe Gen4) 19 2 x 12 Pin Power Connector (ATX2) 20 2 x 12 Pin Power Connector (ATX1) 21 VROC Module Connector 22 MCIO Connector (U2_P1_PE0/PCIe Gen4) 23 MCIO Connector (U2_P1_PE1/PCIe Gen4) 24 MCIO Connector (U2_P01_PE0/PCIe Gen4) 25 TPM Connector 26 System Battery Cable Connector *NOTE! If UPI is setting at x4 link, the PCIe can link by Gen 4	14	MCIO Connector (U2_P1_PE2B/PCIe Gen4)					
17 MCIO Connector (U2_P1_PE3/PCIe Gen4)* 18 MCIO Connector (U2_P1_PE4PCIe Gen4) 19 2 x 12 Pin Power Connector (ATX2) 20 2 x 12 Pin Power Connector (ATX1) 21 VROC Module Connector 22 MCIO Connector (U2_P1_PE0/PCIe Gen4) 23 MCIO Connector (U2_P1_PE1/PCIe Gen4) 24 MCIO Connector (U2_P01_PE0/PCIe Gen4) 25 TPM Connector 26 System Battery Cable Connector *NOTE! If UPI is setting at x4 link, the PCIe can link by Gen 4	15	SlimLine SAS Connector					
18 MCIO Connector (U2_P1_PE4PCle Gen4) 19 2 x 12 Pin Power Connector (ATX2) 20 2 x 12 Pin Power Connector (ATX1) 21 VROC Module Connector 22 MCIO Connector (U2_P1_PE0/PCle Gen4) 23 MCIO Connector (U2_P1_PE1/PCle Gen4) 24 MCIO Connector (U2_P01_PE0/PCle Gen4) 25 TPM Connector 26 System Battery Cable Connector *NOTE! If UPI is setting at x4 link, the PCle can link by Gen 4	16						
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20 2 x 12 Pin Power Connector (ATX1) 21 VROC Module Connector 22 MCIO Connector (U2 P1 PE0/PCIe Gen4) 23 MCIO Connector (U2 P1 PE1/PCIe Gen4) 24 MCIO Connector (U2 P01 PE0/PCIe Gen4) 25 TPM Connector 26 System Battery Cable Connector *NOTE! If UPI is setting at x4 link, the PCIe can link by Gen 4	18	MCIO Connector (U2_P1_PE4PCle Gen4)					
21 VROC Module Connector 22 MCIO Connector (U2_P1_PE0/PCle Gen4) 23 MCIO Connector (U2_P1_PE1/PCle Gen4) 24 MCIO Connector (U2_P01_PE0/PCle Gen4) 25 TPM Connector 26 System Battery Cable Connector *NOTE! If UPI is setting at x4 link, the PCle can link by Gen 4	_						
22 MCIO Connector (U2_P1_PE0/PCle Gen4) 23 MCIO Connector (U2_P1_PE1/PCle Gen4) 24 MCIO Connector (U2_P01_PE0/PCle Gen4) 25 TPM Connector 26 System Battery Cable Connector *NOTE! If UPI is setting at x4 link, the PCle can link by Gen 4	_	2 x 12 Pin Power Connector (ATX1)					
23 MCIO Connector (U2_P1_PE1/PCIe Gen4) 24 MCIO Connector (U2_P01_PE0/PCIe Gen4) 25 TPM Connector 26 System Battery Cable Connector *NOTE! If UPI is setting at x4 link, the PCIe can link by Gen 4	_						
24 MCIO Connector (U2_P01_PE0/PCIe Gen4) 25 TPM Connector 26 System Battery Cable Connector *NOTE! If UPI is setting at x4 link, the PCIe can link by Gen 4							
25 TPM Connector 26 System Battery Cable Connector *NOTE! If UPI is setting at x4 link, the PCIe can link by Gen 4		\ = = /					
26 System Battery Cable Connector *NOTE! If UPI is setting at x4 link, the PCIe can link by Gen 4							
*NOTE! If UPI is setting at x4 link, the PCIe can link by Gen 4							
If UPI is setting at x4 link, the PCIe can link by Gen 4		- Commenter of the comment of the co					
,	*NOTE	*NOTE!					
If UPI is setting at x3 link, the PCIe can link by Gen 5	If UPI is	If UPI is setting at x4 link, the PCIe can link by Gen 4					
	If UPI is	s setting at x3 link, the PCle can link by Gen 5					

4-2 Jumper Settings

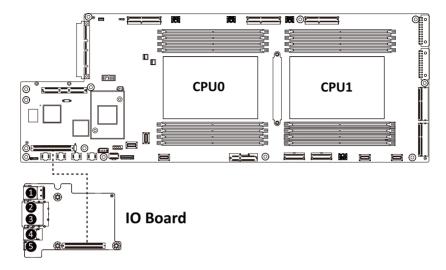


J1		ON	OFF	
1	HSMB_SEL	BIO	S defined	
2	PMBUS_SEL	BIOS defined		
3	S3_MASK	Stop initial power on when BMC is not ready	Normal [Default]	
4	DP_PLD	CPLD debug mode	Normal [Default]	

J2		ON	OFF
1	ME_UPDATE	Force ME update	Normal [Default]
2	BIOS_PWD	Clear supervisor password	Normal [Default]
3	BIOS_RCVR	BIOS recovery mode	Normal [Default]
4	ME_RCVR	ME recovery mode	Normal [Default]

4-3 IO Board

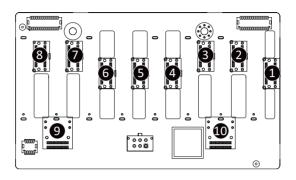
4-3-1 CLBH160



Item	Description
1	10/100/1000 Server Management LAN Port
2	1GbE LAN Port #2
3	1GbE LAN Port #1
4	USB 3.2 Gen1 Port x 2
5	Mini DP Port

4-4 Backplane Board Storage Connector

4-4-1 CBP2081 (Front System Storage Board)



Item	Description
1	SlimLine SAS Connector (SFF-8654 4i/U.2_0)
2	SlimLine SAS Connector (SFF-8654 4i/U.2_1)
3	SlimLine SAS Connector (SFF-8654 4i/U.2_2)
4	SlimLine SAS Connector (SFF-8654 4i/U.2_3)
5	SlimLine SAS Connector (SFF-8654 4i/U.2_4)
6	SlimLine SAS Connector (SFF-8654 4i/U.2_5)
7	SlimLine SAS Connector (SFF-8654 4i/U.2_6)
8	SlimLine SAS Connector (SFF-8654 4i/U.2_7)
9	SlimLine SAS Connector (SFF-8654 4i/SL_SAS1)
10	SlimLine SAS Connector (SFF-8654 4i/SL_SAS0)

Chapter 5 BIOS Setup

BIOS (Basic Input and Output System) records hardware parameters of the system in the EFI on the motherboard. Its major functions include conducting the Power-On Self-Test (POST) during system startup, saving system parameters, loading the operating system etc. The BIOS includes a BIOS Setup program that allows the user to modify basic system configuration settings or to activate certain system features. When the power is turned off, the battery on the motherboard supplies the necessary power to the CMOS to keep the configuration values in the CMOS.

To access the BIOS Setup program, press the key during the POST when the power is turned on.



- BIOS flashing is potentially risky, if you do not encounter any problems when using the current BIOS version, it is recommended that you don't flash the BIOS. To flash the BIOS, do it with caution. Inadequate BIOS flashing may result in system malfunction.
- It is recommended that you not alter the default settings (unless you need to) to prevent system
 instability or other unexpected results. Inadequately altering the settings may result in system's
 failure to boot. If this occurs, try to clear the CMOS values and reset the board to default values.
 (Refer to the Exit section in this chapter or introductions of the battery/clearing CMOS jumper in
 Chapter 1 for how to clear the CMOS values.)

BIOS Setup Program Function Keys

<←><→>	Move the selection bar to select the screen
<↑><↓>	Move the selection bar to select an item
<+>	Increase the numeric value or make changes
<->	Decrease the numeric value or make changes
<enter></enter>	Execute command or enter the submenu
<esc></esc>	Main Menu: Exit the BIOS Setup program
	Submenus: Exit current submenu
<f1></f1>	Show descriptions of general help
<f3></f3>	Restore the previous BIOS settings for the current submenus
<f9></f9>	Load the Optimized BIOS default settings for the current submenus
<f10></f10>	Save all the changes and exit the BIOS Setup program

■ Main

This setup page includes all the items of the standard compatible BIOS.

Advanced

This setup page includes all the items of AMI BIOS special enhanced features. (ex: Auto detect fan and temperature status, automatically configure hard disk parameters.)

■ Chipset

This setup page includes all the submenu options for configuring the functions of the Platform Controller Hub.

■ Server Management

Server additional features enabled/disabled setup menus.

■ Security

Change, set, or disable supervisor and user password. Configuration supervisor password allows you to restrict access to the system and BIOS Setup.

A supervisor password allows you to make changes in BIOS Setup.

A user password only allows you to view the BIOS settings but not to make changes.

■ Boot

This setup page provides items for configuration of the boot sequence.

Save & Exit

Save all the changes made in the BIOS Setup program to the CMOS and exit BIOS Setup. (Pressing <F10> can also carry out this task.)

Abandon all changes and the previous settings remain in effect. Pressing <Y> to the confirmation message will exit BIOS Setup. (Pressing <Esc> can also carry out this task.)

5-1 The Main Menu

Once you enter the BIOS Setup program, the Main Menu (as shown below) appears on the screen. Use arrow keys to move among the items and press <Enter> to accept or enter other sub-menu.

Main Menu Help

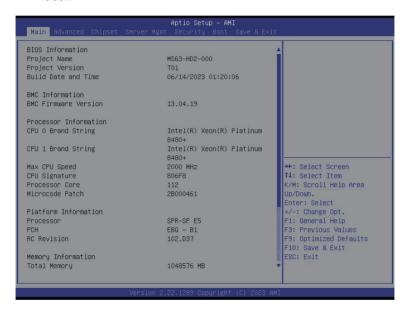
The on-screen description of a highlighted setup option is displayed on the bottom line of the Main Menu.

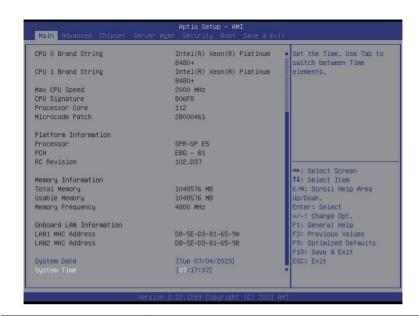
Submenu Help

While in a submenu, press <F1> to display a help screen (General Help) of function keys available for the menu. Press <Esc> to exit the help screen. Help for each item is in the Item Help block on the right side of the submenu.



- When the system is not stable as usual, select the **Restore Defaults** item to set your system to its defaults.
- The BIOS Setup menus described in this chapter are for reference only and may differ by BIOS version.





Parameter	Description
BIOS Information	
Project Name	Displays the project name information.
Project Version	Displays version number of the BIOS setup utility.
Build Date and Time	Displays the date and time when the BIOS setup utility was created.
BMC Information ^(Note1)	
BMC Firmware Version ^(Note1)	Displays BMC firmware version information.
Processor Information	
CPU Brand String/ Max CPU Speed / CPU Signature / Processor Core / Microcode Patch	Displays the technical information for the installed processor(s).
Platform Information	
Processor/ PCH/ RC Revision	Displays the information of the installed processor(s) and PCH.
Memory Information ^(Note2)	
Total Memory	Displays the total memory size of the installed memory.
Usable Memory	Displays the usable memory size of the installed memory.

(Note1) Functions available on selected models.

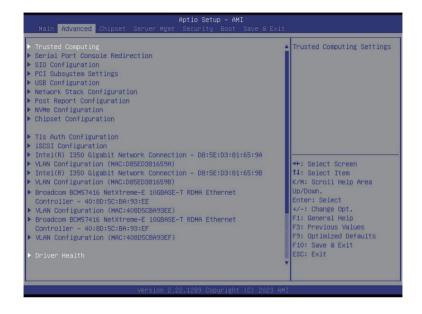
(Note2) This section will display capacity and frequency information of the memory that the customer has installed.

Parameter	Description
Memory Frequency	Displays the frequency information of the installed memory.
Onboard LAN Information ^(Note3)	
LAN# MAC Address	Displays LAN MAC address information.
System Date	Sets the date following the weekday-month-day-year format.
System Time	Sets the system time following the hour-minute-second format.

(Note3) The number of LAN ports listed will depend on the motherboard / system model.

5-2 Advanced Menu

The Advanced Menu displays submenu options for configuring the function of various hardware components. Select a submenu item, then press <Enter> to access the related submenu screen.



5-2-1 Trusted Computing



Parameter	Description		
Configuration			
TPM v1.2 Support	Enable/Disable BIOS support for security device. OS will not show security device. TCG EFI protocol and INT1A interface will not be available. Options available: Disable, Enable. Default setting is Enable .		

5-2-2 Serial Port Console Redirection



Parameter	Description			
COM1 Console Redirection ^(Note)	Console redirection enables the users to manage the system from a remote location. Options available: Enabled, Disabled. Default setting is Disabled .			
COM1 Console Redirection Settings	Press [Enter] to configure advanced items. Please note that this item is configurable when COM1 Console Redirection is set to Enabled. Terminal Type Selects a terminal type to be used for console redirection. Options available: VT100, VT100PLUS, VT-UTF8, ANSI. Default setting is VT100PLUS. Bits per second Selects the transfer rate for console redirection. Options available: 9600, 19200, 38400, 57600, 115200. Default setting is 115200. Data Bits Selects the number of data bits used for console redirection. Options available: 7, 8. Default setting is 8.			

Parameter

Description

Parity

- A parity bit can be sent with the data bits to detect some transmission errors.
- Even: parity bit is 0 if the num of 1's in the data bits is even.
- Odd: parity bit is 0 if num of 1's in the data bits is odd.
- Mark: parity bit is always 1. Space: Parity bit is always 0.
- Mark and Space Parity do not allow for error detection.
- Options available: None, Even, Odd, Mark, Space. Default setting is None.

Stop Bits

- Stop bits indicate the end of a serial data packet. (A start bit indicates the beginning). The standard setting is 1 stop bit.
 Communication with slow devices may require more than 1 stop bit
- Options available: 1, 2. Default setting is 1.

Flow Control

- Flow control can prevent data loss from buffer overflow. When sending data, if the receiving buffers are full, a 'stop' signal can be sent to stop the data flow. Once the buffers are empty, a 'start' signal can be sent to re-start the flow. Hardware flow control uses two wires to send start/stop signals.
- Options available: None, Hardware RTS/CTS. Default setting is None.

VT-UTF8 Combo Key Support

- Enable/Disable the VT-UTF8 Combo Key Support.
- Options available: Enabled, Disabled. Default setting is **Enabled**.

Recorder Mode

- When this mode enabled, only texts will be send. This is to capture Terminal data.
- Options available: Enabled, Disabled. Default setting is **Disabled**.

Resolution 100x31

- Enable/Disable extended terminal resolution.
- Options available: Enabled, Disabled. Default setting is **Enabled**.

Putty KeyPad

- Selects Function Key and KeyPad on Putty.
- Options available: VT100, LINUX, XTERMR6, SC0, ESCN, VT400.
 Default setting is VT100.

COM1 Console Redirection Settings (continued)

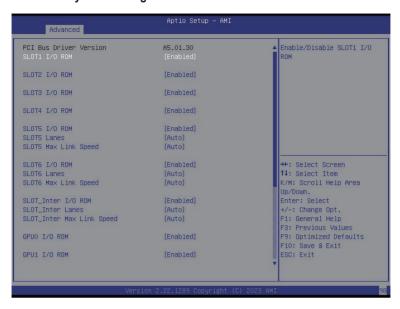
Parameter	Description			
Serial Port for Out-of-Band Management / Windows Emergency Management Services (EMS) Console Redirection ^(Note)	EMS console redirection allows the user to configure Console Redirection Settings to support Out-of-Band Serial Port management. Options available: Enabled, Disabled. Default setting is Disabled .			
Serial Port for Out-of-Band EMS Console Redirection Settings	Press [Enter] to configure advanced items. Please note that this item is configurable when Serial Port for Out-of-Band Management EMS Console Redirection is set to Enabled. ◆ Out-of-Band Mgmt Port − Microsoft Windows Emergency Management Service (EMS) allows for remote management of a Windows Server OS through a serial port. − Default setting is COM1. ◆ Terminal Type EMS − Selects a terminal type to be used for console redirection. − Options available: VT100, VT100PLUS, VT-UTF8, ANSI. Default setting is VT100PLUS. ◆ Bits per second EMS − Selects the transfer rate for console redirection. − Options available: 9600, 19200, 57600, 115200. Default setting is 115200. ◆ Flow Control EMS − Flow control can prevent data loss from buffer overflow. When sending data, if the receiving buffers are full, a 'stop' signal can be sent to stop the data flow. Once the buffers are empty, a 'start' signal can be sent to re-start the flow. Hardware flow control uses two wires to send start/stop signals. − Options available: None, Hardware RTS/CTS, Software Xon/Xoff. Default setting is None.			

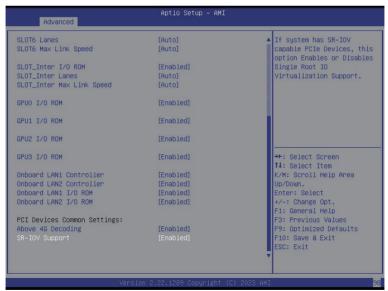
5-2-3 SIO Configuration



Parameter	Description			
AMI SIO Driver Version	Displays the AMI SIO driver version information.			
AMI SIO Driver Version Super IO Chip Logical Device(s) Configuration	Press [Enter] to configure advanced items. Use This Device When set to Enabled allows you to configure the serial port settings. When set to Disabled, displays no configuration for the serial port. Options available: Enabled, Disabled. Default setting is Enabled. Logical Device Settings/Current: Displays the serial port base I/O address and IRQ.			
[*Active*] Serial Port	 Possible: Configures the serial port base I/O address and IRQ. Use Automatic Settings IO=3F8h; IRQ=4; DMA; IO=3F8h; IRQ=4; DMA; IO=2F8h; IRQ=4; DMA; IO=3E8h; IRQ=4; DMA; IO=2E8h; IRQ=4; DMA; Default setting is Use Automatic Settings. 			

5-2-4 PCI Subsystem Settings





Parameter	Description		
PCI Bus Driver Version	Displays the PCI Bus Driver version information.		
SLOT_# I/O ROM ^(Note1)	When enabled, this setting will initialize the device expansion ROM for the related PCI-E slot. Options available: Enabled, Disabled. Default setting is Enabled .		
SLOT_# Lanes ^(Note1)	Change the PCle lanes. Default setting is Auto .		
SLOT_#_Max Link Speed(Note1)	Configure PCIe max link speed. Options available: Auto, Gen1, Gen2, Gen3, Gen4, Gen5. Default setting is Auto .		
M2_# I/O ROM ^(Note2)	When enabled, this setting will initialize the device expansion ROM for the related M.2 slot. Options available: Enabled, Disabled. Default setting is Enabled .		
M2_# Lanes ^(Note2)	Change the M.2 lanes. Default setting is Auto.		
M2_#_Max Link Speed ^(Note2)	Configure M.2 max link speed. Options available: Auto, Gen1, Gen2, Gen3, Gen4, Gen5. Default setting is Auto .		
GPU_# I/O ROM ^(Note3)	When enabled, this setting will initialize the device expansion ROM for the related GPU slot. Options available: Enabled, Disabled. Default setting is Enabled .		
Onboard LAN1/ LAN2 Controller ^(Note4)	Enable/Disable the onboard LAN controller. Options available: Enabled, Disabled. Default setting is Enabled .		
Onboard LAN1/ LAN2 I/O ROM(Note4)	Enable/Disable the onboard LAN devices, and initializes device expansion ROM. Options available: Enabled, Disabled. Default setting is Enabled .		
PCI Devices Common Settings			
Above 4G Decoding	Enable/Disable memory mapped I/O to 4GB or greater address space (Above 4G Decoding). Options available: Enabled, Disabled. Default setting is Enabled .		
SR-IOV Support	If the system has SR-IOV capable PCIe devices, this item Enable/Disable Single Root IO Virtualization Support. Options available: Enabled, Disabled. Default setting is Enabled .		

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⁽Note2) This section is dependent on the available M.2 Slot.

⁽Note3) This section is dependent on the available GPU Slot.

⁽Note4) This section is dependent on the available LAN controller.

5-2-5 USB Configuration



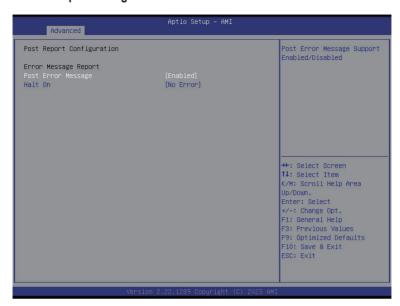
Parameter	Description
USB Configuration	
USB Devices:	Displays the USB devices connected to the system.
XHCI Hand-off	Enable/Disable the XHCI (USB 3.0) Hand-off support. Options available: Enabled, Disabled. Default setting is Enabled .
USB Mass Storage Driver Support ^(Note)	Enable/Disable the USB Mass Storage Driver Support. Options available: Enabled, Disabled. Default setting is Enabled .
Port 60/64 Emulation	Enables the I/O port 60h/64h emulation support. This should be enabled for the complete USB Keyboard Legacy support for non-USB aware OSes. Options available: Enabled, Disabled. Default setting is Enabled .

5-2-6 Network Stack Configuration



Parameter	Description
Network Stack	Enable/Disable the UEFI network stack. Options available: Enabled, Disabled. Default setting is Enabled .
Ipv4 PXE Support	Enable/Disable the Ipv4 PXE feature. Options available: Enabled, Disabled. Default setting is Enabled .
Ipv4 HTTP Support	Enable/Disable the Ipv4 HTTP feature. Options available: Enabled, Disabled. Default setting is Disabled .
Ipv6 PXE Support	Enable/Disable the Ipv6 PXE feature. Options available: Enabled, Disabled. Default setting is Disabled .
Ipv6 HTTP Support	Enable/Disable the Ipv6 HTTP feature. Options available: Enabled, Disabled. Default setting is Disabled .
PXE boot wait time	Wait time in seconds to press ESC key to abort the PXE boot. Press the <+> / <-> keys to increase or decrease the desired values.
Media detect count	Number of times the presence of media will be checked. Press the <+> / <-> keys to increase or decrease the desired values.

5-2-7 Post Report Configuration



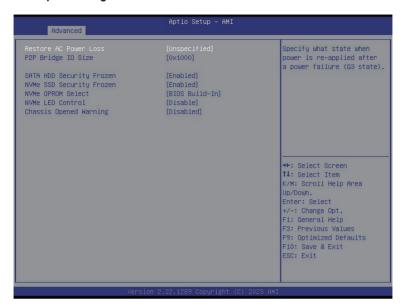
Parameter	Description
Post Report Configuration	
Error Message Report	
Post Error Message	Enable/Disable the POST Error Message support. Options available: Enabled, Disabled. Default setting is Enabled .
Halt On	Options available: No Error, All Error. Default setting is No Error .

5-2-8 NVMe Configuration



Parameter	Description
NVMe Configuration	Displays the NVMe devices connected to the system.
NVMe OPROM Select	Options available: BIOS Build-In, NVMe Device. Default setting is BIOS Build-In .

5-2-9 Chipset Configuration



Parameter	Description
Restore on AC Power Loss ^(Note1)	Defines the power state to resume to after a system shutdown that is due to an interruption in AC power. When set to Last State, the system will return to the active power state prior to shutdown. When set to Power Off, the system remains off after power shutdown. Options available: Last State, Power Off, Power On, Unspecified. The default setting depends on the BMC setting.
P2P Bridge IO Size	Specifies P2P Bridge IO aligned to the size. Options available: 0x100, 0x150, 0x1000. Default setting is 0x1000 .
SATA HDD Security Frozen	Enable/Disable this item to send freeze lock command to SATA HDD. Options available: Enabled, Disabled. Default setting is Enabled .
NVMe SSD Security Frozen	Attempt to send freeze lock command to NVMe SSDs during boot. Options available: Enabled, Disabled. Default setting is Enabled .
Chassis Opened Warning ^(Note2)	Enable/Disable the chassis intrusion alert function. Options available: Enabled, Disabled, Clear. Default setting is Disabled.

- (Note1) When the power policy is controlled by BMC, please wait for 15-20 seconds for BMC to save the last power state.
- (Note2) Functions available on selected models.

5-2-10 Tls Auth Configuration



Parameter	Description
	Press [Enter] for configuration of advanced items.
	Enroll Cert
	- Press [Enter] to enroll a certificate
	Enroll Cert Using File
Conver CA Configuration	Cert GUID
Server CA Configuration	Input digit character in 1111111-2222-3333-4444-1234567890ab
	format.
	 Commit Changes and Exit
	 Discard Changes and Exit
	Delete Cert
Client Cert Configuration	Press [Enter] for configuration of advanced items.

5-2-11 iSCSI Configuration



Parameter	Description
Attempt Priority	Press [Enter] configure advanced items. Attempt Priority Use arrow keys to select the attempt, then press +/- keys to move the attempt up/down in the attempt order list. Commit Changes and Exit
Host iSCSI Configuration	Press [Enter] to configure advanced items. • iSCSI Initiator Name - Only IQN format is accepted. Range: from 4 to 223 • Add an Attempt • Delete Attempts • Change Attempt Order

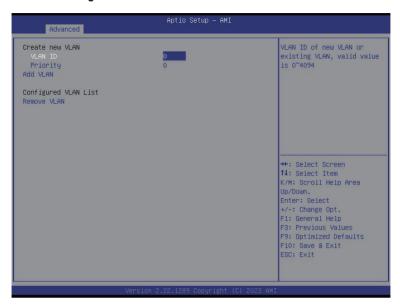
5-2-12 Intel(R) i350 Gigabit Network Connection





Parameter	Description
NIC Configuration	Press [Enter] to configure advanced items. Link Speed Allows for automatic link speed adjustment. Options available: Auto Negotiated, 10 Mbps Half, 10 Mbps Full, 100 Mbps Half, 100 Mbps Full. Default setting is Auto Negotiated. Wake On LAN Enables power on of the system via LAN. Note that configuring Wake on LAN in the operating system does not change the value of this setting, but does override the behavior of Wake on LAN in OS controlled power states. Options available: Enabled, Disabled. Default setting is Enabled.
Blink LEDs	Identifies the physical network port by blinking the associated LED. Press the numeric keys to adjust desired values (up to 15 seconds).
UEFI Driver	Displays the technical specifications for the Network Interface Controller.
Adapter PBA	Displays the technical specifications for the Network Interface Controller.
Device Name	Displays the technical specifications for the Network Interface Controller.
Chip Type	Displays the technical specifications for the Network Interface Controller.
PCI Device ID	Displays the technical specifications for the Network Interface Controller.
PCI Address	Displays the technical specifications for the Network Interface Controller.
Link Status	Displays the technical specifications for the Network Interface Controller.
MAC Address	Displays the technical specifications for the Network Interface Controller.
Virtual MAC Address	Displays the technical specifications for the Network Interface Controller.

5-2-13 VLAN Configuration



Parameter	Description
Enter Configuration Menu	Press [Enter] to configure advanced items. Create new VLAN VLAN ID Sets VLAN ID for a new VLAN or an existing VLAN. Press the <+> / <-> keys to increase or decrease the desired values. The valid range is from 0 to 4094. Priority Sets 802.1Q Priority for a new VLAN or an existing VLAN. Press the <+> / <-> keys to increase or decrease the desired values. The valid range is from 0 to 7. Add VLAN Press [Enter] to create a new VLAN or update an existing VLAN. Configured VLAN List Remove VLAN Press [Enter] to remove an existing VLAN.

5-2-14 Broadcom(R) BCM57416 NetXtreme-E 10GBASE-T Network Connection

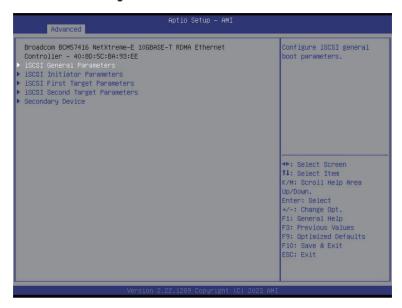


Parameter	Description
Firmware Image Menu	Press [Enter] to view firmware image information.
	· · · · · · · · · · · · · · · · · · ·
	 Enable/Disable Energy Efficient Ethernet operation.
	 Options available: Disabled, Enabled. Default setting is Disabled. Operational Link Speed
	 Configures the link speed setting to be used as the default link speed for the selected port.
	 Options available: AutoNeg. Default setting is AutoNeg.

Parameter	Description
Device Configuration Menu (continued)	 Support RDMA Enable/Disable RDMA support for this port. Options available: Disabled, Enabled. Default setting is Disabled. DCB Protocol Enable/Disable DCB protocol. Options available: Disabled, Enabled (IEEE only), CEE (only), Both (IEEE preferred with fallback to CEE). Default setting is Disabled. LLDP nearest bridge Enable/Disable LLDP nearest bridge state. Options available: Disabled, Enabled. Default setting is Enabled. Default EVB Mode Configures the default Edge Virtual Bridging mode. Options available: VEB, VEPA, None. Default setting is VEB. Enable PME Capability Enable/Disable PME Capability support. Options available: Disabled, Enabled. Default setting is Enabled. Flow Offload Options available: Enabled, Disabled. Default setting is Disabled. Live Firmware Upgrade Options available: Enabled, Disabled. Default setting is Disabled. Adapter Error Recovery Options available: Enabled, Disabled. Default setting is Disabled.
MBA Configuration Menu	Press [Enter] to configure advanced items. Option ROM Enable/Disable Boot Option ROM. Options available: Disabled, Enabled. Default setting is Enabled. Legacy Boot Protocol Selects non-UEFI Boot Protocol: Preboot Execution Environment (PXE)/iSCSI. Options available: PXE, iSCSI, NONE. Default setting is PXE. Boot Strap Type Selects the boot strap type. Options available: Auto Detect, BBS, Int 18h, Int 19h. Default setting is Auto Detect. Hide Setup Prompt Configures whether the Setup Prompt is displayed during ROM initialization. Options available: Disabled, Enabled. Default setting is Disabled. Setup Key Stroke Configures key strokes to invoke the configuration menu. Options available: Ctrl-S, Ctrl-B. Default setting is Ctrl-S. Banner Message Timeout Selects the timeout value. (0 defaults to 4 seconds, 15 is no delay, 1-14 is timeout value in seconds) Default setting is 5.

Parameter	Description
MBA Configuration Menu (continued)	 Pre-boot Wake On LAN Configures Pre-boot Wake on LAN (WOL). Options available: Disabled, Enabled. Default setting is Enabled. VLAN Mode Configures the virtual LAN (VLAN) mode. Options available: Disabled, Enabled. Default setting is Disabled. VLAN ID Configures the VLAN ID (14094). This item is available only when VLAN Mode is Enabled. Boot Retry Count Selects the number of boot retries. Options available: No Retry, 1 Retry, 2 Retries, 3 Retries, 4 Retries, 5 Retries, 6 Retries, Indefinite Retries. Default setting is No Retry.
iSCSI Boot Configuration Menu	Press [Enter] to configure advanced items.
Blink LEDs	Identifies the physical network port by blinking the associated LED. Press the numeric keys to adjust desired values.
Link Status	Specifies the link status of the port.
Physical Link Speed	Displays the technical specifications for the Network Interface Controller.
Chip Type	Displays the technical specifications for the Network Interface Controller.
PCI Device ID	Displays the technical specifications for the Network Interface Controller.
Bus:Device:Function	Displays the technical specifications for the Network Interface Controller.
Permanent MAC Address	Displays the MAC address of the Ethernet controller.
Virtual MAC Address	Displays the virtual MAC address of the Ethernet controller.
Restore Defaults	Resets the adapter to factory defaults.

5-2-14-1 iSCSI Boot Configuration Menu



	Description
iSCSI General Parameters	Press [Enter] to configure advanced items. ◆ TCP/IP Parameters via DHCP — Acquires TCP/IP Parameters via DHCP. — Options available: Disabled, Enabled. Default setting is Enabled. ◆ IP Autoconfiguration — Auto-configures the IP configuration. ◆ iSCSI Parameters via DHCP — Acquires iSCSI Parameters via DHCP. — Options available: Disabled, Enabled. Default setting is Disabled. ◆ CHAP Authentication — Enable/Disable the CHAP authentication. — Options available: Disabled, Enabled. Default setting is Disabled. ◆ Boot to iSCSI Target — Enable/Disable booting to iSCSI target after log-on. — Options available: Disabled, Enabled, One Time Disabled. Default setting is Enabled. ◆ DHCP Vendor ID — Configures the DHCP vendor ID (up to 32 characters long). ◆ Link Up Delay Time

Parameter	Description
iSCSI General Parameters (continued)	 Use TCP Timestamp Enable/Disable the TCP timestamp. Options available: Disabled, Enabled. Default setting is Disabled. Target as First HDD Enable/Disable target appears as first hard disk drive (HDD) in the system. Options available: Disabled, Enabled. Default setting is Disabled. LUN Busy Retry Count Configures the number of retries in 2 second intervals when LUN is busy (0-60). Default setting is 0. IP Version Displays the IP version supported. Modifying this parameter will reset all IP-related fields. Options available: IPv4, IPv6. Disabled. Default setting is IPv4.
iSCSI Initiator Parameters	Press [Enter] to configure advanced items. IP Address Configures the initiator IP address. Subnet Mask Configures the IP subnet mask. Default Gateway Configures the default gateway IP address. Primary DNS Configures the primary DNS IP address. Secondary DNS Configures the secondary DNS IP address. Secondary DNS Configures the iSCSI name. CHAP ID Configures the Challenge-Handshake Authentication Protocol (CHAP) ID (up to 128 characters in length). CHAP Secret Configure the Challenge-Handshake Authentication Protocol (CHAP) Secret (12 to 16 characters in length).
iSCSI First/Second Target Parameters	Press [Enter] to configure advanced items. Connect Enable/Disable the target establishment. Options available: Disabled, Enabled. Default setting is Disabled . IP Address Configures the Target IP address. TCP Port Configures the Target TCP port number (1-65535).

Parameter	Description
iSCSI First/Second Target Parameters (continued)	Boot LUN Configures the Target boot LUN number (0-255). ISCSI Name Configures the iSCSI name. CHAP ID Configures the Challenge-Handshake Authentication Protocol (CHAP) ID (up to 128 characters in length). CHAP Secret Configure the Challenge-Handshake Authentication Protocol (CHAP) Secret (12 to 16 characters in length).
Secondary Device	Press [Enter] to configure advanced items. Secondary Device Inputs the secondary device MAC address. Use Independent Target Portal Use Independent target portal when multipath I/O is enabled. Options available: Disabled, Enabled. Default setting is Disabled. Use Independent Target Name Use Independent target name when multipath I/O is enabled. Options available: Disabled, Enabled. Default setting is Disabled.

5-2-15 VLAN Configuration



Parameter	Description
Enter Configuration Menu	Press [Enter] to configure advanced items. Create new VLAN VLAN ID Sets VLAN ID for a new VLAN or an existing VLAN. Press the <+> / <-> keys to increase or decrease the desired values. The valid range is from 0 to 4094. Priority Sets 802.1Q Priority for a new VLAN or an existing VLAN. Press the <+> / <-> keys to increase or decrease the desired values. The valid range is from 0 to 7. Add VLAN Press [Enter] to create a new VLAN or update an existing VLAN. Configured VLAN List Remove VLAN Press [Enter] to remove an existing VLAN.

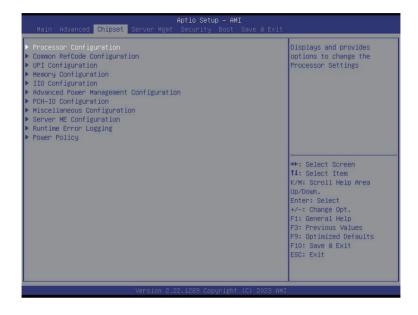
5-2-16 Driver Health



Parameter	Description
Driver Health	Displays driver health status of the devices/controllers if installed

5-3 Chipset Menu

Chipset Setup menu displays submenu options for configuring the function of Platform Controller Hub(PCH). Select a submenu item, then press <Enter> to access the related submenu screen.



5-3-1 Processor Configuration



Chipset Processor O Version Intel(R) Xeon(R) Platin ▲ Displays and provides um 8480+ option to change the Processor CFR Settings Intel(R) Xeon(R) Platin Processor 1 Version um 8480+ Enable LP [Global] [ALL LPs] Hardware Prefetcher [Enable] L2 RFO Prefetch Disable [Disable] Adjacent Cache Prefetch [Enable] DCU Streamer Prefetcher [Enable] DCU IP Prefetcher [Enable] Extended APIC [Enable] Enable Intel(R) TXT [Disable] [Enable] →+: Select Screen [Disable] ↑↓: Select Item Enable SMX AES-NI [Enable] K/M: Scroll Help Area Up/Down. Debug Consent [Disable] Enter: Select +/-: Change Opt. TME, TME-MT, TDX F1: General Help Memory Encryption (TME) [Disabled] F3: Previous Values F9: Optimized Defaults SGX setup configuration preconditions for enabling were NOT met. Please check TME. MirrorMode or Extended APIC settings. F10: Save & Exit

Parameter	Description
Processor Configuration	
Pre-Socket Configuration	Press [Enter] to configure advanced items. CPU Socket 0/1 Configuration Core Disable Bitmap(Hex) Number of Cores to enable. 0 means all cores. FFFFFFF means to disable all cores. The maximum value depends on the number of CPUs available. Press the numeric keys to adjust desired values.
Processor Socket / Processor ID / Processor Die Type / Processor Frequency / Processor Max Ratio / Processor Min Ratio / Microcode Revision / L1 Cache RAM(Per Core) / L2 Cache RAM(Per Core) / L3 Cache RAM(Per Package) / Processor # Version	Displays the technical specifications for the installed processor(s).
Enable LP [Global]	Enables Logical processor (Software Method to Enable/Disable Logical Processor threads). Options available: ALL LPs, Single LP. Default setting is ALL LPs .
Hardware Prefetcher	Select whether to enable the speculative prefetch unit of the processor. Options available: Enable, Disable. Default setting is Enable .
L2 RF0 Prefetch Disable	Options available: Enable, Disable. Default setting is Disable .
Adjacent Cache Prefetch	When enabled, cache lines are fetched in pairs. When disabled, only the required cache line is fetched. Options available: Enable, Disable. Default setting is Enable .
DCU Streamer Prefetcher	Enable/Disable DCU streamer prefetcher. Options available: Enable, Disable. Default setting is Enable .
DCU IP Prefetcher	Enable/Disable DCU IP Prefetcher. Options available: Enable, Disable. Default setting is Enable .
Extended APIC	Enable/Disable extended APIC support. Note: The VT-d will be enabled automatically when x2APIC is enabled. Options available: Enable, Disable. Default setting is Enable.
Enable Intel(R) TXT	Enable/Disable the Intel Trusted Execution Technology support function. Options available: Enable, Disable. Default setting is Disable .
VMX	Enable/Disable the Vanderpool Technology. This will take effect after rebooting the system. Options available: Enable, Disable. Default setting is Enable .
Enable SMX	Enable/Disable the Safer Mode Extensions (SMX) support function. Options available: Enable, Disable. Default setting is Disable .
AES-NI	Enable/Disable the AES-NI support. Options available: Enable, Disable. Default setting is Enable .
Debug Consent	Options available: Enable, Disable. Default setting is Disable .

Parameter	Description
Memory Encryption (TME)(Note)	Enable/Disable memory encryption (TME). Options available: Enabled, Disabled. Default setting is Disabled .
Total Memory Encryption Multi-Tenant (TME-MT)	Options available: Enabled, Disabled. Default setting is Disabled .
Processor CFR Configuration	Press [Enter] to configure advanced items. Provision S3M CFR Options available: Disable, Enable. Default setting is Enable. Manual Commit S3M FW CFR Options available: Disable, Enable, Auto. Default setting is Auto. Provision PUcode CFR Options available: Disable, Enable. Default setting is Enable. Manual Commit PUcode CFR Options available: Enable, Disable. Default setting is Enable. Socket0 CFR Revision Info Displays CFR Revision information of the socket.

5-3-2 Common RefCode Configuration



Parameter	Description
Common RefCode Configuration	
Virtual Numa	Divide physical NUMA nodes into evenly sized virtual NUMA nodes in ACPI table. This may improve Windows performance on CPUs with more than 64 logical processors. Options available: Enable, Disable. Default setting is Disable .

5-3-3 UPI Configuration



Parameter	Description
UPI General Configuration	Press [Enter] to configure advanced items. UPI Status Press [Enter] to view the Uncore status. Link Frequency Select Selects the UPI link frequency. Options available: 12.8GT/s, 14.4GT/s, 16.0GT/s, Auto, Use Per Link Setting. Default setting is Auto. SNC Enable/Disable Sub NUMA Cluster function. Options available: Auto, Disable, Enable SNC2 (2-clusters), Enable SNC4 (4-clusters). Default setting is Auto. Stale AtoS Enable/Disable Stale A to S directory optimization. Options available: Disable, Enable, Auto. Default setting is Auto. LLC dead line alloc Enable/Disable fill dead lines in LLC. Options available: Disable, Enable, Auto. Default setting is Enable. MMIO High Base Options available: 56T, 40T, 32T, 24T, 16T, 4T, 2T, 1T, 512G, 3584T. Default setting is 32T.

Parameter	De	scription
	*	MMIO High Granularity Size
		 Selects the allocation size used to assign mmioh resources.
UPI General Configuration		- Options available: 1G, 4G, 16G, 64G, 256G, 1024G. Default setting is
(continued)		64G.
	•	Clock Modulation Enabled
		 Options available: Disable, Enable, Auto. Default setting is Auto.

5-3-4 Memory Configuration



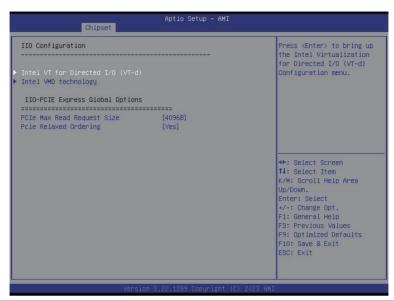
Parameter	Description
Integrated Memory Controller (iMC)	
	When set to Enable, the system enforces Plan Of Record restrictions
Enforce DDR Memory Frequency POR	for DDR frequency programming.
	Options available: POR, Disable. Default setting is POR .
	Configures the maximum memory frequency. If Enforce POR is
Memory Frequency	disabled, user will be able to run at higher frequencies than the
Memory r requericy	memory support (limited by processor support).
	Default setting is Auto .
	Enables the detecting and enabling of ADR (Asynchronous DRAM
Enable ADR	Refresh) function.
	Options available: Enable, Disable. Default setting is Enable .
Lagrany ADD Mada	Enable/Disable the Legacy ADR Mode.
Legacy ADR Mode	Options available: Enable, Disable, Auto. Default setting is Auto .
Minimum Custom Mamony Cina	Configures the minimum memory size.
Minimum System Memory Size	Options available: 2GB, 4GB, 6GB, 8GB. Default setting is 2GB .
	Specifies the Data Save Mode for ADR. Batterybacked or Type 01
ADR Data Save Mode	NVDIMM.
ADR Data Save Mode	Options available: Disable, Batterybacked DIMMs, NVDIMMs, Copy
	to Flash. Default setting is NVDIMMs .
Assert ADR on Reset	Enable/Disable Assert ADR on Reset.
ASSETT ADIT OII NESEL	Options available: Enabled, Disabled. Default setting is Disabled .

Parameter	Description
A 1 ADD OF	Enable/Disable Assert ADR on S5.
Assert ADR on S5	Options available: Enabled, Disabled. Default setting is Disabled .
Cot Momony Timing	Auto is the detected SPD value and use it, otherwise use BIOS Build-in.
Get Memory Timing	Options available: Auto, BIOS Build-in. Default setting is BIOS Build-in .
Memory Topology	Press [Enter] to view memory topology with DIMM population information.
Memory Map ^(Note)	Press [Enter] to configure advanced items. Volatile Memory Mode Selects 1LM or 2LM mode for volatile memory.
	 Options available: 1LM, 2LM. Default setting is 2LM.
Memory RAS Configuration	Press [Enter] to configure advanced items. Mirror Mode (Note) Mirror Mode will set entire 1LM memory in system to be mirrored, consequently reducing the memory capacity by half. Enables the Mirror Mode will disable the XPT Prefetch. Options available: Disabled, Full Mirror Mode, Partial Mirror Mode. Default setting is Disabled. Partial Mirror 1 Size (GB) Selects multiplier of 1GB for the size of the SAD to be created. Correctable Error Threshold Correctable Error Threshold (0x01-0x7fff) used for sparing, and leaky bucket. Press the <+> / <-> keys to increase or decrease the desired values. Trigger SW Error Threshold(Note) Enable/Disable Sparing trigger SW Error Match Threshold. Options available: Disabled, Enabled. Default setting is Disabled. SW Per Bank Threshold SW Per Bank Threshold (1-0x7FFF) used for DDR bank level error. Press the <+> / <-> keys to increase or decrease the desired values. SW Correctable Error Time Window SW Correctable Error Time Window SW Correctable Error Time window based interface in hour (0-24). Press the <+> / <-> keys to increase or decrease the desired values. Leaky bucket time window based interface Enable/Disable leaky bucket time window based interface. Options available: Disabled, Enabled. Default setting is Disabled.

(Note) Advanced items prompt when HBM CPU is installed.

Parameter	Description
Memory RAS Configuration (continued)	 Leaky bucket time window based interface Hour Leaky bucket time window based interface hour used for DDR (0-24). Press the <+> / <-> keys to increase or decrease the desired values. Leaky bucket time window based interface Minute Leaky bucket time window based interface minute used for DDR (0-60). Press the <+> / <-> keys to increase or decrease the desired values. Leaky bucket low bit Configures leaky bucket low bit (0x1 - 0x29). Press the <+> / <-> keys to increase or decrease the desired values. Leaky bucket high bit Configures leaky bucket high bit (0x1 - 0x29). Press the <+> / <-> keys to increase or decrease the desired values. ADDDC Sparing^(Note) Enable/Disable ADDDC Sparing. Options available: Disabled, Enabled. Default setting is Disabled. Enable ADDDC Error Injection Options available: Disabled, Enable at End of POST. Default setting is Enable at End of POST. Patrol Scrub Interval
	 Selects the number of hours (1-24) required to complete full scrub. A value of zero means auto. DDR5 ECS Options available: Disabled, Enabled, Enable ECS with Result Collection. Default setting is Enabled.

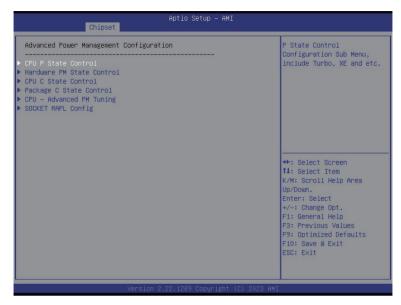
5-3-5 IIO Configuration



Parameter	Description
IIO Configuration	
Intel® VT for Directed I/O (VT-d)	Press [Enter] to configure advanced items. ◆ Intel® VT for Directed I/O - Enable/Disable the Intel VT for Directed I/O (VT-d) support function by reporting the I/O device assignment to VMM through DMAR ACPI Tables. - Options available: Enable, Disable. Default setting is Enable. ◆ ACS Control - Enable: Programs ACS only to Chipset PCle Root Ports Bridges. - Disable: Programs ACS to all PCle bridges. - Default setting is Enable. ◆ Cache Allocation - Options available: Enable, Disable. Default setting is Enable. ◆ Opt-Out Illegal MSI Mitigation - Enable/Disable Opt-Out Illegal 0xFEE Platform Mitigation. - Options available: Disable, Enable. Default setting is Disable. ◆ DMA Control Opt-In Flag - Enable/Disable DMA_CTRL_PLATFORM_OPT_IN_FLAG in DMAR table in ACPI. Not compatible with Direct Device Assignment (DDA). - Options available: Enable, Disable. Default setting is Disable.

Parameter	Description
	 Interrupt Remapping Enable/Disable the interrupt remapping support function. Options available: Auto, Enable, Disable. Default setting is Auto x2APIC Opt Out Options available: Enable, Disable. Default setting is Disable. Pre-boot DMA Protection Options available: Enable, Disable. Default setting is Disable.
Intel® VMD technology	Press [Enter] to configure advanced items. Intel® VMD Configuration Enable/Disable Intel® VMD technology. Options available: Enable, Disable. Default setting is Disable . Intel® VMD for Non-Hotplug NVMe ^(Note) Enable/Disable Intel® VMD for Non-Hotplug NVMe. Options available: Enable, Disable. Default setting is Disable .

5-3-6 Advanced Power Management Configuration



Parameter	Description
CPU P State Control	Press [Enter] to configure advanced items. ◆ SpeedStep (Pstates) - Conventional Intel SpeedStep Technology switches both voltage and frequency in tandem between high and low levels in response to processor load. - Options available: Enable, Disable. Default setting is Enable . ◆ Turbo Mode - When this item is enabled, the processor will automatically ramp up the clock speed of 1-2 of its processing cores to improve its performance. When this item is disabled, the processor will not overclock any of its core. - Options available: Enable, Disable. Default setting is Enable .
Hardware PM State Control	Press [Enter] to configure advanced items. ◆ Hardware P-States - When this item is disabled, the processor hardware chooses a P-state based on OS Request (Legacy P-States). - In Native mode, the processor hardware chooses a P-state based on OS guidance. - In Out of Band mode, the processor hardware autonomously chooses a P-state (with no OS guidance). - Options available: Disable, Native Mode, Out of Band Mode, Native Mode with No Legacy Support. Default setting is Native Mode.

Parameter	Description
CPU C State Control	Press [Enter] to configure advanced items. Enable Monitor MWAIT Allows Monitor and MWAIT instructions. Options available: Disable, Enable, Auto. Default setting is Auto. CPU C6 Report Enable/Disable CPU C6(ACPI C3) report to OS. Options available: Disable, Enable, Auto. Default setting is Auto. Enhanced Halt State (C1E) Core C1E auto promotion control. Takes effect after reboot. Options available: Enable, Disable. Default setting is Enable.
Package C State Control	Press [Enter] to configure advanced items. Package C State Configures the state for the C-State package limit. Options available: C0/C1 state, C2 state, C6(non Retention) state, C6(Retention) state, No Limit, Auto. Default setting is Auto.
CPU - Advanced PM Tuning	Press [Enter] to configure advanced items. ◆ Energy Perf BIAS − Press [Enter] to configure advanced items. » Power Performance Tuning • Options available: OS Controls EPB, BIOS Controls EPB, PECI Controls EPB. Default setting is OS Controls EPB . » Energy_PERF_BIAS_CFG mode ^(Note) • Options available: Performance, Balanced Performance, Balanced Power, Power. Default setting is Balanced Performance.
SOCKET RAPL Config	Press [Enter] to configure advanced items. ◆ PL1 Power Limit − Press the <+> / <-> keys to increase or decrease the desired values. ◆ PL1 Timer Window − Configure PL1 Timer Window. ◆ PL2 Power Limit − Press the <+> / <-> keys to increase or decrease the desired values. ◆ PL2 Timer Window − Configure PL1 Timer Window.

(Note)

5-3-7 PCH Configuration



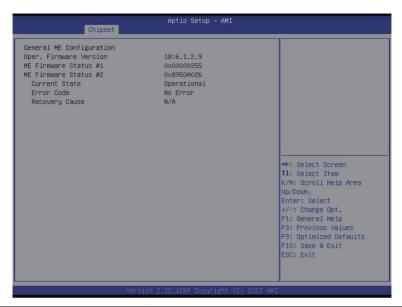
Parameter	Description
SATA And RST Configuration (continued)	 Port 0/1/2/3/4/5/6/7 Enable/Disable Port 0/1/2/3/4/5/6/7 device. Options available: Enabled, Disabled. Default setting is Enabled. Hot Plug (for Port 0/1/2/3/4/5/6/7) Enable/Disable HDD Hot-Plug function. Options available: Enabled, Disabled. Default setting is Enabled. Spin Up Device (for Port 0/1/2/3/4/5/6/7) On an edge detect from 0 to 1, the PCH starts a COM reset initialization to the device. Options available: Enabled, Disabled. Default setting is Disabled.

5-3-8 Miscellaneous Configuration



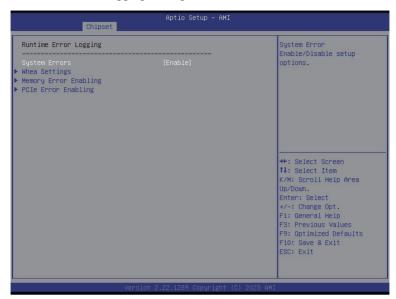
Parameter	Description
Miscellaneous Configuration	
	Selects the active video type.
Active Video	Options available: Auto, Onboard Device, PCIE Device, Specific PCIE
	Device. Default setting is Auto.
External SSC - CK440	Enables Spread spectrum - only affects external clock generator.
	Options available: SSC Off, SSC = -0.3%, SSC = -0.5%, Hardware.
	Default setting is SSC Off.

5-3-9 Server ME Configuration



Parameter	Description
General ME Configuration	
Oper. Firmware Version	Displays the operational firmware version.
ME Firmware Status #1/#2	Displays ME Firmware status information.
Current State	Displays ME Firmware current status information.
Error Code	Displays ME Firmware status error code.
Recovery Cause	Displays ME Firmware recovery cause.

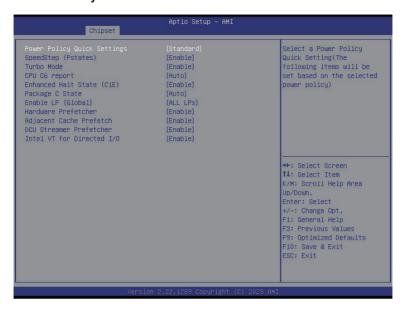
5-3-10 Runtime Error Logging Settings



Parameter	Description
Runtime Error Logging	
System Errors	Enable/Disable system error logging function. Options available: Enable, Disable. Default setting is Enable .
S/W Error Injection Support	Enable/Disable software injection error logging function. Options available: Enable, Disable. Default setting is Disable .
Whea Settings	Press [Enter] to configure advanced items. • WHEA (Windows Hardware Error Architecture) Support - Enable/Disable WHEA Support. - Options available: Enable, Disable. Default setting is Enable.
Memory Error Enabling	Press [Enter] to configure advanced items. ◆ Memory Corrected Error - Enable/Disable Memory Corrected Error. - Options available: Enable, Disable. Default setting is Enable. ◆ Uncorrected Error disable Memory - Enable/Disable the Memory that triggers Uncorrected Error. - Options available: Enable, Disable. Default setting is Disable.

Parameter	Description
PCle Error Enabling	Press [Enter] to configure advanced items. PCIE Error Enable/Disable PCIE error. Options available: Enable, Disable. Default setting is Disable. Uncorrected Error ^(Note) Enables and escalates Uncorrectable/Recoverable Errors to error pins. Options available: Enable, Disable. Default setting is Enable. Fatal Error Enable ^(Note) Enables and escalates Fatal Errors to error pins. Options available: Enable, Disable. Default setting is Enable. Assert NMI on SERR ^(Note) Enable/Disable BIOS generates a non-maskable interrupt (NMI) and logs an error when a system error (SERR) occurs. Options available: Enabled, Disabled. Default setting is Enabled. Assert NMI on PERR ^(Note) Enable/Disable BIOS generates a non-maskable interrupt (NMI) and logs an error when a processor bus parity error (PERR) occurs. Options available: Enabled, Disabled. Default setting is Enabled.

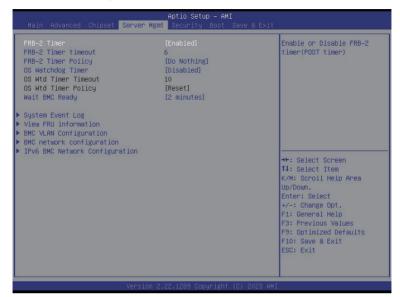
5-3-11 Power Policy



Parameter	Description
Power Policy Quick Settings	Selects a Power Policy Quick Setting.
	Options available: Standard, Best Performance, Energy Efficient. Default
	setting is Standard .
	Conventional Intel SpeedStep Technology switches both voltage and
ChoodCton (Datatas)	frequency in tandem between high and low levels in response to processor
SpeedStep (Pstates)	load.
	Options available: Enable, Disable. Default setting is Enable .
	When this item is enabled, the processor will automatically ramp up the
Turbo Mode	clock speed of 1-2 of its processing cores to improve its performance.
Turbo Mode	When this item is disabled, the processor will not overclock any of its core.
	Options available: Enable, Disable. Default setting is Enable .
	Enable/Disable the BIOS to enable the report from the CPU C6 state (ACPI
CPU C6 report	C3) to the OS.
	Options available: Disable, Enable, Auto. Default setting is Auto .
Enhanced Halt State (C1E)	Enable/Disable the C1E support for lower power consumption. Takes effect
	after reboot.
	Options available: Enable, Disable. Default setting is Enable .
Package C State	Configures the C-State package limit.
	Options available: C0/C1 state, C2 state, C6(non Retention) state,
	C6(Retention) state, No Limit, Auto. Default setting is Auto .

Parameter	Description
	Enables Logical processor (Software Method to Enable/Disable Logical
Enable LP [Global]	Processor threads).
	Options available: ALL LPs, Single LP. Default setting is ALL LPs.
Hardware Prefetcher	Options available: Enable, Disable. Default setting is Enable .
Adjacent Cache Prefetch	Options available: Enable, Disable. Default setting is Enable .
DCU Streamer Prefetcher	Options available: Enable, Disable. Default setting is Enable .
	Enable/Disable the Intel VT for Directed I/O (VT-d) support function by
Intel® VT for Directed I/O	reporting the I/O device assignment to VMM through DMAR ACPI Tables.
	Options available: Enable, Disable. Default setting is Enable.

5-4 Server Management Menu



Parameter	Description	
FRB-2 Timer	Enable/Disable FRB-2 timer (POST timer). Options available: Enabled, Disabled. Default setting is Enabled .	
FRB-2 Timer ^(Note1) timeout	Configures the FRB2 Timer timeout. The value is between 1 to 30 minutes. Default setting is 6 minutes .	
FRB-2 Timer Policy ^(Note1)	Configures the FRB2 Timer policy. Options available: Do Nothing, Reset, Power Down, Power Cycle. Default setting is Do Nothing .	
OS Watchdog Timer	Enable/Disable OS Watchdog Timer function. Options available: Enabled, Disabled. Default setting is Disabled .	
OS Wtd Timer Timeout ^(Note2)	Configures OS Watchdog Timer. The value is between 1 to 30 minutes. Default setting is 10 minutes .	
OS Wtd Timer Policy ^(Note2)	Configure OS Watchdog Timer Policy. Options available: Reset, Do Nothing, Power Down, Power Cycle. Default setting is Reset .	
Wait BMC Ready	POST wait BMC ready and reboot system. Options available: Disabled, 2 minutes, 4 minutes, 6 minutes. Default setting is 2 minutes .	

(Note1) This item is configurable when FRB-2 Timer is set to Enabled.

(Note2) This item is configurable when OS Watchdog Timer is set to Enabled.

Parameter	Description
System Event Log	Press [Enter] to configure advanced items.
View FRU Information	Press [Enter] to view the FRU information.
BMC VLAN Configuration	Press [Enter] to configure advanced items.
BMC network Configuration	Press [Enter] to configure advanced items.
IPv6 BMC Network Configuration	Press [Enter] to configure advanced items.

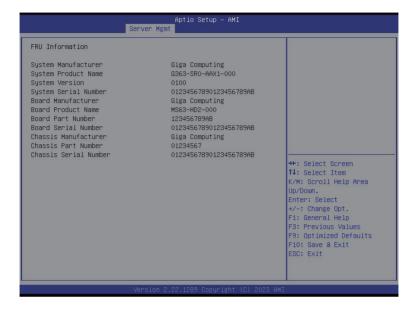
5-4-1 System Event Log



Parameter	Description
Enabling / Disabling Options	
SEL Components	Change this item to enable or disable all features of System Event Logging during boot. Options available: Enabled, Disabled. Default setting is Enabled .
Erasing Settings	
Erase SEL	Choose options for erasing SEL. Options available: No, Yes, On next reset, Yes, On every reset. Default setting is No .
When SEL is Full	Choose options for reactions to a full SEL. Options available: Do Nothing, Erase Immediately, Delete Oldest Record. Default setting is Do Nothing .
Custom EFI Logging Options	
Log EFI Status Codes	Enable/Disable the logging of EFI Status Codes (if not already converted to legacy). Options available: Disabled, Both, Error code, Progress code. Default setting is Error code.

5-4-2 View FRU Information

The FRU page is a simple display page for basic system ID information, as well as System product information. Items on this window are non-configurable.



5-4-3 BMC VLAN Configuration



Parameter	Description
BMC VLAN Configuration	
BMC VLAN ID	Select to configure BMC VLAN ID. The valid range is from 0 to 4094. When set to 0, BMC VLAN ID will be disabled.
BMC VLAN Priority	Select to configure BMC VLAN Priority. The valid range is from 0 to 7. When BMC VLAN ID is set to 0, BMC VLAN Priority will not be selected.

5-4-4 BMC Network Configuration



Parameter	Description
BMC network configuration	
Select NCSI and Dedicated LAN	Options available: Do Nothing, Model1(Dedicated), Model2(NCSI), Mode3(Failover). Default setting is Do Nothing .
Lan Channel 1	
Configuration Address source	Selects to configure LAN channel parameters statically or dynamically (DHCP). Options available: Unspecified, Static, DynamicBmcDhcp. Default setting is DynamicBmcDhcp .
Station IP address	Displays IP Address information.
Subnet mask	Displays Subnet Mask information. Please note that the IP address must be in three digitals, for example, 192.168.000.001.
Router IP address	Displays the Router IP Address information.
Station MAC address	Displays the MAC Address information.
Real-time get BMC network address	Press [Enter] will set LAN mode and Address source and then get IP, Subnet, Gateway and MAC address.

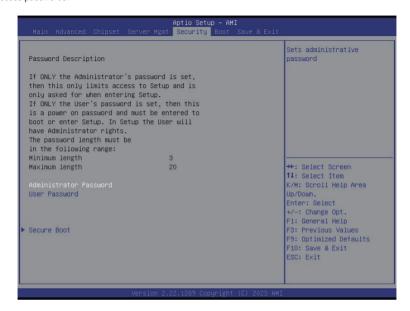
5-4-5 IPv6 BMC Network Configuration



Parameter	Description
IPv6 BMC network configuration	
IPv6 BMC Lan Channel 1	
IPv6 BMC Lan Option	Enable/Disable IPv6 BMC LAN channel function. When this item is disabled, the system will not modify any BMC network during BIOS phase. Options available: Unspecified, Disable, Enable. Default setting is Enable.
IPv6 BMC Lan IP Address Source	Selects to configure LAN channel parameters statically or dynamically (by BIOS or BMC). Options available: Unspecified, Static, Dynamic-Obtained by BMC running DHCP. Default setting is Dynamic-Obtained by BMC running DHCP .
IPv6 BMC Lan IP Address/ Prefix Length	Check if the IPv6 BMC LAN IP address matches those displayed on the screen.

5-5 Security Menu

The Security menu allows you to safeguard and protect the system from unauthorized use by setting up access passwords.



There are two types of passwords that you can set:

· Administrator Password

Entering this password will allow the user to access and change all settings in the Setup Utility.

User Password

Entering this password will restrict a user's access to the Setup menus. To enable or disable this field, a Administrator Password must first be set. A user can only access and modify the System Time, System Date, and Set User Password fields.

Parameter	Description
Administrator Password	Press [Enter] to configure the administrator password.
User Password	Press [Enter] to configure the user password.
Secure Boot	Press [Enter] to configure advanced items.

5-5-1 Secure Boot

The Secure Boot feature is applicable if supported by your Operating System.

If your Operating System is not supporting Secure Boot, the system will hang when starting the Operating System.



Parameter	Description	
System Mode	Displays if the system is in User mode or Setup mode.	
Secure Boot	Enable/ Disable the Secure Boot function. Options available: Enabled, Disabled. Default setting is Disabled .	
Secure Boot Mode ^(Note)	Secure Boot requires all the applications that are running during the booting process to be pre-signed with valid digital certificates. This way, the system knows all files being loaded before the Operating System loads to the login screen have not been tampered with. When set to Standard, it will automatically load the Secure Boot keys form the BIOS databases. When set to Custom, you can customize the Secure Boot settings and manually load its keys from the BIOS database. Options available: Standard, Custom. Default setting is Standard .	
Restore Factory Keys	Forces the system to user mode and installs factory default Secure Boot key database.	
Reset To Setup Mode	Reset the system to Setup Mode.	

(Note) Advanced items prompt when this item is set to Custom.

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Description

Press [Enter] to configure advanced items.

Please note that this item is configurable when Secure Boot Mode is set to Custom.

- Factory Key Provision
 - Allows to provision factory default Secure Boot keys when system is in Setup Mode.
 - Options available: Enabled, Disabled. Default setting is **Disabled**.
- Restore Factory Keys
 - Installs all factory default keys. It will force the system in User Mode.
 - Options available: Yes, No.
- Reset To Setup Mode
 - Reset the system to Setup Mode.
 - Options available: Yes, No.
- Enroll Efi Image
 - Press [Enter] to enroll SHA256 hash of the binary into Authorized Signature Database (db).
- Export Secure Boot variables
 - Copy NVRAM content of Secure Boot variables to files in a root folder on a file system device.

Expert Key Management

- Secure Boot variable
 - Displays the current status of the variables used for secure boot.
- Platform Key (PK)
 - Displays the current status of the Platform Key (PK).
 - Press [Enter] to configure a new PK.
 - Options available: Update.
- Key Exchange Keys (KEK)
 - Displays the current status of the Key Exchange Key Database (KEK).
 - Press [Enter] to configure a new KEK or load additional KEK from storage devices.
 - Options available: Update, Append.
- Authorized Signatures (DB)
 - Displays the current status of the Authorized Signature Database.
 - Press [Enter] to configure a new DB or load additional DB from storage devices.
 - Options available: Update, Append.
- Forbidden Signatures (DBX)
 - Displays the current status of the Forbidden Signature Database.
 - Press [Enter] to configure a new dbx or load additional dbx from storage devices.
 - Options available: Update, Append.

Parameter	Description	
Key Management (continued)	 Authorized TimeStamps (DBT) Displays the current status of the Authorized TimeStamps Database. Press [Enter] to configure a new DBT or load additional DBT from storage devices. Options available: Update, Append. OsRecovery Signatures Displays the current status of the OsRecovery Signature Database. Press [Enter] to configure a new OsRecovery Signature or load additional OsRecovery Signature from storage devices. Options available: Update, Append. 	

5-6 Boot Menu

The Boot menu allows you to set the drive priority during system boot-up. BIOS setup will display an error message if the legacy drive(s) specified is not bootable.



Parameter	Description
Boot Configuration	
Setup Prompt Timeout	Number of seconds to wait for setup activation key. 65535 (0xFFFF) means indefinite waiting. Press the numeric keys to input the desired values.
Bootup NumLock State	Enable/Disable the Bootup NumLock function. Options available: On, Off. Default setting is On .
Quiet Boot	Enable/Disable showing the logo during POST. Options available: Enabled, Disabled. Default setting is Enabled .
Endless Retry Boot	Options available: Disable, Enable. Default setting is Disable .
Setup Flash	Press [Enter] to run setup flash.
Dump full Setup Data	Press [Enter] to dump full setup data to file.
Dump non-default Setup Data	Press [Enter] to dump non-default setup data to file.
Restore Setup Data	Press [Enter] to restore setup data from file.

Parameter	Description	
FIXED BOOT ORDER Priorities		
Boot Option #1 / #2 / #3 / #4 / #5	Press [Enter] to configure the boot order priority. By default, the server searches for boot devices in the following sequence: 1. Hard drive. 2. CD-COM/DVD drive. 3. USB device. 4. Network. 5. UEFI.	
UEFI Network Drive BBS Priorities	Press [Enter] to configure the boot priority.	
UEFI Application Boot Priorities	Press [Enter] to configure the boot priority.	

5-7 Save & Exit Menu

The Save & Exit menu displays the various options to quit from the BIOS setup. Highlight any of the exit options then press <Enter>.



Parameter	Description
Save Options	
Save and Exit	Saves changes made and closes the BIOS setup. Options available: Yes, No.
Discard changes and exit	Discards changes made and exits the BIOS setup. Options available: Yes, No.
Save Changes and Reset	Restarts the system after saving the changes made. Options available: Yes, No.
Discard Changes and Reset	Restarts the system without saving any changes. Options available: Yes, No.
Save Changes	Saves changes done so far to any of the setup options. Options available: Yes, No.
Discard Changes	Discards changes made and closes the BIOS setup. Options available: Yes, No.
Default Options	

Parameter	Description
Restore Defaults	Loads the default settings for all BIOS setup parameters. Setup Defaults are quite demanding in terms of resources consumption. If you are using low-speed memory chips or other kinds of low-performance components and you choose to load these settings, the system might not function properly. Options available: Yes, No.
Save the User Default Values	Saves the changes made as the user default settings. Options available: Yes, No.
Restore the User Default Values	Loads the user default settings for all BIOS setup parameters. Options available: Yes, No.
Boot Device Priority	Press [Enter] to configure the device as the boot-up drive.
Launch EFI Shell	Attempts to Launch EFI Shell application (Shell.efi) from one of the available file system devices.

5-8 BIOS Recovery

The system has an embedded recovery technique. In the event that the BIOS becomes corrupt the boot block can be used to restore the BIOS to a working state. To restore your BIOS, please follow the instructions listed below:

Recovery Instruction:

- 1. Copy the XXX.rom to USB diskette.
- 2. Setting BIOS Recovery jump to enabled status.
- 3. Boot into BIOS recovery.
- 4. Run Proceed with flash update.
- 5. BIOS updated.



