GIGABYTE[™] XV24-SX0-AAJ1

NVIDIA MGX™ Server - Intel® Xeon® 6 Processors - 2U DP 4 x PCIe Gen5 GPUs

User Manual

Rev. 1.0

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Documentation Classifications

In order to assist in the use of this product, Giga Computing provides the following types of documentation:

- User Manual: detailed information & steps about the installation, configuration and use of this
 product (e.g. motherboard, server barebones), covering hardware and BIOS.
- User Guide: detailed information about the installation & use of an add-on hardware or software component (e.g. BMC firmware, rail-kit) compatible with this product.
- Quick Installation Guide: a short guide with visual diagrams that you can reference easily for installation purposes of this product (e.g. motherboard, server barebones).

Please see the support section of the online product page to check the current availability of these documents

For More Information

For related product specifications, the latest firmware and software, and other information please visit our website at http://www.gioabyte.com/Enterprise

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For further technical assistance, please contact your GIGABYTE representative or visit https://esupport.gigabyte.com/ to create a new support ticket

For any general sales or marketing enquiries, you may also message GIGABYTE server directly by email: server.qrp@qiqabyte.com

Conventions

The following conventions are used in this user's guide:

| | NOTE! Gives bits and pieces of additional information related to the current topic. |
|---|---|
| ⚠ | CAUTION! Gives precautionary measures to avoid possible hardware or software problems. |
| A | WARNING! Alerts you to any damage that might result from doing or not doing specific actions. |

Server Warnings and Cautions

Before installing a server, be sure that you understand the following warnings and cautions.



WARNING

To reduce the risk of electric shock or damage to the equipment:

- Do not disable the power cord grounding plug. The grounding plug is an important safety feature.
- Plug the power cord into a grounded (earthed) electrical outlet that is easily accessible at all times.
- Unplug all the power cords from the power supplies to disconnect power to the equipment.





- · Shock Hazard! Disconnect all power supply cords before servicing.
- Do not route the power cord where it can be walked on or pinched by items placed against it. Pay particular
 attention to the plug, electrical outlet, and the point where the cord extends from the server.



WARNING!

To reduce the risk of personal injury from hot surfaces, allow the drives and the internal system components to cool before touching them.



WARNING

This server is equipped with high speed fans. Keep away from hazardous moving fan blades during servicing.



WARNING!

This equipment is intended to be used in Restricted Access Area. The access can only be gained by Skilled person. Only authorized by well trained professional person can access the restrict access location.



CAUTION!

- Do not operate the server for long periods with the access panel open or removed. Operating the server in this manner results in improper airflow and improper cooling that can lead to thermal damage.
- · Danger of explosion if battery is incorrectly replaced.
- · Replace only with the same or equivalent type recommended by the manufacturer.
- Dispose of used batteries according to the manufacturer's instructions.

Electrostatic Discharge (ESD) CAUTION!

ESD CAN DAMAGE DRIVES, BOARDS, AND OTHER PARTS. WE RECOMMEND THAT YOU PERFORM ALL PROCEDURES AT AN ESD WORKSTATION. IF ONE IS NOT AVAILABLE, PROVIDE SOME ESD PROTECTION BY WEARING AN ANTI-STATIC WRIST STRAP ATTACHED TO CHASSIS GROUND -- ANY UNPAINTED METAL SURFACE -- ON YOUR SERVER WHEN HANDLING PARTS.

Always handle boards carefully. They can be extremely sensitive to ESD. Hold boards only by their edges without any component and pin touching. After removing a board from its protective wrapper or from the system, place the board component side up on a grounded, static free surface. Use a conductive foam pad if available but not the board wrapper. Do not slide board over any surface.

System power on/off: To remove power from system, you must remove the system from rack. Make sure the system is removed from the rack before opening the chassis, adding, or removing any non hot-plug components.

Hazardous conditions, devices and cables: Hazardous electrical conditions may be present on power, telephone, and communication cables. Turn off the system and disconnect the cables attached to the system before servicing it. Otherwise, personal injury or equipment damage can result.

Electrostatic discharge (ESD) and ESD protection: ESD can damage drives, boards, and other parts. We recommend that you perform all procedures in this chapter only at an ESD workstation. If one is not available, provide some ESD protection by wearing an antistatic wrist strap attached to chassis ground (any unpainted metal surface on the server) when handling parts.

ESD and handling boards: Always handle boards carefully. They can be extremely sensitive to electrostatic discharge (ESD). Hold boards only by their edges. After removing a board from its protective wrapper or from the system, place the board component side up on a grounded, static free surface. Use a conductive foam pad if available but not the board wrapper. Do not slide board over any surface.

Installing or removing jumpers: A jumper is a small plastic encased conductor that slips over two jumper pins. Some jumpers have a small tab on top that can be gripped with fingertips or with a pair of fine needle nosed pliers. If the jumpers do not have such a tab, take care when using needle nosed pliers to remove or install a jumper; grip the narrow sides of the jumper with the pliers, never the wide sides. Gripping the wide sides can dam-age the contacts inside the jumper, causing intermittent problems with the function con-trolled by that jumper. Take care to grip with, but not squeeze, the pliers or other tool used to remove a jumper, or the pins on the board may bend or break.



Risk of explosion if battery is replaced incorrectly or with an incorrect type. Replace the battery only with the same or equivalent type recommended by the manufacturer. Dispose of used batteries according to the manufacturer's instructions

CAUTION

- "Warning Stability hazard"
- "The slide-rail may tip over causing serious personal injury"
- Before extending the rack to its installation position, read the installation instructions.
- Do not put any load on the slide-rail mounted equipment in the installation position.
- Do not leave the slide-rail mounted equipment in the installation position.



WARNING!

- This equipment is intended to be used in Restrict Access Location. The access can only be gained by Skilled person. Only authorized by well trained professional person can access the restrict access location.
- This equipment is not intended for use by children.



WARNING

- The equipment should only be repaired, maintained or replaced by skilled personnel.

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Chapter 1 Hardware Installation

1-1 Installation Precautions

The motherboard/system contain numerous delicate electronic circuits and components which can become damaged as a result of electrostatic discharge (ESD). Prior to installation, carefully read the service guide and follow these procedures:

- Prior to installation, do not remove or break motherboard S/N (Serial Number) sticker or warranty sticker provided by your dealer. These stickers are required for warranty validation.
- Always remove the AC power by unplugging the power cord from the power outlet before installing or removing the motherboard or other hardware components.
- When connecting hardware components to the internal connectors on the motherboard, make sure they are connected tightly and securely.
- · When handling the motherboard, avoid touching any metal leads or connectors.
- It is best to wear an electrostatic discharge (ESD) wrist strap when handling electronic components such as a motherboard, CPU or memory. If you do not have an ESD wrist strap, keep your hands dry and first touch a metal object to eliminate static electricity.
- Prior to installing the motherboard, please have it on top of an antistatic pad or within an
 electrostatic shielding container.
- Before unplugging the power supply cable from the motherboard, make sure the power supply has been turned off.
- Before turning on the power, make sure the power supply voltage has been set according to the local voltage standard.
- Before using the product, please verify that all cables and power connectors of your hardware components are connected.
- To prevent damage to the motherboard, do not allow screws to come in contact with the motherboard circuit or its components.
- Make sure there are no leftover screws or metal components placed on the motherboard or within the computer casing.
- Do not place the computer system on an uneven surface.
- · Do not place the computer system in a high-temperature environment.
- Turning on the computer power during the installation process can lead to damage to system components as well as physical harm to the user.
- If you are uncertain about any installation steps or have a problem related to the use of the product, please consult a certified computer technician.

1-2 Product Specifications



NOTE:

We reserve the right to make any changes to the product specifications and product-related information without prior notice.

| Dimensions | ◆ 2U |
|-------------|--|
| | |
| (WxHxD, mm) | • 438mm (W) x 87mm (H) x 900mm (D) |
| Motherboard | ◆ MSX4-MG0 |
| CPU CPU | ◆ Intel® Xeon® 6 Processors: |
| | - Intel® Xeon® 6700-Series Processors |
| | - Intel® Xeon® 6500-Series Processors |
| | |
| | Dual processor, TDP up to 350W |
| | [Note] If only 1 CPU is installed, some PCIe or memory functions might be unavailable. |
| Socket | 2 x LGA 4710 |
| Socket | |
| | Socket E2 |
| Chipset | System on Chip |
| Memory | ◆ 32 x DIMM slots |
| | DDR5 memory supported |
| | 8-Channel memory per processor |
| | MRDIMM supported [1] |
| | RDIMM: Up to 6400 MT/s (1DPC), 5200 MT/s (2DPC) |
| | MRDIMM: Up to 8000 MT/s |
| LAN | Front (I/O board - CFPX141): |
| | |
| | - Support NCSI function |
| | ◆ 1 x 10/100/1000 Mbps Management LAN |
| Video | Integrated in ASPEED® AST2600 |
| Video | - 1 x Mini-DP |
| Storage | Front hot-swap: |
| Otorage | |
| | - (2 x NVMe from CPU_0, 2 x NVMe from CPU_1) |
| | (2 × 1 × 110 110 110 110 110 110 110 110 1 |
| | Internal M.2: |
| | • 2 x M.2 (2280/22110), PCIe Gen5 x2, from CPU_0 |
| | |
| | Storage card is required to support SATA and SAS drives. |
| SAS | Require SAS add-in cards |
| | ., |



- Require RAID add-in cards
- Onboard VROC key header



Expansion Slot

- PCIe Cable x 7:
 - 2 x FHFL x16 (Gen5 x16), from CPU 0, for GPUs
 - 2 x FHFL x16 (Gen5 x16), from CPU 1, for GPUs
 - 1 x FHFL x16 (Gen5 x16), from CPU 0, for DPUs
 - 1 x FHFL x16 (Gen5 x16), from CPU 0, for NICs
 - 1 x FHFL x16 (Gen5 x16), from CPU_1, for NICs

[Note] The system supports 4 x NVIDIA H200 NVL PCIe GPUs at 25°C ambient, arranged as two 2-GPU sets, each with a 2-way NVLink bridge. Please contact our sales representatives for more details.

[Note] The system is only validated with a uniform GPU model.

Front I/O

- 2 x USB 3.2 Gen1 ports (Type-A)
- 1 x Power button with LED
- 1 x ID button with I FD
- 1 x NMI button
- 1 x Reset button
- 1 x Storage activity LED
- 1 x System status LED



Rear I/O

- 2 x USB 3.2 Gen1 ports (Type-A)
- 1 x Mini-DP
- 2 x RJ45 ports
- 1 x MLAN port
- 1 x ID LED



Backplane I/O

Speed and bandwidth: PCle Gen5 x4 or SATA 6Gb/s or SAS-4 24Gb/s



- Security Modules
- - Optional TPM2.0 kit: CTM012

1 x TPM header with SPI interface

1 x PRoT connector (only enabled on RoT SKU)



- 3+1 2000W 80 PLUS Titanium redundant power supplies
- AC Input:
 - 100-127V~/ 13A, 50-60Hz
 - 200-220V~/ 10A. 50-60Hz
 - 220-240V~/ 10A, 50-60Hz
- DC Input: (Only for China)
 - 240Vdc/ 10A
- DC Output:
 - Max 1000W/ 100-127V~
 - +12.2V/82A
 - +12.2Vsb/3A
 - Max 1800W/ 200-220V~
 - +12.2V/ 148A
 - +12 2Vsb/3A
 - Max 2000W/ 220-240V~ or 240Vdc Input
 - +12.2V/ 164A
 - +12.2Vsb/3A



- ◆ ASPEED® AST2600 Baseboard Management Controller
- GIGABYTE Management Console web interface
- Dashboard
- HTML5 KVM
- Sensor Monitor (Voltage, RPM, Temperature, CPU Status ...etc.)
- · Sensor Reading History Data
- FRU Information
- SEL Log in Linear Storage / Circular Storage Policy
- Hardware Inventory
- Fan Profile
- System Firewall
- Power Consumption
- Power Control
- Advanced power capping
- LDAP / AD / RADIUS Support
- Backup & Restore Configuration
- Remote BIOS/BMC/CPLD Update
- Event Log Filter
- User Management
- Media Redirection Settings
- PAM Order Settings
- SSL Settings
- SMTP Settings

Compat

Please refer to OS compatibility table in support page

Compatibility

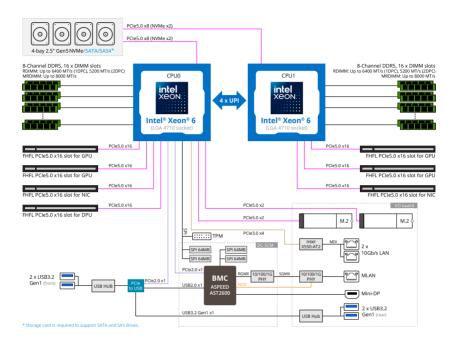


• 4 x 80x80x80mm (17,500rpm)



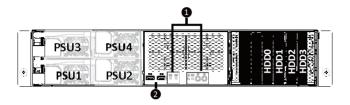
- ◆ Operating temperature: 10°C to 35°C
- Operating humidity: 8-80% (non-condensing)
- Non-operating temperature: -40°C to 60°C
- Non-operating humidity: 20%-95% (non-condensing)

1-3 System Block Diagram



Chapter 2 System Appearance

2-1 Front View

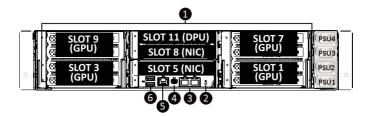


| No. | Description |
|-----|--|
| 1. | Front Panel LEDs and Buttons |
| 2. | USB 3.2 Gen1 Port x 2 |
| NC | OTE! Drives with green latches support NVMe. |



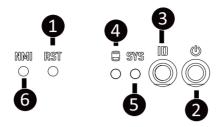
Please Go to Chapter 2-3 Front Panel LED and Buttons for detail description of function LEDs.

2-2 Rear View



| No. | Description |
|-----|----------------------|
| 1. | PCIe Card Slot |
| 2. | ID LED |
| 3. | Data LAN Port x2 |
| 4. | Mini DP |
| 5. | Management LAN Port |
| 6. | USB 3.2 Gen1 Port x2 |

2-3 Front Panel LEDs and Buttons



| No. | Name | Color | Status | Description | | | | |
|-----|-----------------|-----------------|----------|--|--|--|--|--|
| 1. | Reset Button | | | Press the button to reset the system. | | | | |
| 2. | Power button | Green | On | System is powered on | | | | |
| ۷. | with LED | N/A | Off | System is not powered on or in ACPI S5 state (power off) | | | | |
| 3. | ID Button(Note) | | | Press the button to activate system identification | | | | |
| | | 0 | On | HDD locate | | | | |
| | | Green | Blink | HDD access | | | | |
| 4. | HDD Status | Amber | On | HDD fault | | | | |
| | LED - | Green/ Amber | Blink | HDD rebuilding | | | | |
| | | N/A | Off | No HDD access or no HDD fault. | | | | |
| | | Green | Solid On | System is operating normally. | | | | |
| | | | Solid On | Critical condition, may indicate: | | | | |
| | | | | System fan failure; System temperature | | | | |
| | System | Amber | | Non-critical condition, may indicate: | | | | |
| 5. | Status | Allibei | Blink | Redundant power module failure | | | | |
| | LED(Note) | | DIIIK | Temperature and voltage issue | | | | |
| | | | | Chassis intrusion | | | | |
| | | N/A | Off | System is not ready, may indicate: | | | | |
| | | IN/A | Oll | POST error; NMI error; Processor or terminator missing | | | | |
| | | | | Press the button server generates a NMI to | | | | |
| 6. | NMI button | | | the processor if the multiple-bit ECC errors | | | | |
| | | | | occur, which effectively halt the server. | | | | |

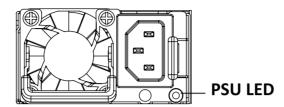
(Note) If your server features RoT function, please see the following section for detail LED behavior.

2-4 Power Supply Unit (PSU) LED



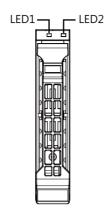
NOTE!

The power supply may be vary based on the system configuration.



| State | Description |
|--------------------|---|
| OFF | No AC power to all power supplies |
| 1Hz Green Blinking | AC present / only standby on / Cold redundant mode |
| 2Hz Green Blinking | Power supply firmware updating mode |
| Amber | AC cord unplugged or AC power lost; with a second power supply in parallel still with AC input power |
| | Power supply critical event causing shut down: failure, OCP, OVP, fan failure and UVP |
| 1Hz Amber Blinking | Power supply warning events where the power supply continues to operate: high temp, high power, high current and slow fan |

2-5 Hard Disk Drive LEDs



| RAID | LED #1 | Locate | HDD Fault | Rebuilding | HDD Access | HDD Present (No Access) | |
|---|-----------------------------|--------|--------------|------------|----------------------|----------------------------|-----|
| | Disk LED (LED | Green | ON(*1) | OFF | | BLINK (*2) | OFF |
| No RAID | on Back Panel) | Amber | OFF | OFF | | OFF | OFF |
| configuration (via PCH, HBA) | Removed HDD | Green | ON(*1) | OFF | | | |
| | Slot (LED on Back Panel) | Amber | OFF | OFF | | | |
| RAID | | Green | ON | OFF | | BLINK (*2) | OFF |
| configuration (via HW RAID Card or SW RAID Card) | Disk LED | Amber | OFF | ON | (Low Speed: 2 Hz) | OFF | OFF |
| | Removed | Green | ON(*1) | OFF | (*3) | | |
| | HDD Slot | Amber | OFF | ON | (*3) | | |

| LED #2 | HDD Present | No HDD |
|--------|-------------|--------|
| Green | ON | OFF |

NOTE:

- *1: Depends on HBA/Utility Spec.
- *2: Blink cycle depends on HDD's activity signal.
- *3: If HDD is pulled out during rebuilding, the disk status of this HDD is regarded as faulty.

Chapter 3 System Hardware Installation



Pre-installation Instructions

Computer components and electronic circuit boards can be damaged by electrostatic discharge. Working on computers that are still connected to a power supply can be extremely dangerous. Follow the simple guidelines below to avoid damage to your computer or injury to yourself.

- Always disconnect the computer from the power outlet whenever you are working inside the computer case.
- If possible, wear a grounded wrist strap when you are working inside the computer case.
 Alternatively, discharge any static electricity by touching the bare metal system of the computer case, or the bare metal body of any other grounded appliance.
- Hold electronic circuit boards by the edges only. Do not touch the components on the board unless it is necessary to do so. Do not flex or stress the circuit board.
- Leave all components inside the static-proof packaging until you are ready to use the component for the installation.

3-1 Removing Chassis Cover

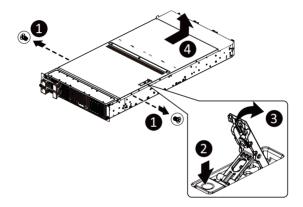


Before you remove or install the system cover

• Make sure the system is not turned on or connected to AC power.

Follow these instructions to remove the chassis cover:

- 1. Remove the screw securing the chassis cover.
- 2. Push button to unlock the handle.
- 3. Pull the grip handle to open the panel cover.
- 4. Slide the chassis cover towards the rear and remove the chassis cover in the direction indicated.
- 5. To reinstall the chassis cover reverse steps 1-4.



3-2 Installing the PCIe / GPU Card



Voltages can be present within the server whenever an AC power source is connected. This
voltage is present even when the main power switch is in the off position. Ensure that the
system is powered-down and all power sources have been disconnected from the server prior to
installing a PCI card.

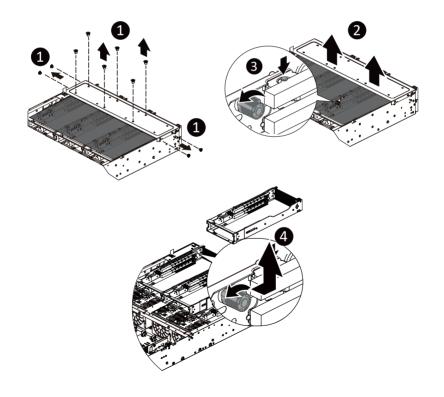
Failure to observe these warnings could result in personal injury or damage to equipment.



The PCI riser assembly does not include a riser card or any cabling as standard. To install a PCI card, a riser card must be installed.

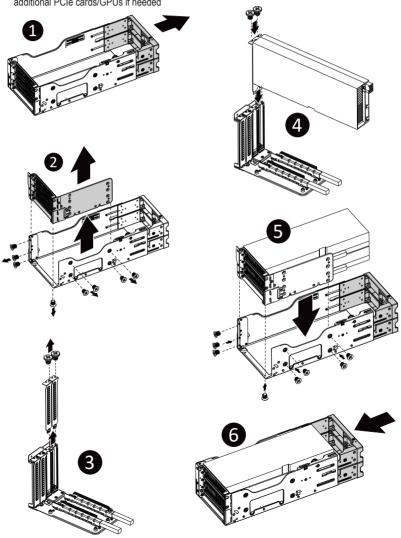
Follow these instructions to remove the PCIe cage:

- 1. Loosen and remove the six screws securing the PCI cage at the top of the system.
- 2. Loosen and remove the four screws at the side of the system securing the PCI cage.
- 3. Press the bottom that securing the PCI cage.
- 4. Pull the plastic handles to lift up the PCI cage from the system.



Follow these instructions for the PCI Expansion / GPU card:

- 1. Remove the riser bracket from the system.
- Loosen and remove the screw securing the slot covers on the riser bracket then detach the slot covers.
- 3. Unscrew the screws securing the PCIe Card/GPU in the Slot.
- Alignthe PCIe card/GPU with the riser guide slot, and gently push it in the direction of the arrow until it securely connects to the PCIe card connector.
- 5. Secure the PCle card / GPU using the screw.
- Reinsert the PCle card / GPU into the PCle Cage. Follow the reverse order of these steps to install
 additional PCle cards/GPUs if needed



3-3 Installing the Hard Disk Drive

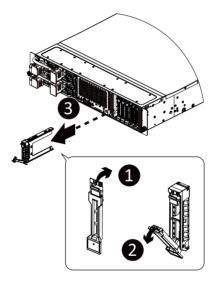


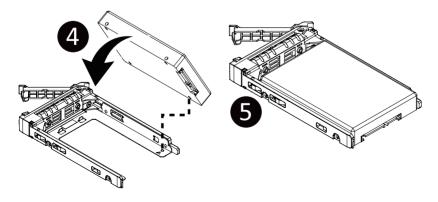
Read the following guidelines before you begin to install the HDD:

- · Take note of the drive tray orientation before sliding it out.
- The tray will not fit back into the bay if inserted incorrectly.
- Make sure that the HDD is connected to the HDD connector on the backplane.

Follow these instructions to install a 2.5" HDD:

- 1. Press the release button.
- Extend the locking lever.
- 3. Pull the locking lever in the direction indicated to remove the HDD tray.
- 4. Slide the hard disk drive into the HDD tray.
- 5. Reinsert the HDD tray into the slot and close the locking lever.





3-4 Installing the CPU



Read the following guidelines before you begin to install the CPU:

- · Make sure that the motherboard supports the CPU.
- Always turn off the computer and unplug the power cord from the power outlet before installing the CPU to prevent hardware damage.
- Unplug all cables from the power outlets.
- Disconnect all telecommunication cables from their ports.
- · Place the system unit on a flat and stable surface.
- · Open the system according to the instructions.



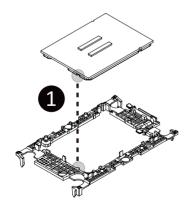
WARNING!

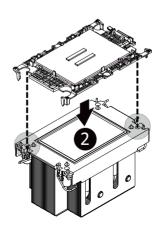
Failure to properly turn off the server before you start installing components may cause serious damage. Do not attempt the procedures described in the following sections unless you are a qualified service technician.

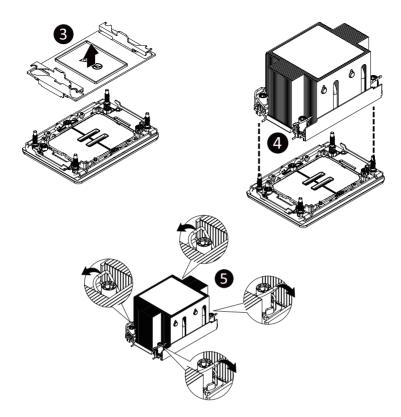
Follow these instructions to Install the CPU:

- 1. Align and install the processor on the carrier.
 - **NOTE:** Apply thermal compound evenly on the top of the CPU. Remove the protective cover from the underside of the heat sink.
- Carefully flip the heat sink cover. Then install the carrier assembly on the bottom of the heat sink and make sure the gold arrow is located in the correct direction.
- 3. Remove the CPU cover.
 - NOTE: Save the CPU cover in the event that you need to remove the CPU from the socket.
- Align the heat sink with the CPU socket by the guide pins and make sure the gold arrow is located in the correct direction. Then place the heat sink onto the top of the CPU socket.
- 5. Position the rotating wires into the latch position. Tighten the screws in sequential order $(1\rightarrow 2\rightarrow 3\rightarrow 4)$.

NOTE: When dissembling the heat sink, loosen the screws in reverse order $(4\rightarrow3\rightarrow2\rightarrow1)$ and then move the rotating wires into the unlatch position.







Carrier Types used for Package Types

| Package Type | Granite Rapids-SP (R1S) XCC | Granite Rapids-SP (R1S) HCC/LCC | | |
|--------------|--------------------------------|------------------------------------|--|--|
| Carrier Code | arrier Code E2A | | | |

NOTE!

- The carrier code is marked on each carrier and matches a code laser marked on to the IHS(Integrated Heat Spreader) to ensure the right parts are used together
- When installing the heat sink to CPU, use T30-Lobe driver to tighten 4 captive nuts in sequence as 1-4.
- Please refer to the Heatsink Label for the screw tightening torque value.

3-5 Installing the Memory

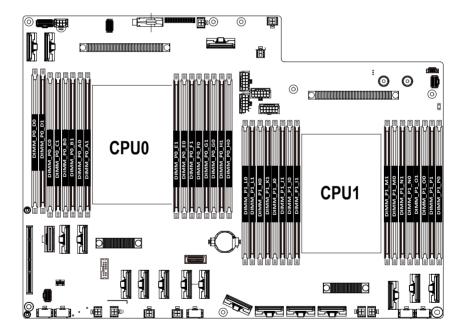


Read the following guidelines before you begin to install the memory:

- Make sure that the motherboard supports the memory. It is recommended that memory of the same capacity, brand, speed, and chips be used.
- Always turn off the computer and unplug the power cord from the power outlet before installing
 the memory to prevent hardware damage.
- Memory modules have a foolproof design. A memory module can be installed in only one direction. If you are unable to insert the memory, switch the direction.

3-5-1 Eight Channel Memory Configuration

This motherboard provides 32 DDR5 memory slots and supports 8-Channel Technology. After the memory is installed, the BIOS will automatically detect the specifications and capacity of the memory.



3-5-2 Installing the Memory

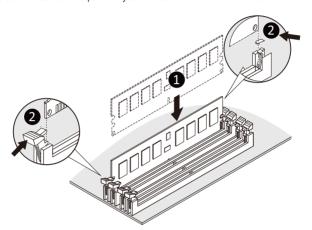


Before installing a memory module, make sure to turn off the computer and unplug the power cord from the power outlet to prevent damage to the memory module.

Be sure to install DDR5 DIMMs on this motherboard.

Follow these instructions to install the Memory:

- 1. Insert the DIMM memory module vertically into the DIMM slot, and push it down.
- 2. Close the plastic clip at both edges of the DIMM slots to lock the DIMM module.
- 3. Reverse the installation steps when you want to remove the DIMM module.



3-5-3 Processor and Memory Module Matrix Table

| | Г | | | | | | | | PU0 | | | | | | | |
|--------------|----|--|----|----|----|----|----|----|-----|----|----|----|----|----|----|----|
| Memory Q'ty | H0 | Н1 | G0 | G1 | F0 | F1 | E0 | E1 | A1 | A0 | В1 | ВО | C1 | C0 | D1 | D0 |
| for each CPU | | CPU1 PO P1 00 01 N0 N1 M0 M1 I1 I0 J1 J0 K1 K0 L1 L0 | | | | | | | | | | | | | | |
| | P0 | P1 | 00 | 01 | N0 | N1 | M0 | M1 | 11 | 10 | J1 | J0 | K1 | K0 | L1 | L0 |
| 1 DIMM | | | | | | | | | | v | | | | | | |
| 4 DIMM | | | v | | | | v | | | v | | | | v | | |
| 4 DIIVIIVI | ٧ | | | | v | | | | | | | v | | | | v |
| | v | | v | | v | | v | | | v | | v | | v | | v |
| 8 DIMM | L | | v | v | | | v | v | v | v | | | v | v | | |
| | v | v | | | v | v | | | | | v | v | | | v | v |
| 12 DIMM | v | | v | v | v | | > | v | v | v | | v | v | v | | v |
| 16 DIMM | v | V | v | V | v | v | v | ٧ | V | v | v | v | V | v | V | v |

3-5-4 DIMM Population Table

Intel Xeon 6700E-Series Memory Support

| Туре | Ranks Per DIMM and | | | MM Capac | Channel Speed (MT/s); Voltage (V); Slots per Channel (SPC) & DIMMs per Channel (DPC) | | | | |
|-------|--------------------|------|------|----------|--|-------|-------|--|--|
| | | 16Gb | | 24Gb | | 32Gb | | 1DPC/2SPC | 2DPC/2SPC |
| | | 1DPC | 2DPC | 1DPC | 2DPC | 1DPC | 2DPC | 1. | 1V |
| | 1Rx4 | 32GB | | | | | | 6400, 6000, 5600, 5200, 4800 (DDR5-6400 rated RDIMMS only) | NA |
| | 2Rx8 | 32GB | | | | | | | NA |
| | 2Rx4 | 64GB | 64GB | 96GB | 96GB | | | | 5200, 4800 |
| RDIMM | 2Rx4 | | | | | 128GB | 128GB | | (DDR5-6400 rated RDIMMS only) NA |

Intel Xeon 6700E-Series CXL Memory Support

| Nativ | e DDR5 Mer | mory Per S | Socket | CXL Memory Per Socket | | | | | |
|----------------------|------------------------------|--------------|----------------------|------------------------|--------------------|---------------------------------|-------------------|-------------------------------|--|
| Slot 0 DIMM Ranks | Slot 0 DIMM Capacity (GB) | DIMM Type | DRAM Density (Gb) | CXL Memory Channels | CXL Memory Type | CXL Capacity Per Device/ Module | CXL Interleave | CXL Mode | |
| 2Rx4 | 64 | 10x4 | 16 | 2+2 | DDR5 x8 | 64 GB | 1x4*, 2x2, 4x1 | 1LM+Vol | |
| 2Rx4 | 64 | 10x4 | 16 | 1+1 | DDR5 x16 | 128 GB | 1x2*, 2x1 | 1LM+Vol | |
| 1Rx4 | 32 | 10x4 | 16 | 2 | DDR5 x8 | 128 GB | 1x2* | Intel® Flat Memory Mode | |

NOTE:

- Intel Xeon 6700E-series CXL memory configs are 1DPC ('Slot 0') only for native DDR5
- CXL Memory Channel notation: # of devices per root port, with root ports separated by "+". i.e. 2+2+2+2
 = four root ports populated with two devices per root port
- CXL Interleave notation: sets x ways. i.e. 2x4 = One set of two modules, interleaved four-way
- CXL Modes:
 - 1LM+Vol = DDR5 ('1LM') and (Volatile) CXL memory visible to SW as separate tiers, separately
 interleaved.
 - Flat Memory Mode = HW manages data movement between DDR5 and CXL memory, total capacity visible to SW

Intel Xeon 6500P/6700P-Series Memory Support

| Туре | Ranks Per | | | | pacity (GB) |) | | Channel Speed (N Slots per Channel (Channel De | SPC) & DIMMs per |
|-----------|------------|-------|--------|-------|-------------|--------|---------|---|---------------------|
| | Data Width | 16Gb | | 24Gb | | 32Gb | | 1DPC/2SPC | 2DPC/2SPC |
| | | 1DPC | 2DPC | 1DPC | 2DPC | 1DPC | 2DPC | 1.1 | IV |
| | 1Rx8 | 16GB | | 24GB | | | | 6400, 6000, | |
| RDIMM | 1Rx4 | 32GB | | 48GB | | | | 5600, 5200, 4800 | 5200, 4800 |
| KDIIVIIVI | 2Rx8 | 32GB | 32GB | 48GB | | | | , | |
| | 2Rx4 | 64GB* | 64GB*^ | 96GB* | 96GB*^ | 128GB* | 128GB*^ | (DDR5-6400 | (DDR5-6400 rated |
| RDIMM 3DS | 8Rx4 | | 256GB* | | | | | rated RDIMMS only) | RDIMMS only) |
| MRDIMM | 2Rx8 | 32GB | | | | | | 8000, 7200 | N/A (no 2DPC |
| | 2Rx4 | 64GB | | | | | | (MRDIMM-8800 only) | configs for MRDIMM) |

NOTE:

- *Supported in 1S/2S/4S systems
- Supported in 8S systems

Intel Xeon 6500P/6700P-Series CXL Memory Support

| Nativ | ve DDR5 M Soci | , | Per | CXL Memory Per Socket | | | | | | |
|---------------------|-----------------------------|--------------|-------------------------|------------------------|--------------------|---------------------------------------|-------------------|-------------------------------|-------------------|--|
| Slot0 DIMM Ranks | Slot0 DIMM Capacity (GB) | DIMM Type | DRAM Density (Gb) | CXL Memory Channels | CXL Memory Type | CXL Capacity Per Device/ Module | CXL Interleave | CXL Mode | 4S &8S Support | |
| 2Rx4 | 96 | 10x4 | 24 | 2+2 | DDR5 x8 | 96 GB# | 1x4*, 2x2, 4x1 | 1LM+Vol | Yes | |
| 2Rx4 | 128 | 10x4 | 32 | 2+2 | DDR4x8# DDR5 x8 | 128 GB | 1x4*, 2x2, 4x1 | 1LM+Vol | Yes | |
| 2Rx4 | 128 | 10x4 | 32 | 2+2 | DDR5 x8 | 128 GB | hetero x12 | Hetero | Yes | |
| 2Rx4 | 64 | 10x4 | 16 | 2+2+2 | DDR5 x8 | 128 GB | 1x6*, 2x3, 3x2 | 1LM+Vol | No | |
| 2Rx4 | 64 | 10x4 | 16 | 2 | DDR5 x8 | 128 GB | 1x2* | 1LM+Vol | No | |
| 2Rx4 | 64 | 10x4 | 16 | 1+1 | DDR5 x16 | 2ch 128 GB | 1x2* | Intel® Flat Memory Mode | No | |

NOTE:

- Xeon 6500P/6700P-series processors CXL memory configs are 1DPC ('Slot 0') only for native DDR5
- CXL Memory Channel notation: # of devices per root port, with root ports separated by "+". i.e. 2+2+2+2
 four root ports populated with two devices per root port
- CXL Interleave notation: sets x ways. i.e. 2x4 = Set of two modules, interleaved four-way
- CXL Modes:
 - 1LM+Vol = Native DDR5 ('1LM') and (volatile) CXL memory visible to SW as separate tiers, separately interleaved
 - Hetero x12 = DDR5 and (volatile) CXL memory interleaved together in one 12-way set (See graphic in next slide)
 - Flat Memory Mode = HW manages data movement between DDR5 and CXL memory, total capacity visible to SW

3-6 Installing the M.2 Device and Heat Sink



CAUTION

The position of the stand-off screw will depend on the size of the M.2 device. Refer to the size of the M.2 device and change the position of the stand-off screw accordingly.



WARNING:

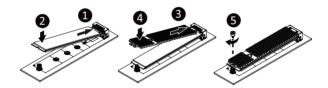
Please ensure a heatsink is attached to any M.2 device installed into the system. Installing an M.2 device without any heatsink may result in the system overheating or system performance being throttled.



To install/remove the M.2 module and Heatsink use a No. 1 Phillips-head screwdriver with a screw torque of 1.5 ± 0.2 kgf*cm

Follow these instructions to install the M.2 device and heat sink:

- Insert the M.2 device into the M.2 connector.
- Press down on the M.2 device.
- 3. Install the thermal pad of the M.2 device to the M.2 device.
- 4. Press down on the thermal pad.
- 5. Secure the M.2 device and its thermal pad to the motherboard with a single screw.
- 6. Reverse steps 1-2 to remove the M.2 device.



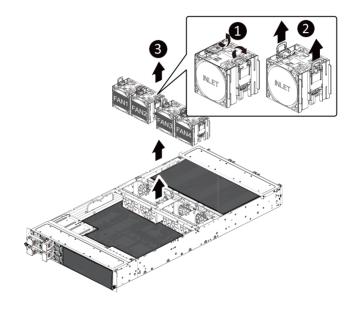
3-7 Replacing the Fan Assembly



- Voltages can be present within the server whenever an AC power source is connected. This
 voltage is present even when the main power switch is in the off position. Ensure that the
 system is powered-down and all power sources have been disconnected from the server prior to
 replacing a system fan.
- Failure to observe these warnings could result in personal injury or damage to equipment.

Follow these instructions to replace the fan assembly:

- 1. Lift up the fan assembly from the chassis.
- 2. Reverse the previous steps to install the replacement fan assembly.



3-8 Replacing the Power Supply

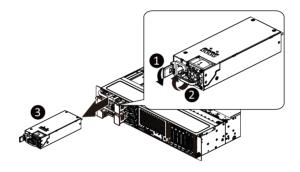


CAUTION!

 In order to reduce the risk of injury from electric shock, disconnect AC power from the power supply before removing the power supply from the system

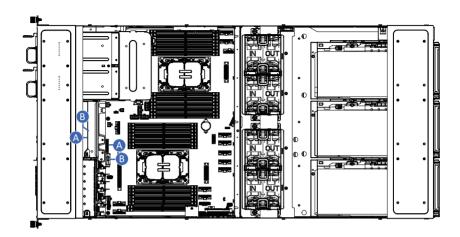
Follow these instructions to replace the power supply:

- 1. Flip and then grasp the power supply handle.
- 2. Press the retaining clip on the top side of the power supply in the direction indicated.
- 3. Pull out the power supply using the handle.
- Insert the replacement power supply firmly into the chassis. Connect the AC power cord to the replacement power supply.



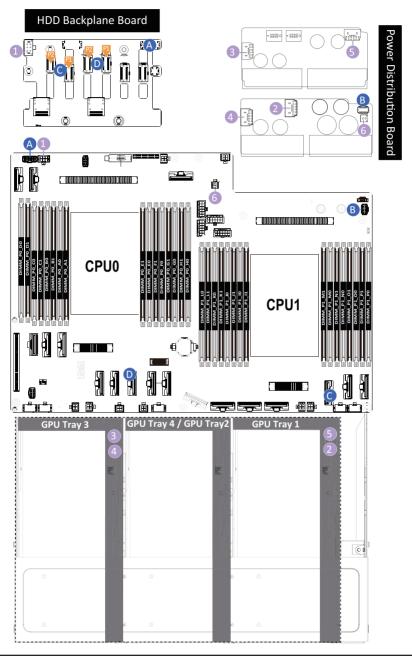
3-9 Cable Routing

3-9-1 Front Panel IO Board



| | A Fr | Front Switch/LED Cable | Motherboard: FP_1 | | |
|--|------|--------------------------|------------------------|--|--|
| | | 1 TOTIL SWILCH/LLD Cable | Front IO Board: FP_1 | | |
| | В | Front USB 3 Cable | Motherboard: F_USB1 | | |
| | D | FIORE USB 3 Cable | Front IO Board: F_USB3 | | |

3-9-2 Motherboard to HDD BPB / Power Distribution Board



Signal Cable

| A | Motherboard | BP_1 |
|---|--------------------------|------------|
| ^ | HDD BPB | BP_1 |
| В | Motherboard | MB_PSU_CON |
| • | Power Distribution Board | MB_PSU_CON |

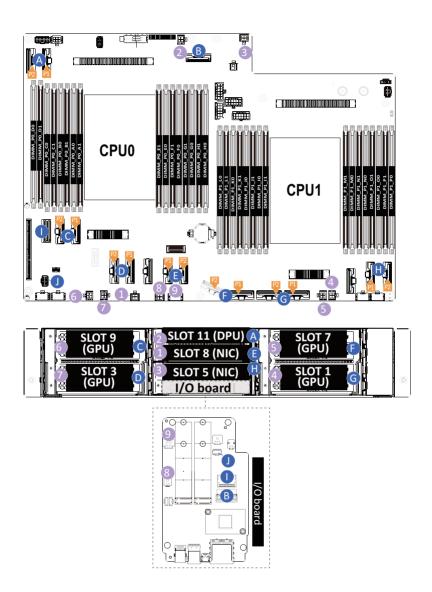
Power Cable

| | Motherboard | ATX3 |
|---|---------------------------------------|------------|
| 1 | HDD BPB | BPB_PWR |
| | GPU Power | GPU TRAY 1 |
| 2 | Power Distribution Board (CPDXD41) | P12V_S1 |
| | GPU Power | GPU TRAY 3 |
| 3 | Power Distribution Board (CPDXD40) | P12V_S9 |
| | GPU Power | GPU TRAY 3 |
| 4 | Power Distribution Board (CPDXD41) | P12V_S3 |
| | GPU Power | GPU TRAY 1 |
| 5 | Power Distribution Board (CPDXD40) | P12V_S7 |
| | Motherboard | PWR_STBY |
| 6 | Power Distribution Board (CPDXD40) | PWR_STBY |

MCIO Cable

| С | HDD BPB | P1(U2_2_4) | | |
|---|-------------|------------|--|--|
| | пии вгв | P2(U2_2_5) | | |
| | Motherboard | U2_P1_1AC | | |
| D | HDD BPB | P1(U2_2_2) | | |
| | пии вгв | P2(U2_2_3) | | |
| | Motherboard | U2_P0_1AC | | |

3-9-3 Motherboard to Riser Slot / Rear IO Board



MCIO Cable

| | Motherboard | P1(U2_P0_4GE) |
|---|---------------|---------------|
| Α | Wollierboard | P2(U2_P0_4CA) |
| | PCIe Slot | Slot 11 |
| В | Motherboard | IO_MCIO |
| В | Rear IO Board | MCIO |
| | Madhadaaad | P1(U2_P0_2AC) |
| С | Motherboard | P2(U2_P0_2EG) |
| | PCIe Slot | Slot 9 |
| | Madhadaaad | P1(U2_P0_0GE) |
| D | Motherboard | P2(U2_P0_0CA) |
| | PCIe Slot | Slot 3 |
| | Motherboard | P1(U2_P0_3AC) |
| Е | | P2(U2_P0_3EG) |
| | PCIe Slot | Slot 8 |
| | Motherboard | P1(U2_P1_2AC) |
| F | Wolfierboard | P2(U2_P1_2EG) |
| | PCIe Slot | Slot 7 |
| | M. d l | P1(U2_P1_0GE) |
| G | Motherboard | P2(U2_P1_0CA) |
| | PCle Slot | Slot 1 |
| Н | Motherboard | P1(U2_P1_3AC) |
| | mouleiboald | P2(U2_P1_3EG) |
| | PCle Slot | Slot 5 |
| | Motherboard | IO_SLIM |
| ı | Rear IO Board | FP_IO |
| | | |

Power Cable

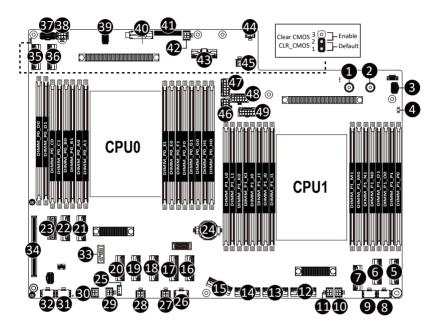
| 1 | PCIe Slot | Slot 8 |
|---|---------------|------------|
| ' | Motherboard | PCIE8_PWR |
| 2 | PCIe Slot | Slot 11 |
| | Motherboard | PCIE11_PWR |
| 3 | PCIe Slot | Slot 5 |
| , | Motherboard | PCIE5_PWR |
| 4 | PCIe Slot | Slot 1 |
| 4 | Motherboard | PCIE1_PWR |
| 5 | PCIe Slot | Slot 7 |
| , | Motherboard | PCIE7_PWR |
| 6 | PCIe Slot | Slot 9 |
| | Motherboard | PCIE9_PWR |
| 7 | PCIe Slot | Slot 3 |
| ' | Motherboard | PCIE3_PWR |
| 8 | Rear IO Board | PWR_FP |
| 8 | Motherboard | IO_PWR |
| 9 | Rear IO Board | PWR_12V |
| 9 | Motherboard | IO_PWR |
| | | |

Signal Cable

| | Motherboard | NCSI_CONN |
|---|---------------|-----------|
| J | Rear IO Board | NCSI_CONN |

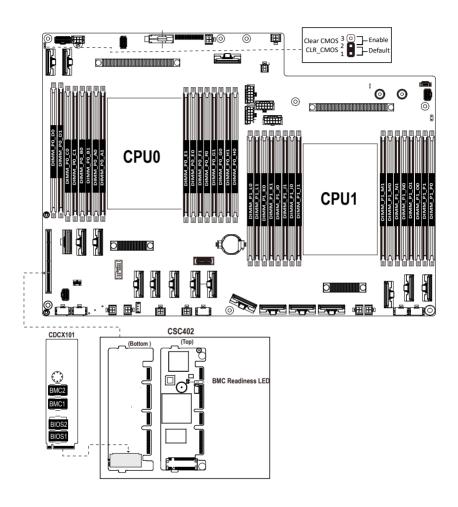
Chapter 4 Motherboard Components

4-1 Motherboard Components



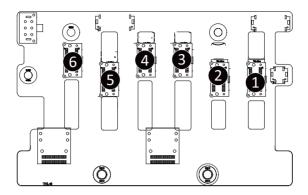
| BUSBAR GND | | Code | Description |
|--|----------|-----------|---|
| 2 BUSBAR P12V Bus Bar Input Power (P12V) 3 MB PSU CON Power Distribution Board sideband Connector 4 CASE OPEN Case Open Intrusion Alert Header 5 U2_P1_3EG MCIO Connector Slot 5 (PCIe Gen5)_P2 6 U2_P1_3AC MCIO Connector Slot 5 (PCIe Gen5)_P1 7 U2_P1_1AC MCIO Connector BP (PCIe Gen5)_P1 8 FAN12 2 x 4 Pin Power Connector for Fan 1 / 2 9 FAN34 2 x 4 Pin Power Connector for Fan 3 / 4 10 PCIE1 PWR 2 x2 Pin P12V Slot1 Power Connector 11 PCIE7 PWR 2 x2 Pin P12V Slot7 Power Connector 12 U2_P1_0GE MCIO Connector Slot 1 (PCIe Gen5)_P1 13 U2_P1_0GE MCIO Connector Slot 1 (PCIe Gen5)_P2 14 U2_P1_2AC MCIO Connector Slot 7 (PCIe Gen5)_P2 15 U2_P1_2AC MCIO Connector Slot 8 (PCIe Gen5)_P2 16 U2_P0_3EG MCIO Connector Slot 8 (PCIe Gen5)_P2 17 U2_P0_3AC MCIO Connector Slot 8 (PCIe Gen5)_P2 18 U2_P0_3AC MCIO Connector Slot 8 (PCIe Gen5)_P1 19< | <u> </u> | | • |
| MB_PSU_CON | _ | _ | |
| 4 CASE_OPEN Case Open Intrusion Alert Header 5 U2_P1_3EG MCIO Connector Slot 5 (PCIe Gen5)_P2 6 U2_P1_3AC MCIO Connector Slot 5 (PCIe Gen5)_P1 7 U2_P1_1AC MCIO Connector BP (PCIe Gen5)_P1 8 FAN12 2 x 4 Pin Power Connector for Fan 1 / 2 9 FAN34 2 x 4 Pin Power Connector for Fan 3 / 4 10 PCIE1_PWR 2 x2 Pin P12V Slot7 Power Connector 11 PCIE7_PWR 2 x2 Pin P12V Slot7 Power Connector 12 U2_P1_0GE MCIO Connector Slot 1 (PCIe Gen5)_P1 13 U2_P1_0GE MCIO Connector Slot 7 (PCIe Gen5)_P2 14 U2_P1_2EG MCIO Connector Slot 7 (PCIe Gen5)_P2 15 U2_P1_2EG MCIO Connector Slot 8 (PCIe Gen5)_P2 16 U2_P0_3AC MCIO Connector Slot 8 (PCIe Gen5)_P2 17 U2_P0_3AC MCIO Connector Slot 3 (PCIe Gen5)_P1 18 U2_P0_1AC MCIO Connector Slot 3 (PCIe Gen5)_P1 19 U2_P0_0GE MCIO Connector Slot 3 (PCIe Gen5)_P1 20 U2_P0_0CA MCIO Connector Slot 3 (PCIe Gen5)_P2 2 | | | · · · · · · · · · · · · · · · · · · · |
| 5 U2_P1_3AC MCIO Connector Slot 5 (PCIe Gen5)_P1 6 U2_P1_3AC MCIO Connector Slot 5 (PCIe Gen5)_P1 7 U2_P1_1AC MCIO Connector Slot 5 (PCIe Gen5) 8 FAN12 2 x 4 Pin Power Connector for Fan 1 / 2 9 FAN34 2 x 4 Pin Power Connector Fan 3 / 4 10 PCIE1 PWR 2 x2 Pin P12V Slot7 Power Connector 11 PCIE7_PWR 2 x2 Pin P12V Slot7 Power Connector 12 U2_P1_0GE MCIO Connector Slot 1 (PCIe Gen5)_P1 13 U2_P1_0GE MCIO Connector Slot 1 (PCIe Gen5)_P2 14 U2_P1_2AC MCIO Connector Slot 7 (PCIe Gen5)_P2 15 U2_P1_2AC MCIO Connector Slot 7 (PCIe Gen5)_P2 16 U2_P0_3EG MCIO Connector Slot 8 (PCIe Gen5)_P2 16 U2_P0_3EG MCIO Connector Slot 8 (PCIe Gen5)_P2 17 U2_P0_3AC MCIO Connector Slot 8 (PCIe Gen5)_P1 18 U2_P0_1AC MCIO Connector Slot 3 (PCIe Gen5)_P2 19 U2_P0_3AC MCIO Connector Slot 3 (PCIe Gen5)_P1 20 U2_P0_0EG MCIO Connector Slot 3 (PCIe Gen5)_P1 <t< td=""><td>3</td><td></td><td></td></t<> | 3 | | |
| 6 U2_P1_3AC MCIO Connector Slot 5 (PCIe Gen5)_P1 7 U2_P1_1AC MCIO Connector BP (PCIe Gen5) 8 FAN12 2 x 4 Pin Power Connector for Fan 1 / 2 9 FAN34 2 x 4 Pin Power Connector for Fan 3 / 4 10 PCIE1 PWR 2 x 2 Pin P12V Slot1 Power Connector 11 PCIE7 PWR 2 x 2 Pin P12V Slot7 Power Connector 12 U2_P1_0GE MCIO Connector Slot 1 (PCIe Gen5)_P1 13 U2_P1_0GE MCIO Connector Slot 1 (PCIe Gen5)_P2 14 U2_P1_2AC MCIO Connector Slot 7 (PCIe Gen5)_P2 15 U2_P1_2EG MCIO Connector Slot 7 (PCIe Gen5)_P2 16 U2_P0_3GC MCIO Connector Slot 8 (PCIe Gen5)_P2 17 U2_P0_3AC MCIO Connector Slot 3 (PCIe Gen5)_P1 18 U2_P0_1AC MCIO Connector Slot 3 (PCIe Gen5)_P1 20 U2_P0_0GE MCIO Connector Slot 3 (PCIe Gen5)_P1 21 U2_P0_1AC MCIO Connector Slot 3 (PCIe Gen5)_P1 22 U2_P0_1AC MCIO Connector Slot 3 (PCIe Gen5)_P1 22 U2_P0_1AC MCIO Connector Slot 3 (PCIe Gen5)_P1 | 4 | CASE_OPEN | Case Open Intrusion Alert Header |
| 7 U2_P1_1AC MCIO Connector BP (PCIe Gen5) 8 FAN12 2 x 4 Pin Power Connector for Fan 1 / 2 9 FAN34 2 x 4 Pin Power Connector for Fan 3 / 4 10 PCIE1 PWR 2 x 2 Pin P12V Slot1 Power Connector 11 PCIE7 PWR 2 x 2 Pin P12V Slot7 Power Connector 12 U2_P1_0CE MCIO Connector Slot 1 (PCIe Gen5)_P1 13 U2_P1_0CA MCIO Connector Slot 7 (PCIe Gen5)_P1 14 U2_P1_2EG MCIO Connector Slot 7 (PCIe Gen5)_P2 15 U2_P1_2EG MCIO Connector Slot 7 (PCIe Gen5)_P2 16 U2_P0_3EG MCIO Connector Slot 8 (PCIe Gen5)_P2 17 U2_P0_3AC MCIO Connector Slot 8 (PCIe Gen5)_P1 18 U2_P0_3AC MCIO Connector Slot 8 (PCIe Gen5)_P1 19 U2_P0_0GE MCIO Connector Slot 3 (PCIe Gen5)_P1 20 U2_P0_0GE MCIO Connector Slot 3 (PCIe Gen5)_P2 21 U2_P0_2AC MCIO Connector Slot 9 (PCIe Gen5)_P2 21 U2_P0_2EG MCIO Connector Slot 9 (PCIe Gen5)_P2 21 U2_P0_2EG MCIO Connector Slot 9 (PCIe Gen5)_P2 | 5 | U2_P1_3EG | MCIO Connector Slot 5 (PCIe Gen5)_P2 |
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| 31 FAN56 2 x 4 Pin Power Connector for Fan 5 / 6 32 FAN78 2 x 4 Pin Power Connector for Fan 7 / 8 33 SPI_TPM TPM Module Connector (SPI Interface) 34 DC_SCI DC-SCM (BMC Module) Connector 35 U2_P0_4CA MCIO Connector Slot 11 (PCIe Gen5)_P2 36 U2_P0_4GE MCIO Connector Slot 11 (PCIe Gen5)_P1 37 BP_1 HDD Backplane Board Connector 38 ATX3 2 x 3 Pin ATX Power Connector 39 JLFP Front Panel Header 40 F_USB Front Panel USB 3.2 Gen 1 Connector 41 FP_1 Front Panel Header 42 PCIE11_PWR 2 x2 Pin P12V Slot11 Power Connector 43 IO_MCIO IO Singnal Connector 44 PCIE5_PWR 2 x2 Pin P12V Slot5 Power Connector 45 PWR_STBY 1x 2 Pin 12V Standby Power Connector 46 PWR_IN1 Input Power Connector 48 PWR_IN2 Input Power Connector | | _ | |
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| 33 SPI_TPM TPM Module Connector (SPI Interface) 34 DC_SCI DC-SCM (BMC Module) Connector 35 U2_P0_4CA MCIO Connector Slot 11 (PCIe Gen5)_P2 36 U2_P0_4GE MCIO Connector Slot 11 (PCIe Gen5)_P1 37 BP_1 HDD Backplane Board Connector 38 ATX3 2 x 3 Pin ATX Power Connector 39 JLFP Front Panel Header 40 F_USB Front Panel USB 3.2 Gen 1 Connector 41 FP_1 Front Panel Header 42 PCIE11_PWR 2 x2 Pin P12V Slot11 Power Connector 43 IO_MCIO IO Singnal Connector 44 PCIE5_PWR 2 x2 Pin P12V Slot5 Power Connector 45 PWR_STBY 1x 2 Pin 12V Standby Power Connector 46 PWR_IN1 Input Power Connector 48 PWR_IN2 Input Power Connector 48 PWR_IN3 Input Power Connector | | | |
| 34 DC_SCI DC-SCM (BMC Module) Connector 35 U2_P0_4CA MCIO Connector Slot 11 (PCIe Gen5)_P2 36 U2_P0_4GE MCIO Connector Slot 11 (PCIe Gen5)_P1 37 BP_1 HDD Backplane Board Connector 38 ATX3 2 x 3 Pin ATX Power Connector 39 JLFP Front Panel Header 40 F_USB Front Panel USB 3.2 Gen 1 Connector 41 FP_1 Front Panel Header 42 PCIE11_PWR 2 x2 Pin P12V Slot11 Power Connector 43 IO_MCIO IO Singnal Connector 44 PCIE5_PWR 2 x2 Pin P12V Slot5 Power Connector 45 PWR_STBY 1x 2 Pin 12V Standby Power Connector 46 PWR_IN1 Input Power Connector 47 PWR_IN2 Input Power Connector 48 PWR_IN3 Input Power Connector | | | |
| 35 U2_P0_4CA MCIO Connector Slot 11 (PCIe Gen5)_P2 36 U2_P0_4GE MCIO Connector Slot 11 (PCIe Gen5)_P1 37 BP_1 HDD Backplane Board Connector 38 ATX3 2 x 3 Pin ATX Power Connector 39 JLFP Front Panel Header 40 F_USB Front Panel USB 3.2 Gen 1 Connector 41 FP_1 Front Panel Header 42 PCIE11_PWR 2 x2 Pin P12V Slot11 Power Connector 43 IO_MCIO IO Singnal Connector 44 PCIE5_PWR 2 x2 Pin P12V Slot5 Power Connector 45 PWR_STBY 1x 2 Pin 12V Standby Power Connector 46 PWR_IN1 Input Power Connector 47 PWR_IN2 Input Power Connector 48 PWR_IN3 Input Power Connector | | | , |
| 36 U2_P0_4GE MCIO Connector Slot 11 (PCIe Gen5)_P1 37 BP_1 HDD Backplane Board Connector 38 ATX3 2 x 3 Pin ATX Power Connector 39 JLFP Front Panel Header 40 F_USB Front Panel USB 3.2 Gen 1 Connector 41 FP_1 Front Panel Header 42 PCIE11_PWR 2 x2 Pin P12V Slot11 Power Connector 43 IO_MCIO IO Singnal Connector 44 PCIE5_PWR 2 x2 Pin P12V Slot5 Power Connector 45 PWR_STBY 1x 2 Pin 12V Standby Power Connector 46 PWR_IN1 Input Power Connector 47 PWR_IN2 Input Power Connector 48 PWR_IN3 Input Power Connector | | | , , |
| 37 BP 1 HDD Backplane Board Connector 38 ATX3 2 x 3 Pin ATX Power Connector 39 JLFP Front Panel Header 40 F_USB Front Panel USB 3.2 Gen 1 Connector 41 FP_1 Front Panel Header 42 PCIE11_PWR 2 x2 Pin P12V Slot11 Power Connector 43 IO_MCIO IO Singnal Connector 44 PCIE5_PWR 2 x2 Pin P12V Slot5 Power Connector 45 PWR_STBY 1x 2 Pin 12V Standby Power Connector 46 PWR_IN1 Input Power Connector 47 PWR_IN2 Input Power Connector 48 PWR_IN3 Input Power Connector | | | · /= |
| 38 ATX3 2 x 3 Pin ATX Power Connector 39 JLFP Front Panel Header 40 F_USB Front Panel USB 3.2 Gen 1 Connector 41 FP_1 Front Panel Header 42 PCIE11_PWR 2 x2 Pin P12V Slot11 Power Connector 43 IO_MCIO IO Singnal Connector 44 PCIE5_PWR 2 x2 Pin P12V Slot5 Power Connector 45 PWR_STBY 1x 2 Pin 12V Standby Power Connector 46 PWR_IN1 Input Power Connector 47 PWR_IN2 Input Power Connector 48 PWR_IN3 Input Power Connector | | | · · · · · · · · · · · · · · · · · · · |
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| 40 F_USB Front Panel USB 3.2 Gen 1 Connector 41 FP_1 Front Panel Header 42 PCIE11_PWR 2 x2 Pin P12V Slot11 Power Connector 43 IO_MCIO IO Singnal Connector 44 PCIE5_PWR 2 x2 Pin P12V Slot5 Power Connector 45 PWR_STBY 1x 2 Pin 12V Standby Power Connector 46 PWR_IN1 Input Power Connector 47 PWR_IN2 Input Power Connector 48 PWR_IN3 Input Power Connector | | | |
| 41 FP_1 Front Panel Header 42 PCIE11_PWR 2 x2 Pin P12V Slot11 Power Connector 43 IO_MCIO IO Singnal Connector 44 PCIE5_PWR 2 x2 Pin P12V Slot5 Power Connector 45 PWR_STBY 1x 2 Pin 12V Standby Power Connector 46 PWR_IN1 Input Power Connector 47 PWR_IN2 Input Power Connector 48 PWR_IN3 Input Power Connector | | | |
| 42 PCIE11_PWR 2 x2 Pin P12V Slot11 Power Connector 43 IO_MCIO IO Singnal Connector 44 PCIE5_PWR 2 x2 Pin P12V Slot5 Power Connector 45 PWR_STBY 1x 2 Pin 12V Standby Power Connector 46 PWR_IN1 Input Power Connector 47 PWR_IN2 Input Power Connector 48 PWR_IN3 Input Power Connector | | | |
| 43 IO_MCIO IO Singnal Connector 44 PCIE5_PWR 2 x2 Pin P12V Slot5 Power Connector 45 PWR_STBY 1x 2 Pin 12V Standby Power Connector 46 PWR_IN1 Input Power Connector 47 PWR_IN2 Input Power Connector 48 PWR_IN3 Input Power Connector | | | |
| 44 PCIE5 PWR 2 x2 Pin P12V Slot5 Power Connector 45 PWR_STBY 1x 2 Pin 12V Standby Power Connector 46 PWR_IN1 Input Power Connector 47 PWR_IN2 Input Power Connector 48 PWR_IN3 Input Power Connector | | | |
| 45 PWR_STBY 1x 2 Pin 12V Standby Power Connector 46 PWR_IN1 Input Power Connector 47 PWR_IN2 Input Power Connector 48 PWR_IN3 Input Power Connector | | | 1 |
| 46 PWR_IN1 Input Power Connector 47 PWR_IN2 Input Power Connector 48 PWR_IN3 Input Power Connector | | | |
| 47 PWR_IN2 Input Power Connector 48 PWR_IN3 Input Power Connector | | | |
| 48 PWR_IN3 Input Power Connector | | | - |
| | | _ | · · |
| | 49 | PWR IN4 | Input Power Connector |

4-2 Jumper Setting



4-3 Backplane Board Storage Connector

4-3-1 CBPX060



| Item | Description | |
|------|------------------------|--|
| 1 | MCIO Connector (U_2_0) | |
| 2 | MCIO Connector (U_2_1) | |
| 3 | MCIO Connector (U_2_2) | |
| 4 | MCIO Connector (U_2_3) | |
| 5 | MCIO Connector (U_2_4) | |
| 6 | MCIO Connector (U_2_5) | |

Chapter 5 BIOS Setup

BIOS (Basic Input and Output System) records hardware parameters of the system in the EFI on the motherboard. Its major functions include conducting the Power-On Self-Test (POST) during system startup, saving system parameters, loading the operating system etc. The BIOS includes a BIOS Setup program that allows the user to modify basic system configuration settings or to activate certain system features. When the power is turned off, the battery on the motherboard supplies the necessary power to the CMOS to keep the configuration values in the CMOS.

To access the BIOS Setup program, press the key during the POST when the power is turned on.



- BIOS flashing is potentially risky, if you do not encounter any problems when using the current BIOS version, it is recommended that you don't flash the BIOS. To flash the BIOS, do it with caution. Inadequate BIOS flashing may result in system malfunction.
- It is recommended that you not alter the default settings (unless you need to) to prevent system
 instability or other unexpected results. Inadequately altering the settings may result in system's
 failure to boot. If this occurs, try to clear the CMOS values and reset the board to default values.
 (Refer to the Exit section in this chapter or introductions of the battery/clearing CMOS jumper in
 Chapter 4 for how to clear the CMOS values.)

BIOS Setup Program Function Keys

| <←><→> | Move the selection bar to select the screen |
|-----------------|---|
| <↑><↓> | Move the selection bar to select an item |
| <+> | Increase the numeric value or make changes |
| <-> | Decrease the numeric value or make changes |
| <enter></enter> | Execute command or enter the submenu |
| <esc></esc> | Main Menu: Exit the BIOS Setup program |
| | Submenus: Exit current submenu |
| <f1></f1> | Show descriptions of general help |
| <f3></f3> | Restore the previous BIOS settings for the current submenus |
| <f9></f9> | Load the Optimized BIOS default settings for the current submenus |
| <f10></f10> | Save all the changes and exit the BIOS Setup program |
| | |

■ Main

This setup page includes all the items of the standard compatible BIOS.

Advanced

This setup page includes all the items of AMI BIOS special enhanced features. (ex: Auto detect fan and temperature status, automatically configure hard disk parameters.)

■ Chipset

This setup page includes all the submenu options for configuring the functions of the Platform Controller Hub.

■ Server Management

Server additional features enabled/disabled setup menus.

■ Security

Change, set, or disable supervisor and user password. Configuration supervisor password allows you to restrict access to the system and BIOS Setup.

A supervisor password allows you to make changes in BIOS Setup.

A user password only allows you to view the BIOS settings but not to make changes.

■ Boot

This setup page provides items for configuration of the boot sequence.

Save & Exit

Save all the changes made in the BIOS Setup program to the CMOS and exit BIOS Setup. (Pressing <F10> can also carry out this task.)

Abandon all changes and the previous settings remain in effect. Pressing <Y> to the confirmation message will exit BIOS Setup. (Pressing <Esc> can also carry out this task.)

5-1 The Main Menu

Once you enter the BIOS Setup program, the Main Menu (as shown below) appears on the screen. Use arrow keys to move among the items and press <Enter> to accept or enter other sub-menu.

Main Menu Help

The on-screen description of a highlighted setup option is displayed on the bottom line of the Main Menu.

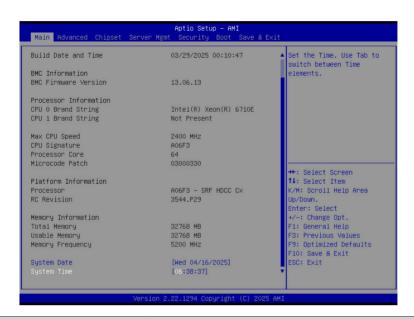
Submenu Help

While in a submenu, press <F1> to display a help screen (General Help) of function keys available for the menu. Press <Esc> to exit the help screen. Help for each item is in the Item Help block on the right side of the submenu.



- When the system is not stable as usual, select the **Restore Defaults** item to set your system to its defaults
- The BIOS Setup menus described in this chapter are for reference only and may differ by BIOS version.





| Parameter | Description | |
|--|---|--|
| BIOS Information | | |
| Project Name | Displays the project name information. | |
| Project Version | Displays version number of the BIOS setup utility. | |
| Build Date and Time | Displays the date and time when the BIOS setup utility was created. | |
| BMC Information ^(Note1) | | |
| BMC Firmware Version ^(Note1) | Displays BMC firmware version information. | |
| Processor Information | | |
| CPU Brand String/ Max CPU Speed / CPU Signature / Processor Core / Microcode Patch | Displays the technical information for the installed processor(s). | |
| Platform Information | | |
| Processor/ PCH/ RC Revision | Displays the information of the installed processor(s) and PCH. | |
| Memory Information ^(Note2) | | |
| Total Memory | Displays the total memory size of the installed memory. | |
| Usable Memory | Displays the usable memory size of the installed memory. | |

(Note1) Functions available on selected models.

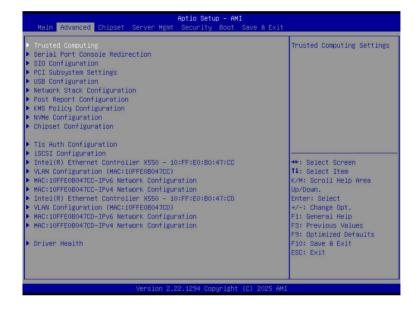
(Note2) This section will display capacity and frequency information of the memory that the customer has installed.

| Parameter | Description |
|--|---|
| Memory Frequency | Displays the frequency information of the installed memory. |
| Onboard LAN Information ^(Note3) | |
| LAN# MAC Address | Displays LAN MAC address information. |
| System Date | Sets the date following the weekday-month-day-year format. |
| System Time | Sets the system time following the hour-minute-second format. |

(Note3) The number of LAN ports listed will depend on the motherboard / system model.

5-2 Advanced Menu

The Advanced Menu displays submenu options for configuring the function of various hardware components. Select a submenu item, then press <Enter> to access the related submenu screen.

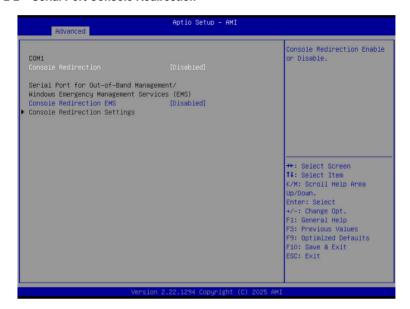


5-2-1 Trusted Computing



| Parameter | Description |
|------------------|--|
| Configuration | |
| TPM v1.2 Support | Enable/Disable BIOS support for security device. OS will not show security device. TCG EFI protocol and INT1A interface will not be available. Options available: Disable, Enable. Default setting is Enable . |

5-2-2 Serial Port Console Redirection



| Parameter | Description |
|---|--|
| COM1 Console Redirection ^(Note) | Console redirection enables the users to manage the system from a remote location. Options available: Enabled, Disabled. Default setting is Disabled . |
| COM1 Console Redirection Settings | Press [Enter] to configure advanced items. Please note that this item is configurable when COM1 Console Redirection is set to Enabled. Terminal Type Selects a terminal type to be used for console redirection. Options available: VT100, VT100PLUS, VT-UTF8, ANSI. Default setting is VT100PLUS. Bits per second Selects the transfer rate for console redirection. Options available: 9600, 19200, 38400, 57600, 115200. Default setting is 115200. Data Bits Selects the number of data bits used for console redirection. Options available: 7, 8. Default setting is 8. |

(Note)

Parameter

Description

Parity

- A parity bit can be sent with the data bits to detect some transmission errors.
- Even: parity bit is 0 if the num of 1's in the data bits is even.
- Odd: parity bit is 0 if num of 1's in the data bits is odd.
- Mark: parity bit is always 1. Space: Parity bit is always 0.
- Mark and Space Parity do not allow for error detection.
- Options available: None, Even, Odd, Mark, Space. Default setting is None

Stop Bits

- Stop bits indicate the end of a serial data packet. (A start bit indicates the beginning). The standard setting is 1 stop bit.
 Communication with slow devices may require more than 1 stop bit
- Options available: 1, 2. Default setting is 1.

Flow Control

- Flow control can prevent data loss from buffer overflow. When sending data, if the receiving buffers are full, a 'stop' signal can be sent to stop the data flow. Once the buffers are empty, a 'start' signal can be sent to re-start the flow. Hardware flow control uses two wires to send start/stop signals.
- Options available: None, Hardware RTS/CTS. Default setting is None.

VT-UTF8 Combo Key Support

- Enable/Disable the VT-UTF8 Combo Key Support.
- Options available: Enabled, Disabled. Default setting is **Enabled**.

Recorder Mode

- When this mode enabled, only texts will be send. This is to capture Terminal data.
- Options available: Enabled, Disabled. Default setting is **Disabled**.

Resolution 100x31

- Enable/Disable extended terminal resolution.
- Options available: Enabled, Disabled. Default setting is **Enabled**.

Putty KeyPad

- Selects Function Key and KeyPad on Putty.
- Options available: VT100, LINUX, XTERMR6, SC0, ESCN, VT400.
 Default setting is VT100.

COM1 Console Redirection Settings (continued)

| Parameter | Description |
|--|---|
| Serial Port for Out-of-Band Management / Windows Emergency Management Services (EMS) Console Redirection ^(Note) | EMS console redirection allows the user to configure Console Redirection Settings to support Out-of-Band Serial Port management. Options available: Enabled, Disabled. Default setting is Disabled . |
| Serial Port for Out-of-Band EMS Console Redirection Settings | Press [Enter] to configure advanced items. Please note that this item is configurable when Serial Port for Out-of-Band Management EMS Console Redirection is set to Enabled. ◆ Out-of-Band Mgmt Port − Microsoft Windows Emergency Management Service (EMS) allows for remote management of a Windows Server OS through a serial port. − Default setting is COM1. ◆ Terminal Type EMS − Selects a terminal type to be used for console redirection. − Options available: VT100, VT100PLUS, VT-UTF8, ANSI. Default setting is VT100PLUS. ◆ Bits per second EMS − Selects the transfer rate for console redirection. − Options available: 9600, 19200, 57600, 115200. Default setting is 115200. ◆ Flow Control EMS − Flow control can prevent data loss from buffer overflow. When sending data, if the receiving buffers are full, a 'stop' signal can be sent to stop the data flow. Once the buffers are empty, a 'start' signal can be sent to re-start the flow. Hardware flow control uses two wires to send start/stop signals. − Options available: None, Hardware RTS/CTS, Software Xon/Xoff. Default setting is None. |

5-2-3 SIO Configuration



| Parameter | Description |
|--|--|
| AMI SIO Driver Version | Displays the AMI SIO driver version information. |
| Super IO Chip Logical Device(s) Configuration | Displays the AMI SIO driver version information. Press [Enter] to configure advanced items. ◆ Use This Device − When set to Enabled allows you to configure the serial port settings. When set to Disabled, displays no configuration for the serial port. − Options available: Enabled, Disabled. Default setting is Enabled. ◆ Logical Device Settings/Current: − Displays the serial port base I/O address and IRQ. ◆ Possible: |
| [*Active*] Serial Port | - Configures the serial port base I/O address and IRQ. Use Automatic Settings IO=3F8h; IRQ=4; DMA; IO=3F8h; IRQ=4; DMA; IO=2F8h; IRQ=4; DMA; IO=3E8h; IRQ=4; DMA; IO=2E8h; IRQ=4; DMA; Default setting is Use Automatic Settings . |

5-2-4 PCI Subsystem Settings



Aptio Setup - AMI Advanced SLOTS Lanes [Auto] If system has SR-IOV capable PCIe Devices, this SLOT8 Max Link Speed [Auto] option Enables or Disables SLOT9 I/O ROM [Enabled] Single Root IO SLOT9 Lanes Virtualization Support. SLOT9 Max Link Speed [Auto] SLOT11 I/O ROM [Enabled] SLOT11 Lanes [Auto] SLOT11 Max Link Speed [Auto] [Enabled] M2B I/O ROM LAN I/O ROM [Enabled] ++: Select Screen ↑↓: Select Item LAN Lanes [Auto] LAN Max Link Speed [Auto] K/M: Scroll Help Area Up/Down. M2A I/O ROM [Enabled] Enter: Select +/-: Change Opt. F1: General Help PCI Devices Common Settings: F3: Previous Values Re-Size BAR Support SR-IOV Support F9: Optimized Defaults [Disabled] [Enabled] F10: Save & Exit

(Note1) This section is dependent on the available OCP Slot.

(Note2) This section is dependent on the available PCle Slot.

| Parameter | Description |
|---|--|
| PCI Bus Driver Version | Displays the PCI Bus Driver version information. |
| OCP# I/O ROM(Note1) | When enabled, this setting will initialize the device expansion ROM for the related PCI-E slot. Options available: Enabled, Disabled. Default setting is Enabled . |
| OCP# Lanes(Note1) | Change the PCle lanes. Default setting is Auto. |
| OCP# Max Link Speed ^(Note1) | Configure PCIe max link speed. Options available: Auto, Gen1, Gen2, Gen3, Gen4, Gen5. Default setting is Auto . |
| SLOT# I/O ROM ^(Note2) | When enabled, this setting will initialize the device expansion ROM for the related PCI-E slot. Options available: Enabled, Disabled. Default setting is Enabled . |
| SLOT# Lanes ^(Note2) | Change the PCIe lanes. Default setting is Auto. |
| SLOT# Max Link Speed ^(Note2) | Configure PCIe max link speed. Options available: Auto, Gen1, Gen2, Gen3, Gen4, Gen5. Default setting is Auto . |
| Onboard LAN1/ LAN2 I/O Controller ^(Note3) | Enable/Disable the onboard LAN controller. Options available: Enabled, Disabled. Default setting is Enabled . |
| Onboard LAN1 I/O ROM(Note3) | Enable/Disable the onboard LAN devices, and initializes device expansion ROM. Options available: Enabled, Disabled. Default setting is Enabled . |
| PCI Devices Common Settings | |
| Above 4G Decoding | Enable/Disable memory mapped I/O to 4GB or greater address space (Above 4G Decoding). Options available: Enabled, Disabled. Default setting is Enabled . |
| SR-IOV Support | If the system has SR-IOV capable PCIe devices, this item Enable/Disable Single Root IO Virtualization Support. Options available: Enabled, Disabled. Default setting is Enabled . |

(Note3) This section is dependent on the available LAN controller.

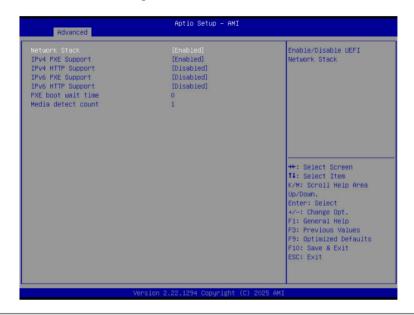
5-2-5 USB Configuration



| Parameter | Description | |
|--|---|--|
| USB Configuration | | |
| USB Devices: | Displays the USB devices connected to the system. | |
| XHCI Hand-off | Enable/Disable the XHCI (USB 3.0) Hand-off support. Options available: Enabled, Disabled. Default setting is Enabled . | |
| USB Mass Storage Driver Support ^(Note) | Enable/Disable the USB Mass Storage Driver Support. Options available: Enabled, Disabled. Default setting is Enabled . | |
| Port 60/64 Emulation | Enables the I/O port 60h/64h emulation support. This should be enabled for the complete USB Keyboard Legacy support for non-USB aware OSes. Options available: Enabled, Disabled. Default setting is Enabled . | |

(Note) This item is present only if you attach USB devices.

5-2-6 Network Stack Configuration



| Parameter | Description |
|--------------------|--|
| Network Stack | Enable/Disable the UEFI network stack. Options available: Enabled, Disabled. Default setting is Enabled . |
| Ipv4 PXE Support | Enable/Disable the Ipv4 PXE feature. Options available: Enabled, Disabled. Default setting is Enabled . |
| Ipv4 HTTP Support | Enable/Disable the lpv4 HTTP feature. Options available: Enabled, Disabled. Default setting is Disabled . |
| Ipv6 PXE Support | Enable/Disable the Ipv6 PXE feature. Options available: Enabled, Disabled. Default setting is Disabled . |
| Ipv6 HTTP Support | Enable/Disable the Ipv6 HTTP feature. Options available: Enabled, Disabled. Default setting is Disabled . |
| PXE boot wait time | Wait time in seconds to press ESC key to abort the PXE boot. Press the <+> / <-> keys to increase or decrease the desired values. |
| Media detect count | Number of times the presence of media will be checked. Press the <+> / <-> keys to increase or decrease the desired values. |

5-2-7 Post Report Configuration



| Parameter Description | |
|---------------------------|--|
| Post Report Configuration | |
| Error Message Report | |
| Post Error Message | Enable/Disable the POST Error Message support. Options available: Enabled, Disabled. Default setting is Enabled . |
| Halt On | Options available: No Error, All Error. Default setting is No Error . |

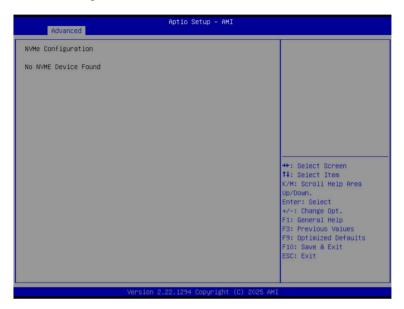
5-2-8 KMS Policy Configuration





| Parameter | Description | |
|-----------------------------|---|--|
| KMS Option | Options available: KMS with KMIP, Disabled. Default setting is KMS with KMIP . | |
| KMS KMIP Server Retry Count | Define KMS KMIP Server Retry Count. | |

5-2-9 NVMe Configuration



| Parameter | Description |
|--------------------|---|
| NVMe Configuration | Displays the NVMe devices connected to the system. |
| NVMe OPROM Select | Options available: BIOS Build-In, NVMe Device. Default setting is BIOS Build-In. |

5-2-10 Chipset Configuration



| Parameter | Description |
|--|---|
| Restore on AC Power Loss ^(Note) | Defines the power state to resume to after a system shutdown that is due to an interruption in AC power. When set to Last State, the system will return to the active power state prior to shutdown. When set to Power Off, the system remains off after power shutdown. Options available: Last State, Power Off, Power On, Unspecified. The default setting depends on the BMC setting. |
| P2P Bridge IO Size | Specifies P2P Bridge IO aligned to the size. Options available: 0x100, 0x150, 0x1000. Default setting is 0x1000 . |
| SATA HDD Security Frozen | Enable/Disable this item to send freeze lock command to SATA HDD. Options available: Enabled, Disabled. Default setting is Enabled . |
| NVMe SSD Security Frozen | Attempt to send freeze lock command to NVMe SSDs during boot. Options available: Enabled, Disabled. Default setting is Enabled . |
| Chassis Opened Warning | Enable/Disable the chassis intrusion alert function. Options available: Enabled, Disabled, Clear. Default setting is Disabled. |

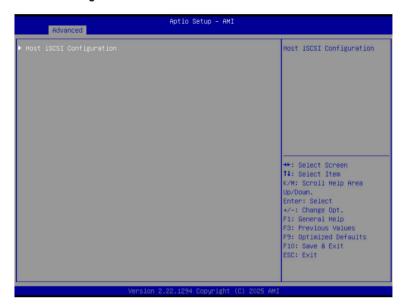
(Note) When the power policy is controlled by BMC, please wait for 15-20 seconds for BMC to save the last power state.

5-2-11 TIs Auth Configuration



| Parameter | Description |
|---------------------------|--|
| | Press [Enter] for configuration of advanced items. |
| | Enroll Cert |
| | Press [Enter] to enroll a certificate |
| | Enroll Cert Using File |
| Conver CA Configuration | Cert GUID |
| Server CA Configuration | Input digit character in 1111111-2222-3333-4444-1234567890ab |
| | format. |
| | Commit Changes and Exit |
| | Discard Changes and Exit |
| | Delete Cert |
| Client Cert Configuration | Press [Enter] for configuration of advanced items. |

5-2-12 iSCSI Configuration



| Parameter | Description |
|--------------------------|--|
| Host iSCSI Configuration | Press [Enter] to configure advanced items. • iSCSI Initiator Name - Only IQN format is accepted. Range: from 4 to 223 • Add an Attempt • Delete Attempts • Change Attempt Order |

5-2-13 Intel(R) Ethernet Controller X550

| | version information. |
|--------------------------------------|---|
| | |
| 0 | |
| Intel(R) 10GbE Driver 8.1.00 x64 | |
| 000000-000 | |
| Intel(R) Ethernet Controller X550 | |
| Intel X550 | |
| 1563 | |
| D6:00:00 | |
| | →+: Select Screen |
| [Disconnected] | ↑↓: Select Item |
| | K/M: Scroll Help Area |
| 10:FF:E0:B0:47:CC | Up/Down. |
| 00:00:00:00:00 | Enter: Select |
| | +/-: Change Opt. |
| | F1: General Help |
| | F3: Previous Values |
| | F9: Optimized Defaults |
| | F10: Save & Exit |
| | ESC: Exit |
| | Intel(R) 10GbE Driver 8.1.00 x64 000000-000 Intel(R) Ethernet Controller X550 Intel X550 1563 D6:00:00 [Disconnected] |





| Parameter | Description |
|---------------------|---|
| NIC Configuration | Press [Enter] to configure advanced items. ◆ Link Speed - Allows for automatic link speed adjustment. - Options available: Auto Negotiated, 10 Mbps Half, 10 Mbps Full, 100 Mbps Half, 100 Mbps Full. ◆ Wake On LAN - Enables power on of the system via LAN. Note that configuring Wake on LAN in the operating system does not change the value of this setting, but does override the behavior of Wake on LAN in OS controlled power states. - Options available: Enabled, Disabled. |
| Blink LEDs | Identifies the physical network port by blinking the associated LED. Press the numeric keys to adjust desired values (up to 15 seconds). |
| UEFI Driver | Displays the technical specifications for the Network Interface Controller. |
| Adapter PBA | Displays the technical specifications for the Network Interface Controller. |
| Device Name | Displays the technical specifications for the Network Interface Controller. |
| Chip Type | Displays the technical specifications for the Network Interface Controller. |
| PCI Device ID | Displays the technical specifications for the Network Interface Controller. |
| PCI Address | Displays the technical specifications for the Network Interface Controller. |
| Link Status | Displays the technical specifications for the Network Interface Controller. |
| MAC Address | Displays the technical specifications for the Network Interface Controller. |
| Virtual MAC Address | Displays the technical specifications for the Network Interface Controller. |

5-2-14 VLAN Configuration



| Parameter | Description |
|--------------------------|---|
| Enter Configuration Menu | Press [Enter] to configure advanced items. Create new VLAN VLAN ID Sets VLAN ID for a new VLAN or an existing VLAN. Press the <+> / <-> keys to increase or decrease the desired values. The valid range is from 0 to 4094. Priority Sets 802.1Q Priority for a new VLAN or an existing VLAN. Press the <+> / <-> keys to increase or decrease the desired values. The valid range is from 0 to 7. Add VLAN Press [Enter] to create a new VLAN or update an existing VLAN. Configured VLAN List Remove VLAN Press [Enter] to remove an existing VLAN. |

5-2-15 Driver Health



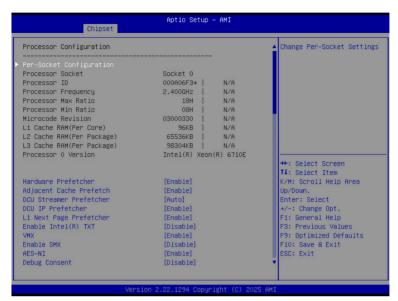
| Parameter | Description |
|---------------|---|
| Driver Health | Displays driver health status of the devices/controllers if installed |

5-3 Chipset Menu

Chipset Setup menu displays submenu options for configuring the function of Platform Controller Hub(PCH). Select a submenu item, then press <Enter> to access the related submenu screen.



5-3-1 Processor Configuration

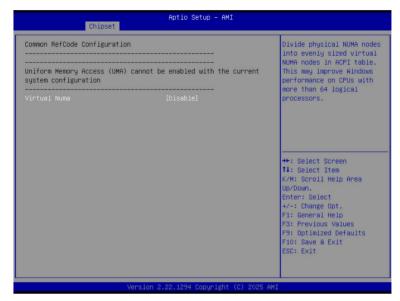


Aptio Setup - AMI Chipset In Field Scan (IFS) Processor Reserved Memory [Outputs] PRMRR Size per domain 16 MiB PRM Size per socket 16 MiB PRM Size per system 16 MiB Software Guard Extension (SGX) [Outputs] SGX activation state Deactivated SGX memory population for SGX enabling is not POR. Please check your memory population. SGX error code [HEX] ++: Select Screen ↑↓: Select Item Software Guard Extension (SGX) [Inputs] K/M: Scroll Help Area SGX Factory Reset [Disabled] SW Guard Extensions (SGX) [Disabled] SGX Package Info In-Band Access [Disabled] SGX PRNRR Size Requested [Auto] Up/Down. Enter: Select +/-: Change Opt. F1: General Help F3: Previous Values In Field Scan (IFS) F9: Optimized Defaults F10: Save & Exit

| Parameter | Description |
|--|---|
| Processor Configuration | |
| Pre-Socket Configuration | Press [Enter] to configure advanced items. CPU Socket 0 Configuration Core Disable Bitmap(Hex) Number of Cores to enable. 0 means all cores. FFFFFFF means to disable all cores. The maximum value depends on the number of CPUs available. Press the numeric keys to adjust desired values. |
| Processor Socket / Processor ID / Processor Die Type / Processor Frequency / Processor Max Ratio / Processor Min Ratio / Microcode Revision / L1 Cache RAM(Per Core) / L2 Cache RAM(Per Package) / Processor # Version | Displays the technical specifications for the installed processor(s). |
| Enable LP [Global] | Enables Logical processor (Software Method to Enable/Disable Logical Processor threads). Options available: ALL LPs, Single LP. Default setting is ALL LPs . |
| Hardware Prefetcher | Select whether to enable the speculative prefetch unit of the processor. Options available: Enable, Disable. Default setting is Enable . |
| L2 RF0 Prefetch Disable | Options available: Enable, Disable. Default setting is Disable . |
| Adjacent Cache Prefetch | When enabled, cache lines are fetched in pairs. When disabled, only the required cache line is fetched. Options available: Enable, Disable. Default setting is Enable . |
| DCU Streamer Prefetcher | Enable/Disable DCU streamer prefetcher. Options available: Enable, Disable. Default setting is Enable . |
| DCU IP Prefetcher | Enable/Disable DCU IP Prefetcher. Options available: Enable, Disable. Default setting is Enable . |
| Extended APIC | Enable/Disable extended APIC support. Note: The VT-d will be enabled automatically when x2APIC is enabled. Options available: Enable, Disable. Default setting is Enable. |
| Enable Intel(R) TXT | Enable/Disable the Intel Trusted Execution Technology support function. Options available: Enable, Disable. Default setting is Disable. |
| VMX | Enable/Disable the Vanderpool Technology. This will take effect after rebooting the system. Options available: Enable, Disable. Default setting is Enable . |
| Enable SMX | Enable/Disable the Safer Mode Extensions (SMX) support function. Options available: Enable, Disable. Default setting is Disable . |
| AES-NI | Enable/Disable the AES-NI support. Options available: Enable, Disable. Default setting is Enable . |
| Debug Consent | Options available: Enable, Disable. Default setting is Disable . |

| Parameter | Description |
|---|---|
| Memory Encryption (TME)(Note) | Enable/Disable memory encryption (TME). Options available: Enabled, Disabled. Default setting is Disabled . |
| Total Memory Encryption Multi-Tenant (TME-MT) | Options available: Enabled, Disabled. Default setting is Disabled . |
| Processor CFR Configuration | Press [Enter] to configure advanced items. Provision S3M CFR Options available: Disable, Enable. Default setting is Enable. Manual Commit S3M FW CFR Options available: Disable, Enable, Auto. Default setting is Auto. Provision PUcode CFR Options available: Disable, Enable. Default setting is Enable. Manual Commit PUcode CFR Options available: Enable, Disable, Auto. Default setting is Auto. Socket0 CFR Revision Info Displays CFR Revision information of the socket. |

5-3-2 Common RefCode Configuration



| Parameter | Description |
|------------------------------|--|
| Common RefCode Configuration | |
| Numa | Enable/Disable Non uniform Memory Access(NUMA). Default setting is Enable . |
| Virtual Numa | Divide physical NUMA nodes into evenly sized virtual NUMA nodes in ACPI table. This may improve Windows performance on CPUs with more than 64 logical processors. Options available: Enable, Disable. Default setting is Disable . |

5-3-3 UPI Configuration



| Parameter | Description |
|------------------------------|--|
| | Press [Enter] to configure advanced items. |
| Uncore General Configuration | n Uncore Status |
| | Press [Enter] to view the Uncore status. |

5-3-4 Memory Configuration



| Parameter | Description |
|---|---|
| Integrated Memory Controller (iMC) | |
| | When set to Enable, the system enforces Plan Of Record restrictions |
| Enforce DDR Memory Frequency POR for DDR frequency programming. | |
| | Options available: POR , Disable. |
| Enforce Population POR | Default setting is Enable . |
| CXL Noncompliant Device Support | Default setting is Disable . |
| Host Memory Frequency | Default setting is Auto . |

| Parameter | Description |
|-------------------------------|---|
| Memory Topology | Press [Enter] to view memory topology with DIMM population information. |
| Memory Map ^(Note1) | Press [Enter] to configure advanced items. ◆ Volatile Memory Mode - Selects 1LM or 2LM mode for volatile memory. - Options available: 1LM, 2LM. Default setting is 2LM. Press [Enter] to configure advanced items. |
| Memory RAS Configuration | Mirror Mode (Note2) Mirror Mode will set entire 1LM memory in system to be mirrored, consequently reducing the memory capacity by half. Enables the Mirror Mode will disable the XPT Prefetch. Options available: Disabled, Full Mirror Mode, Partial Mirror Mode. Default setting is Disabled. Partial Mirror 1 Size (GB) Selects multiplier of 1GB for the size of the SAD to be created. Correctable Error Threshold (0x01-0x7fff) used for sparing, and leaky bucket. Press the <+> / <-> keys to increase or decrease the desired values. Trigger SW Error Threshold(Note2) Enable/Disable Sparing trigger SW Error Match Threshold. Options available: Disabled, Enabled. Default setting is Disabled. SW Per Bank Threshold SW Per Bank Threshold (1-0x7FFF) used for DDR bank level error. Press the <+> / <-> keys to increase or decrease the desired values. SW Correctable Error Time Window SW Correctable Error Time window based interface in hour (0-24). Press the <+> / <-> keys to increase or decrease the desired values. Leaky bucket time window based interface(Note2) Enable/Disable leaky bucket time window based interface. Options available: Disabled, Enabled. Default setting is Disabled. |

(Note1) Advanced items prompt when HBM CPU is installed.

(Note2) Advanced items prompt when this item is defined.

5-3-5 IIO Configuration



| Parameter | Description |
|-----------------------------------|---|
| IIO Configuration | |
| Intel® VT for Directed I/O (VT-d) | Press [Enter] to configure advanced items. Intel® VT for Directed I/O Enable/Disable the Intel VT for Directed I/O (VT-d) support function by reporting the I/O device assignment to VMM through DMAR ACPI Tables. Options available: Enable, Disable. Default setting is Enable. ACS Control Enable: Programs ACS only to Chipset PCle Root Ports Bridges. Disable: Programs ACS to all PCle bridges. Default setting is Enable. Cache Allocation Options available: Enable, Disable. Default setting is Enable. Opt-Out Illegal MSI Mitigation Enable/Disable Opt-Out Illegal 0xFEE Platform Mitigation. Options available: Disable, Enable. Default setting is Disable. DMA Control Opt-In Flag Enable/Disable DMA_CTRL_PLATFORM_OPT_IN_FLAG in DMAR table in ACPI. Not compatible with Direct Device Assignment (DDA). Options available: Enable, Disable. Default setting is Disable. |

| Parameter | Description |
|-----------------------|---|
| | Interrupt Remapping Enable/Disable the interrupt remapping support function. Options available: Auto, Enable, Disable. Default setting is Auto x2APIC Opt Out Options available: Enable, Disable. Default setting is Disable. Pre-boot DMA Protection Options available: Enable, Disable. Default setting is Disable. |
| Intel® VMD technology | Press [Enter] to configure advanced items. Intel® VMD Configuration Enable/Disable Intel® VMD technology. Options available: Enable, Disable. Default setting is Disable. Intel® VMD for Non-Hotplug NVMe(Note) Enable/Disable Intel® VMD for Non-Hotplug NVMe. Options available: Enable, Disable. Default setting is Disable. |

5-3-6 Advanced Power Management Configuration



| Parameter | Description |
|---------------------------|---|
| CPU P State Control | Press [Enter] to configure advanced items. SpeedStep (Pstates) Conventional Intel SpeedStep Technology switches both voltage and frequency in tandem between high and low levels in response to processor load. Options available: Enable, Disable. Default setting is Enable. Turbo Mode When this item is enabled, the processor will automatically ramp up the clock speed of 1-2 of its processing cores to improve its performance. When this item is disabled, the processor will not overclock any of its core. Options available: Enable, Disable. Default setting is Enable. |
| Hardware PM State Control | Press [Enter] to configure advanced items. ◆ Hardware P-States - When this item is disabled, the processor hardware chooses a P-state based on OS Request (Legacy P-States). - In Native mode, the processor hardware chooses a P-state based on OS guidance. - In Out of Band mode, the processor hardware autonomously chooses a P-state (with no OS guidance). - Options available: Disable, Native Mode, Out of Band Mode, Native Mode with No Legacy Support. Default setting is Native Mode. |

| Parameter | Description |
|--------------------------|---|
| CPU C State Control | Press [Enter] to configure advanced items. • Enable Monitor MWAIT - Allows Monitor and MWAIT instructions. - Options available: Disable, Enable, Auto. Default setting is Auto. • CPU C6 Report - Enable/Disable CPU C6(ACPI C3) report to OS. - Options available: Disable, Enable, Auto. Default setting is Auto. • Enhanced Halt State (C1E) - Core C1E auto promotion control. Takes effect after reboot. - Options available: Enable, Disable. Default setting is Enable. |
| Package C State Control | Press [Enter] to configure advanced items. Package C State Configures the state for the C-State package limit. Options available: C0/C1 state, C2 state, C6(non Retention) state, C6(Retention) state, No Limit, Auto. Default setting is Auto. |
| CPU - Advanced PM Tuning | Press [Enter] to configure advanced items. ◆ Energy Perf BIAS − Press [Enter] to configure advanced items. » Power Performance Tuning • Options available: OS Controls EPB, BIOS Controls EPB, PECI Controls EPB. Default setting is OS Controls EPB. » Energy_PERF_BIAS_CFG mode ^(Note) • Options available: Performance, Balanced Performance, Balanced Power, Power. Default setting is Balanced Performance. |

5-3-7 Miscellaneous Configuration



| Parameter | Description |
|-----------------------------|---|
| Miscellaneous Configuration | |
| Active Video | Selects the active video type. |
| | Options available: Auto, Onboard Device, PCIE Device, Specific PCIE |
| | Device. Default setting is Auto . |
| External SSC - CK440 | Enables Spread spectrum - only affects external clock generator. |
| | Options available: SSC Off, SSC = -0.3%, SSC = -0.5%, Hardware. |
| | Default setting is SSC Off. |

5-3-8 Runtime Error Logging Settings



| Parameter | Description |
|-----------------------------|--|
| Runtime Error Logging | |
| 0 -1 | Enable/Disable system error logging function. |
| System Errors | Options available: Enable , Disable. |
| CAN From Injection Connect | Enable/Disable software injection error logging function. |
| S/W Error Injection Support | Options available: Enable, Disable . |
| | Press [Enter] to configure advanced items. |
| Whea Settings | WHEA (Windows Hardware Error Architecture) Support |
| whea Sellings | Enable/Disable WHEA Support. |
| | Options available: Enable, Disable. |
| | Press [Enter] to configure advanced items. |
| Memory Error Enabling | Memory Corrected Error |
| | Enable/Disable Memory Corrected Error. |
| | Options available: Enable, Disable. |
| | Uncorrected Error disable Memory |
| | Enable/Disable the Memory that triggers Uncorrected Error. |
| | Options available: Enable, Disable. |

| Parameter | Description |
|---------------------|--|
| PCIe Error Enabling | Press [Enter] to configure advanced items. PCIE Error Enable/Disable PCIE error. Options available: Enable, Disable. Uncorrected Error ^(Note) Enables and escalates Uncorrectable/Recoverable Errors to error pins. Options available: Enable, Disable. Fatal Error Enable ^(Note) Enables and escalates Fatal Errors to error pins. Options available: Enable, Disable. Assert NMI on SERR ^(Note) Enable/Disable BIOS generates a non-maskable interrupt (NMI) and logs an error when a system error (SERR) occurs. Options available: Enable, Disable. Assert NMI on PERR ^(Note) Enable/Disable BIOS generates a non-maskable interrupt (NMI) and logs an error when a processor bus parity error (PERR) occurs. Options available: Enable, Disable. |

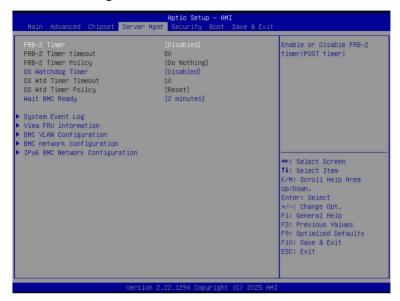
5-3-9 Power Policy



| Parameter | Description |
|-----------------------------|---|
| Power Policy Quick Settings | Selects a Power Policy Quick Setting. |
| | Options available: Standard , Best Performance, Energy Efficient. |
| | Conventional Intel SpeedStep Technology switches both voltage and |
| ChandCton (Datatas) | frequency in tandem between high and low levels in response to processor |
| SpeedStep (Pstates) | load. |
| | Options available: Enable, Disable. |
| | When this item is enabled, the processor will automatically ramp up the |
| Turbo Mada | clock speed of 1-2 of its processing cores to improve its performance. |
| Turbo Mode | When this item is disabled, the processor will not overclock any of its core. |
| | Options available: Enable, Disable |
| | Enable/Disable the BIOS to enable the report from the CPU C6 state (ACPI |
| CPU C6 report | C3) to the OS. |
| · | Options available: Disable, Enable, Auto . |
| | Enable/Disable the C1E support for lower power consumption. Takes effect |
| Enhanced Halt State (C1E) | after reboot. |
| | Options available: Enable , Disable. |
| | Configures the C-State package limit. |
| Package C State | Options available: C0/C1 state, C2 state, C6(non Retention) state, |
| | C6(Retention) state, No Limit, Auto. |

| Parameter | Description |
|----------------------------|---|
| | Enables Logical processor (Software Method to Enable/Disable Logical |
| Enable LP [Global] | Processor threads). |
| | Options available: ALL LPs., Single LP. |
| Hardware Prefetcher | Options available: Enable, Disable. |
| Adjacent Cache Prefetch | Options available: Enable, Disable. |
| DCU Streamer Prefetcher | Options available: Enable, Disable. |
| | Enable/Disable the Intel VT for Directed I/O (VT-d) support function by |
| Intel® VT for Directed I/O | reporting the I/O device assignment to VMM through DMAR ACPI Tables. |
| | Options available: Enable, Disable. |

5-4 Server Management Menu



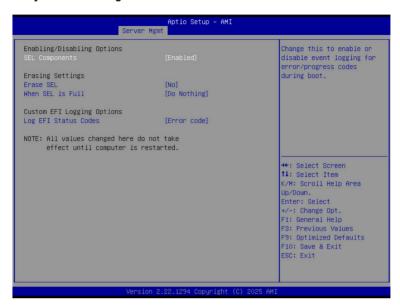
| Parameter | Description |
|--|---|
| FRB-2 Timer | Enable/Disable FRB-2 timer (POST timer). Options available: Enabled, Disabled. Default setting is Enabled . |
| FRB-2 Timer ^(Note1) timeout | Configures the FRB2 Timer timeout. The value is between 1 to 30 minutes. Default setting is 6 minutes . |
| FRB-2 Timer Policy ^(Note1) | Configures the FRB2 Timer policy. Options available: Do Nothing, Reset, Power Down, Power Cycle. Default setting is Do Nothing . |
| OS Watchdog Timer | Enable/Disable OS Watchdog Timer function. Options available: Enabled, Disabled. Default setting is Disabled . |
| OS Wtd Timer Timeout ^(Note2) | Configures OS Watchdog Timer. The value is between 1 to 30 minutes. Default setting is 10 minutes . |
| OS Wtd Timer Policy ^(Note2) | Configure OS Watchdog Timer Policy. Options available: Reset, Do Nothing, Power Down, Power Cycle. Default setting is Reset . |
| Wait BMC Ready | POST wait BMC ready and reboot system. Options available: Disabled, 2 minutes, 4 minutes, 6 minutes. Default setting is 2 minutes. |

(Note1) This item is configurable when FRB-2 Timer is set to Enabled.

(Note2) This item is configurable when OS Watchdog Timer is set to Enabled.

| Parameter | Description |
|-----------------------------------|--|
| System Event Log | Press [Enter] to configure advanced items. |
| View FRU Information | Press [Enter] to view the FRU information. |
| BMC VLAN Configuration | Press [Enter] to configure advanced items. |
| BMC network Configuration | Press [Enter] to configure advanced items. |
| IPv6 BMC Network Configuration | Press [Enter] to configure advanced items. |

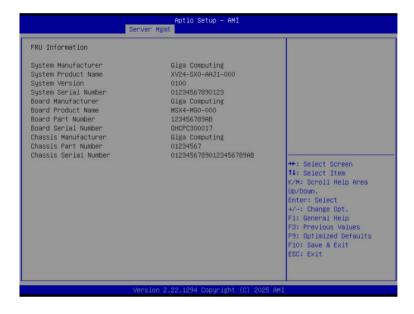
5-4-1 System Event Log



| Parameter | Description |
|------------------------------|--|
| Enabling / Disabling Options | |
| SEL Components | Change this item to enable or disable all features of System Event Logging during boot. Options available: Enabled, Disabled. Default setting is Enabled . |
| Erasing Settings | |
| Erase SEL | Choose options for erasing SEL. Options available: No, Yes, On next reset, Yes, On every reset. Default setting is No . |
| When SEL is Full | Choose options for reactions to a full SEL. Options available: Do Nothing, Erase Immediately, Delete Oldest Record. Default setting is Do Nothing . |
| Custom EFI Logging Options | |
| Log EFI Status Codes | Enable/Disable the logging of EFI Status Codes (if not already converted to legacy). Options available: Disabled, Both, Error code, Progress code. Default setting is Error code. |

5-4-2 View FRU Information

The FRU page is a simple display page for basic system ID information, as well as System product information. Items on this window are non-configurable.



5-4-3 BMC VLAN Configuration



| Parameter | Description |
|------------------------|--|
| BMC VLAN Configuration | |
| BMC VLAN ID | Select to configure BMC VLAN ID. The valid range is from 0 to 4094. When set to 0, BMC VLAN ID will be disabled. |
| BMC VLAN Priority | Select to configure BMC VLAN Priority. The valid range is from 0 to 7. When BMC VLAN ID is set to 0, BMC VLAN Priority will not be selected. |

5-4-4 BMC Network Configuration



| Parameter | Description |
|-----------------------------------|---|
| BMC network configuration | |
| Select NCSI and Dedicated LAN | Options available: Do Nothing, Model1(Dedicated), Model2(NCSI), Mode3(Failover). Default setting is Do Nothing . |
| Lan Channel 1 | |
| Configuration Address source | Selects to configure LAN channel parameters statically or dynamically (DHCP). Options available: Unspecified, Static, DynamicBmcDhcp. Default setting is DynamicBmcDhcp . |
| Station IP address | Displays IP Address information. |
| Subnet mask | Displays Subnet Mask information. Please note that the IP address must be in three digitals, for example, 192.168.000.001. |
| Router IP address | Displays the Router IP Address information. |
| Station MAC address | Displays the MAC Address information. |
| Real-time get BMC network address | Press [Enter] will set LAN mode and Address source and then get IP, Subnet, Gateway and MAC address. |

5-4-5 IPv6 BMC Network Configuration



| Parameter | Description |
|---|---|
| IPv6 BMC network configuration | |
| IPv6 BMC Lan Channel 1 | |
| IPv6 BMC Lan Option | Enable/Disable IPv6 BMC LAN channel function. When this item is disabled, the system will not modify any BMC network during BIOS phase. Options available: Unspecified, Disable, Enable. Default setting is Enable. |
| IPv6 BMC Lan IP Address Source | Selects to configure LAN channel parameters statically or dynamically (by BIOS or BMC). Options available: Unspecified, Static, Dynamic-Obtained by BMC running DHCP. Default setting is Dynamic-Obtained by BMC running DHCP . |
| IPv6 BMC Lan IP Address/ Prefix Length | Check if the IPv6 BMC LAN IP address matches those displayed on the screen. |

5-5 Security Menu

The Security menu allows you to safeguard and protect the system from unauthorized use by setting up access passwords.



There are two types of passwords that you can set:

· Administrator Password

Entering this password will allow the user to access and change all settings in the Setup Utility.

User Password

Entering this password will restrict a user's access to the Setup menus. To enable or disable this field, a Administrator Password must first be set. A user can only access and modify the System Time, System Date, and Set User Password fields.

| Parameter | Description |
|------------------------|--|
| Administrator Password | Press [Enter] to configure the administrator password. |
| User Password | Press [Enter] to configure the user password. |
| Secure Boot | Press [Enter] to configure advanced items. |

5-5-1 Secure Boot

The Secure Boot feature is applicable if supported by your Operating System. If your Operating System is not supporting Secure Boot, the system will hang when starting the Operating System.



| Parameter | Description |
|------------------------------------|--|
| System Mode | Displays if the system is in User mode or Setup mode. |
| Secure Boot | Enable/ Disable the Secure Boot function. Options available: Enabled, Disabled. Default setting is Disabled . |
| Secure Boot Mode ^(Note) | Secure Boot requires all the applications that are running during the booting process to be pre-signed with valid digital certificates. This way, the system knows all files being loaded before the Operating System loads to the login screen have not been tampered with. When set to Standard, it will automatically load the Secure Boot keys form the BIOS databases. When set to Custom, you can customize the Secure Boot settings and manually load its keys from the BIOS database. Options available: Standard, Custom. Default setting is Custom. |
| Restore Factory Keys | Forces the system to user mode and installs factory default Secure Boot key database. |
| Reset To Setup Mode | Reset the system to Setup Mode. |

| Parameter | |
|-----------|--|
| | |

Description

Press [Enter] to configure advanced items.

Please note that this item is configurable when Secure Boot Mode is set to Custom.

- Factory Key Provision
 - Allows to provision factory default Secure Boot keys when system is in Setup Mode.
 - Options available: Enabled, Disabled. Default setting is **Disabled**.
- Restore Factory Keys
 - Installs all factory default keys. It will force the system in User Mode.
 - Options available: Yes, No.
- Reset To Setup Mode
 - Reset the system to Setup Mode.
 - Options available: Yes, No.
- Enroll Efi Image
 - Press [Enter] to enroll SHA256 hash of the binary into Authorized Signature Database (db).
- Export Secure Boot variables
 - Copy NVRAM content of Secure Boot variables to files in a root folder on a file system device.

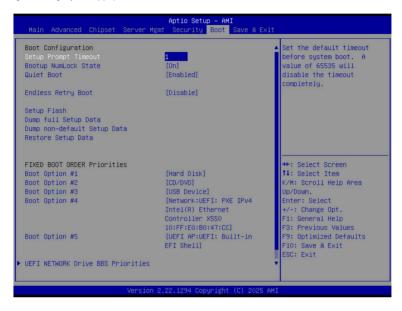
Key Management

- Secure Boot variable
 - Displays the current status of the variables used for secure boot.
- Platform Key (PK)
 - Displays the current status of the Platform Key (PK).
 - Press [Enter] to configure a new PK.
 - Options available: Update.
- Key Exchange Keys (KEK)
 - Displays the current status of the Key Exchange Key Database (KEK).
 - Press [Enter] to configure a new KEK or load additional KEK from storage devices.
 - Options available: Update, Append.
- Authorized Signatures (DB)
 - Displays the current status of the Authorized Signature Database.
 - Press [Enter] to configure a new DB or load additional DB from storage devices.
 - Options available: Update, Append.
- Forbidden Signatures (DBX)
 - Displays the current status of the Forbidden Signature Database.
 - Press [Enter] to configure a new dbx or load additional dbx from storage devices.
 - Options available: Update, Append.

| Parameter | Description |
|-------------------------------|---|
| Key Management (continued) | Authorized TimeStamps (DBT) Displays the current status of the Authorized TimeStamps Database. Press [Enter] to configure a new DBT or load additional DBT from storage devices. Options available: Update, Append. OsRecovery Signatures Displays the current status of the OsRecovery Signature Database. Press [Enter] to configure a new OsRecovery Signature or load additional OsRecovery Signature from storage devices. Options available: Update, Append. |

5-6 Boot Menu

The Boot menu allows you to set the drive priority during system boot-up. BIOS setup will display an error message if the legacy drive(s) specified is not bootable.

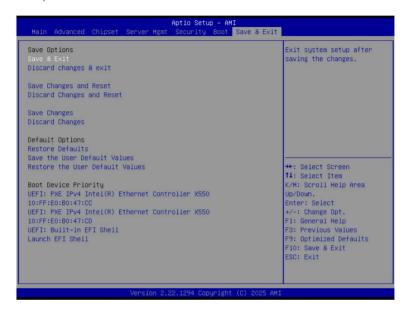




| Parameter | Description |
|---------------------------------------|---|
| Boot Configuration | |
| Setup Prompt Timeout | Number of seconds to wait for setup activation key. 65535 (0xFFFF) means indefinite waiting. Press the numeric keys to input the desired values. |
| Bootup NumLock State | Enable/Disable the Bootup NumLock function. Options available: On, Off. Default setting is On . |
| Quiet Boot | Enable/Disable showing the logo during POST. Options available: Enabled, Disabled. Default setting is Enabled . |
| Endless Retry Boot | Options available: Disable, Enable. Default setting is Disable . |
| Setup Flash | Press [Enter] to run setup flash. |
| Dump full Setup Data | Press [Enter] to dump full setup data to file. |
| Dump non-default Setup Data | Press [Enter] to dump non-default setup data to file. |
| Restore Setup Data | Press [Enter] to restore setup data from file. |
| FIXED BOOT ORDER Priorities | |
| Boot Option #1 / #2 / #3 / #4 / #5 | Press [Enter] to configure the boot order priority. By default, the server searches for boot devices in the following sequence: 1. Hard drive. 2. CD-COM/DVD drive. 3. USB device. 4. Network. 5. UEFI. |
| UEFI Network Drive BBS Priorities | Press [Enter] to configure the boot priority. |
| UEFI Application Boot Priorities | Press [Enter] to configure the boot priority. |

5-7 Save & Exit Menu

The Save & Exit menu displays the various options to quit from the BIOS setup. Highlight any of the exit options then press <Enter>.



| Parameter | Description |
|---------------------------|--|
| Save Options | |
| Save and Exit | Saves changes made and closes the BIOS setup. Options available: Yes, No. |
| Discard changes and exit | Discards changes made and exits the BIOS setup. Options available: Yes, No. |
| Save Changes and Reset | Restarts the system after saving the changes made. Options available: Yes, No. |
| Discard Changes and Reset | Restarts the system without saving any changes. Options available: Yes, No. |
| Save Changes | Saves changes done so far to any of the setup options. Options available: Yes, No. |
| Discard Changes | Discards changes made and closes the BIOS setup. Options available: Yes, No. |
| Default Options | |

| Parameter | Description |
|---------------------------------|--|
| Restore Defaults | Loads the default settings for all BIOS setup parameters. Setup Defaults are quite demanding in terms of resources consumption. If you are using low-speed memory chips or other kinds of low-performance components and you choose to load these settings, the system might not function properly. Options available: Yes, No. |
| Save the User Default Values | Saves the changes made as the user default settings. Options available: Yes, No. |
| Restore the User Default Values | Loads the user default settings for all BIOS setup parameters. Options available: Yes, No. |
| Boot Device Priority | Press [Enter] to configure the device as the boot-up drive. |
| Launch EFI Shell | Attempts to Launch EFI Shell application (Shell.efi) from one of the available file system devices. |

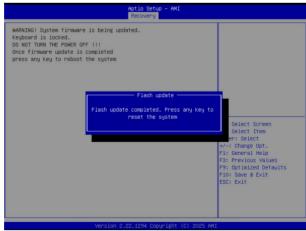
5-8 BIOS Recovery

The system has an embedded recovery technique. In the event that the BIOS becomes corrupt the boot block can be used to restore the BIOS to a working state. To restore your BIOS, please follow the instructions listed below:

Recovery Instruction:

- 1. Copy the XXX.rom to USB diskette.
- 2. Setting BIOS Recovery jump to enabled status.
- 3. Boot into BIOS recovery.
- 4. Run Proceed with flash update.
- 5. BIOS updated.





Appendix I

1-1 NVLink Bridge Removal



Before you remove the NVLink Bridge.

• Make sure the system is not turned on or connected to AC power.



WARNING!

- NVLink Bridges must be removed via the NVLink Bridge Removal Tool to avoid damage to the NVLink interface.
- 2 removal tools are required per NVLink Bridge as shown.

