# **GIGABYTE**<sup>™</sup> R263-S30-AAC1 R263-S30-AAC2 R263-S30-AAH1 R263-S30-AAH2

Rack Server - 4th/5th Gen Intel® Xeon® Scalable 2U UP 12-Bay NVMe/SATA/SAS (AAC1/AAH1) 2U UP 12-Bay SATA/SAS (AAC2/AAH2)

User Manual

Rev. 1.0

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#### **Documentation Classifications**

In order to assist in the use of this product, Giga Computing provides the following types of documentation:

- User Manual: detailed information & steps about the installation, configuration and use of this product (e.g. motherboard, server barebones), covering hardware and BIOS.
- User Guide: detailed information about the installation & use of an add-on hardware or software component (e.g. BMC firmware, rail-kit) compatible with this product.
- Quick Installation Guide: a short guide with visual diagrams that you can reference easily for installation purposes of this product (e.g. motherboard, server barebones).

Please see the support section of the online product page to check the current availability of these documents.

#### For More Information

For related product specifications, the latest firmware and software, and other information please visit our website at http://www.gigabyte.com/Enterprise

For GIGABYTE distributors and resellers, additional sales & marketing materials are available from our reseller portal: http://reseller.b2b.gigabyte.com

For further technical assistance, please contact your GIGABYTE representative or visit https://esupport.gigabyte.com/ to create a new support ticket

For any general sales or marketing enquiries, you may also message GIGABYTE server directly by email: server.grp@gigabyte.com

#### Conventions

The following conventions are used in this user's guide:

L.	<b>NOTE!</b> Gives bits and pieces of additional information related to the current topic.
	CAUTION! Gives precautionary measures to avoid possible hardware or software problems.
	WARNING! Alerts you to any damage that might result from doing or not doing specific actions.

#### **Server Warnings and Cautions**

Before installing a server, be sure that you understand the following warnings and cautions.

## 

#### To reduce the risk of electric shock or damage to the equipment:

- Do not disable the power cord grounding plug. The grounding plug is an important safety feature.
- Plug the power cord into a grounded (earthed) electrical outlet that is easily accessible at all times.
- Unplug all the power cords from the power supplies to disconnect power to the equipment.



- Shock Hazard! Disconnect all power supply cords before servicing.
- Do not route the power cord where it can be walked on or pinched by items placed against it. Pay particular attention to the plug, electrical outlet, and the point where the cord extends from the server.

## 

To reduce the risk of personal injury from hot surfaces, allow the drives and the internal system components to cool before touching them.



This server is equipped with high speed fans. Keep away from hazardous moving fan blades during servicing.

## 

This equipment is intended to be used in Restrict Access Location. The access can only be gained by Skilled person. Only authorized by well trained professional person can access the restrict access location.

## 

- Do not operate the server for long periods with the access panel open or removed. Operating the server in this manner results in improper airflow and improper cooling that can lead to thermal damage.
- Danger of explosion if battery is incorrectly replaced.
- Replace only with the same or equivalent type recommended by the manufacturer.
- Dispose of used batteries according to the manufacturer's instructions.



## CAUTION!

Risk of explosion if battery is replaced incorrectly or with an incorrect type. Replace the battery only with the same or equivalent type recommended by the manufacturer. Dispose of used batteries according to the manufacturer's instructions.

## Electrostatic Discharge (ESD)

ESD CAN DAMAGE DRIVES, BOARDS, AND OTHER PARTS. WE RECOMMEND THAT YOU PERFORM ALL PROCEDURES AT AN ESD WORKSTATION. IF ONE IS NOT AVAILABLE, PROVIDE SOME ESD PROTECTION BY WEARING AN ANTI-STATIC WRIST STRAP AT-TACHED TO CHASSIS GROUND -- ANY UNPAINTED METAL SURFACE -- ON YOUR SERVER WHEN HANDLING PARTS.

Always handle boards carefully. They can be extremely sensitive to ESD. Hold boards only by their edges without any component and pin touching. After removing a board from its protective wrapper or from the system, place the board component side up on a grounded, static free surface. Use a conductive foam pad if available but not the board wrapper. Do not slide board over any surface.

**System power on/off:** To remove power from system, you must remove the system from rack. Make sure the system is removed from the rack before opening the chassis, adding, or removing any non hot-plug components.

Hazardous conditions, devices and cables: Hazardous electrical conditions may be present on power, telephone, and communication cables. Turn off the system and disconnect the cables attached to the system before servicing it. Otherwise, personal injury or equipment damage can result.

**Electrostatic discharge (ESD) and ESD protection:** ESD can damage drives, boards, and other parts. We recommend that you perform all procedures in this chapter only at an ESD workstation. If one is not available, provide some ESD protection by wearing an antistatic wrist strap attached to chassis ground (any unpainted metal surface on the server) when handling parts.

**ESD** and handling boards: Always handle boards carefully. They can be extremely sensitive to electrostatic discharge (ESD). Hold boards only by their edges. After removing a board from its protective wrapper or from the system, place the board component side up on a grounded, static free surface. Use a conductive foam pad if available but not the board wrapper. Do not slide board over any surface.

**Installing or removing jumpers:** A jumper is a small plastic encased conductor that slips over two jumper pins. Some jumpers have a small tab on top that can be gripped with fingertips or with a pair of fine needle nosed pliers. If the jumpers do not have such a tab, take care when using needle nosed pliers to remove or install a jumper; grip the narrow sides of the jumper with the pliers, never the wide sides. Gripping the wide sides can dam-age the contacts inside the jumper, causing intermittent problems with the function con-trolled by that jumper. Take care to grip with, but not squeeze, the pliers or other tool used to remove a jumper, or the pins on the board may bend or break.

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## Chapter 1 Hardware Installation

#### 1-1 Installation Precautions

The motherboard/system contain numerous delicate electronic circuits and components which can become damaged as a result of electrostatic discharge (ESD). Prior to installation, carefully read the service guide and follow these procedures:

- Prior to installation, do not remove or break motherboard S/N (Serial Number) sticker or warranty sticker provided by your dealer. These stickers are required for warranty validation.
- Always remove the AC power by unplugging the power cord from the power outlet before installing or removing the motherboard or other hardware components.
- When connecting hardware components to the internal connectors on the motherboard, make sure they are connected tightly and securely.
- When handling the motherboard, avoid touching any metal leads or connectors.
- It is best to wear an electrostatic discharge (ESD) wrist strap when handling electronic components such as a motherboard, CPU or memory. If you do not have an ESD wrist strap, keep your hands dry and first touch a metal object to eliminate static electricity.
- Prior to installing the motherboard, please have it on top of an antistatic pad or within an electrostatic shielding container.
- Before unplugging the power supply cable from the motherboard, make sure the power supply has been turned off.
- Before turning on the power, make sure the power supply voltage has been set according to the local voltage standard.
- Before using the product, please verify that all cables and power connectors of your hardware components are connected.
- To prevent damage to the motherboard, do not allow screws to come in contact with the motherboard circuit or its components.
- Make sure there are no leftover screws or metal components placed on the motherboard or within the computer casing.
- Do not place the computer system on an uneven surface.
- · Do not place the computer system in a high-temperature environment.
- Turning on the computer power during the installation process can lead to damage to system components as well as physical harm to the user.
- If you are uncertain about any installation steps or have a problem related to the use of the product, please consult a certified computer technician.

## 1-2 Product Specifications



We reserve the right to make any changes to the product specifications and product-related information without prior notice.

Dimension	<ul> <li>2U</li> <li>438mm (W) x 87.5mm (H) x 710mm (D)</li> </ul>			
CPU	5th Generation Intel® Xeon® Scalable Processors     4th Generation Intel® Xeon® Scalable Processors			
	4th Generation Intel® Xeon® Scalable Processors			
	Intel® Xeon® CPU Max Series			
	Single processor, CPU TDP up to 350W			
Socket	<ul> <li>1 x LGA 4677</li> </ul>			
	Socket E			
Chipset	Intel® C741 Chipset			
Security	UEFI Secure Boot			
	Silicon root of trust (Option)			
	SNMP Support: V3			
Memory	16 x DIMM slots			
Monory .	<ul> <li>DDR5 memory supported only</li> </ul>			
	8-Channel memory architecture			
	<ul> <li>3DS RDIMM modules up to 256GB supported</li> </ul>			
	<ul> <li>5th Gen Intel® Xeon®: Up to *5600MHz (1DPC), 4400MHz (2DPC)</li> <li>4th Gen Intel® Xeon®: Up to 4800MHz (1DPC), 4400MHz (2DPC)</li> </ul>			
	<ul> <li>Intel® Xeon® Max Series: Up to 4800MHz (1DPC), 4400MHz (2DPC)</li> <li>Intel® Xeon® Max Series: Up to 4800MHz (1DPC), 4400MHz (2DPC)</li> </ul>			
	• Intel® Xeon® Max Series: Up to 4600MHz (TDPC), 4400MHz (2DPC)			
	*5600MHz support under 2DPC configuration requires verified memory and BIOS			
	setup. Please refer to the QVL for more information.			
	Rear side:			
	<ul> <li>1 x 1Gb/s LAN port (1 x Intel® I210-AT)</li> </ul>			
	Supports NCSI function			
	<ul> <li>1 x 10/100/1000 management LAN</li> </ul>			
Video	<ul> <li>Integrated in Aspeed® AST2600</li> </ul>			
	<ul> <li>2D Video Graphic Adapter with PCIe bus interface</li> </ul>			
	<ul> <li>1920x1200@60Hz 32bpp, DDR4 SDRAM</li> </ul>			
Storage	R263-S30-AAC1/ R263-S30-AAH1			
	• 4 x 3.5"/2.5" Gen4 NVMe/SATA/SAS hot-swappable bays, NVMe from CPU			
	8 x 3.5"/2.5" SATA/SAS hot-swappable bays			
	SAS card is required for SAS devices support			
	R263-S30-AAC2/R263-S30-AAH2			
	<ul> <li>12 x 3.5"/2.5" SATA/SAS hot-swappable bays</li> </ul>			
	<ul> <li>SAS card is required for SAS devices support</li> </ul>			
	Hardware Installation			

Hardware Installation

SAS	Depends on SAS add-on card
5A5	Depends on SAS add-on card
RAID	Intel® SATA RAID 0/1/10/5
Expansion Slot	<ul> <li>R263-S30-AAC1/ R263-S30-AAH1</li> <li>Riser Card CRS201C: <ul> <li>1 x PCle x16 (Gen5 x16) FHHL slot, from CPU</li> </ul> </li> <li>Riser Card CRS201D: <ul> <li>1 x PCle x16 (Gen5 x16) FHHL slot, from CPU</li> </ul> </li> <li>2 x OCP 3.0 slots with PCle Gen5 x16 bandwidth, from CPU <ul> <li>Supports NCSI function</li> </ul> </li> <li>1 x M.2 slot: <ul> <li>M-key</li> </ul> </li> </ul>
	<ul> <li>PCIe Gen3 x4, from PCH</li> <li>Supports NGFF-2280/22110 cards</li> <li>R263-S30-AAC2/ R263-S30-AAH2</li> <li>Riser Card CRS201C:</li> </ul>
	<ul> <li>1 x PCle x16 (Gen5 x16) FHHL slot, from CPU</li> <li>1 x PCle x16 (Gen5 x8) FHHL slot, from CPU</li> <li>Riser Card CRS201D:</li> <li>1 x PCle x16 (Gen5 x16) FHHL slot, from CPU</li> <li>1 x PCle x16 (Gen5 x8) FHHL slot, from CPU</li> </ul>
	<ul> <li>2 x OCP 3.0 slots with PCIe Gen5 x16 bandwidth, from CPU</li> <li>Support NCSI function</li> </ul>
	<ul> <li>1 x M.2 slot:</li> <li>M-key</li> <li>PCIe Gen3 x4, from PCH</li> <li>Supports NGFF-2280/22110 cards</li> </ul>
Internal I/O	<ul> <li>1 x TPM header</li> <li>1 x VROC connector</li> </ul>
Front I/O	<ul> <li>2 x USB 3.2 Gen1</li> <li>1 x Power button with LED</li> <li>1 x ID button with LED</li> <li>1 x NMI button</li> <li>1 x Reset button</li> <li>1 x LAN activity LED</li> <li>1 x UDD activity LED</li> </ul>
	<ul> <li>1 x HDD activity LED</li> <li>1 x System status LED</li> </ul>

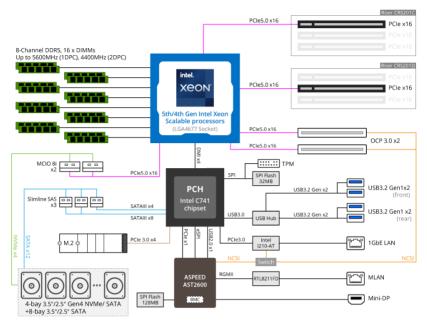
Rear I/O	• 2 x USB 3.2 Gen1
	1 x Mini-DP
	• 1 x RJ45
	1 x MLAN
	1 x ID button with LED
Backplane I/O	<ul> <li>Speed and bandwidth: PCIe Gen4 x4 or SATA 6Gb/s or SAS 12Gb/s</li> </ul>
TPM	1 x TPM header with SPI interface
	Optional TPM2.0 kit: CTM010
Power Supply	R263-S30-AAC1/R263-S30-AAC2
()	<ul> <li>1+1 1300W 80 PLUS Platinum redundant power supplies</li> </ul>
	AC Input:
	- 100-240V~/ 12-7A, 50-60Hz
	- 200-240V~/ 8A, 50-60Hz
	DC Input:
	- 240Vdc/ 6.5A
	DC Output:
	- Max 1000W/ 100-240V~
	+12V/ 80.5A
	+12Vsb/ 3A
	- Max 1300W/ 200-240V~ or 240Vdc Input
	+12V/ 105.4A
	+12Vsb/ 3A
	R263-S30-AAH1/ R263-S30-AAH2
	<ul> <li>1+1 1300W 80 PLUS Titanium redundant power supplies</li> </ul>
	AC Input:
	- 100-127V ~/ 12A, 50-60Hz
	- 200-240V~/ 8A, 50-60Hz
	DC Input:
	- 240Vdc/ 7A
	DC Output:
	- Max 1000W/ 100-127V~
	+12.2V/ 82A
	+12Vsb/ 3A
	- Max 1300W/ 200-240V~ or 240Vdc Input
	+12.2V/ 106A
	+12Vsb/ 3A
	NOTE:
	• The default power supply is based on a basic system configuration. Please
	contact with technical representatives to determine the best power supply
	selection for your system configuration requirements and preferred efficiency.
	• The power supply specifications provided herein is for the default server
	configuration. Different SKUs have different PSU specs, so please see the system

rating label on the server for the accurate PSU specification.

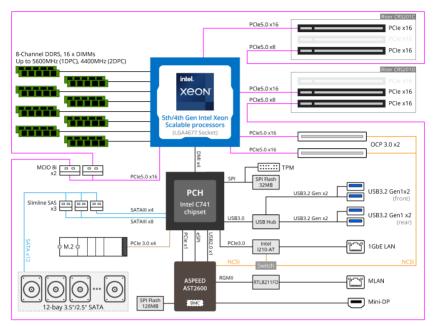
System	Aspeed® AST2600 management controller
Management	GIGABYTE Management Console (AMI MegaRAC SP-X) web interface
Ŭ	<b>o</b> ( <b>o</b> <i>i</i>
	Dashboard
	HTML5 KVM
	<ul> <li>Sensor Monitor (Voltage, RPM, Temperature, CPU Statusetc.)</li> </ul>
	Sensor Reading History Data
	FRU Information
	SEL Log in Linear Storage / Circular Storage Policy
	Hardware Inventory
	Fan Profile
	System Firewall
	Power Consumption
	Power Control
	Advanced power capping
	LDAP / AD / RADIUS Support
	Backup & Restore Configuration
	Remote BIOS/BMC/CPLD Update
	Event Log Filter
	User Management
	Media Redirection Settings
	PAM Order Settings
	SSL Settings
	SMTP Settings
Operating	Operating temperature: 10°C to 35°C
Properties	<ul> <li>Operating humidity: 8-80% (non-condensing)</li> </ul>
	<ul> <li>Non-operating temperature: -40°C to 60°C</li> </ul>
	<ul> <li>Non-operating humidity: 20%-95% (non-condensing)</li> </ul>

### 1-3 System Block Diagram

#### 1-3-1 R263-S30-AAC1/ R263-S30-AAH1

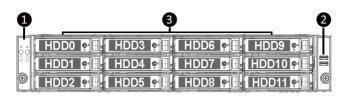


#### 1-3-2 R263-S30-AAC2/ R263-S30-AAH2



## Chapter 2 System Appearance

2-1 Front View



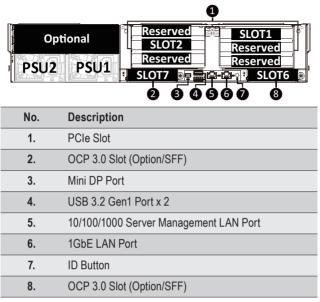
No.	Description		
1.	Front Panel LEDs and Buttons		
2.	Front USB 3.2 Gen1 Ports		
3.	3.5"/2.5" Drive Bays		
NOTE! Drives with green latches support NVMe.			



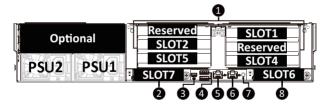
Please Go to Chapter 2-3 Front Panel LED and Buttons for detail description of function LEDs.

#### 2-2 Rear View

#### 2-2-1 R263-S30-AAC1/ R263-S30-AAH1



#### 2-2-2 R263-S30-AAC2/ R263-S30-AAH2



No.	Description
1.	PCIe Slot
2.	OCP 3.0 Slot (Option/SFF)
3.	Mini DP Port
4.	USB 3.2 Gen1 Port x 2
5.	10/100/1000 Server Management LAN Port
6.	1GbE LAN Port
7.	ID Button
8.	OCP 3.0 Slot (Option/SFF)

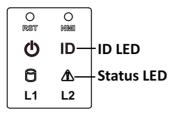
### 2-3 Front Panel LEDs and Buttons



No.	Name	Color	Status	Description
1.	Reset Button			Press the button to reset the system.
2.	NMI button			Press the button server generates a NMI to the processor if the multiple-bit ECC errors occur, which effectively halt the server.
3.	Power button	Green	On	System is powered on
5.	with LED	N/A	Off	System is not powered on or in ACPI S5 state (power off)
4.	ID Button(Note)			Press the button to activate system identification
		0	On	HDD locate
		Green	Blink	HDD access
5.	HDD Status	Amber	On	HDD fault
	LED	Green/ Amber	Blink	HDD rebuilding
		N/A	Off	No HDD access or no HDD fault.
		Green	Solid On	System is operating normally.
			Solid On	Critical condition, may indicate: System fan failure; System temperature
6.	System Status LED <sup>(Note)</sup>	Amber	Blink	Non-critical condition, may indicate: Redundant power module failure Temperature and voltage issue Chassis intrusion
		N/A	Off	System is not ready, may indicate: POST error; NMI error; Processor or terminator missing
		Green	Solid On	Link between system and network or no access.
7.	LAN 1 Active/ Link LED	Green	Blink	Data transmission or receiving is occuring
		N/A	Off	No data transmission or receiving is occuring
8.	LAN 2 Active/ Link LED	The function	on is disabled	d.

(Note) If your server features RoT function, please see the following section for detail LED behavior.

#### 2-3-1 RoT LEDs



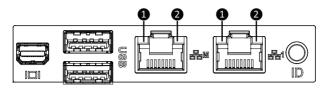
	LED on Front panel(Note5)	
	ID LED	Status LED
EC Firmware (FW) Authentication fail or not exit		
EC FW is broken or not exit (Note1)	OFF	OFF
Authenticating/Recovering BMC/BIOS Images		
Authenticating Images	OFF	OFF
Recovering BMC Active Flash	Blinks Blue 4 times per second	Blinks Green 4 times per second
Recovering BIOS Active Flash	Blinks Blue 4 times per second	Blinks Green 4 times per second
Authentication (AUTH) Pass		
Recovering BIOS Active Flash	OFF	OFF
BMC : AUTH pass after doing recovery BIOS : AUTH pass after doing recovery	OFF	OFF
BMC : AUTH pass after doing recovery BIOS : AUTH pass	OFF	OFF
BMC : AUTH pass BIOS : AUTH pass after doing recovery	OFF	OFF
Active Flash Authentication (AUTH) Fail		
BMC : AUTH Fail <sup>(Note2)</sup>	Blinks Blue 1 time per second	Blinks Green 1 time per second

BIOS : AUTH fail <sup>(Note2)</sup>	Blinks Blue 1 time per second	Blinks Amber 1 time per second	
BMC : AUTH fail after doing recovery <sup>(Note3)</sup>	Blinks Blue 2 times per second [ON OFF OFF]	Blinks Green 2 times per second [ON OFF OFF]	
BIOS : AUTH fail after doing recovery <sup>(Note3)</sup>	Blinks Blue 2 times per second [ON OFF OFF]	Blinks Amber 2 times per second [ON OFF OFF]	
Backup Flash Authentication Fail <sup>(Note4)</sup>			
BMC : AUTH fail	Blinks Blue 2 times per second [ON OFF ON OFF]	Blinks Green 2 times per second [ON OFF ON OFF]	
BIOS : AUTH fail	Blinks Blue 2 times per second [ON OFF ON OFF]	Blinks Amber 2 times per second [ON OFF ON OFF]	

#### NOTE!

- 1. EC FW is broken or not exited result in Microchip CEC1702 cannot load EC FW for authentication.
- 2 (1) Authentication fail include below scenarios Configuration table is missing or modified Public key is missing or modified Protected area or signature is modified Flash empty
- If active flash is still authentication failed after recovery sequence, Microchip CEC1702 stop the process and showing LED behavior.
- If backup flash authentication is failed cause by configuration table, public key or protected area is broken. Microchip CEC1702 stop the process and showing LED behavior.
- Front panel LED is controlled by BMC or Microchip CEC1702. Once Microchip CEC1702 is working(Auth or recovery), the front panel LED is controlled by Microchip CEC1702 and vice versa.

## 2-4 Rear System LAN LEDs



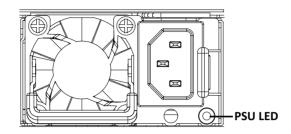
No.	Name	Color	Status	Description				
		Yellow	On	1 Gbps data rate				
1.	1GbE Speed LED	Green	On	100 Mbps data rate				
		N/A	Off	10 Mbps data rate				
	1015		On	Link between system and				
	1GbE	Green		network or no access				
2.	Link/ Blink		Blink	Data transmission or receiving is occurring				
	Activity N/A Off			No data transmission or				
				receiving is occurring				

## 2-5 Power Supply Unit (PSU) LED



#### NOTE!

The power supply may be vary based on the system configuration.



State	Description
OFF	No AC power to all power supplies
1Hz Green Blinking	AC present / only standby on / Cold redundant mode
2Hz Green Blinking	Power supply firmware updating mode
Andrea	AC cord unplugged or AC power lost; with a second power supply in parallel still with AC input power
Amber	Power supply critical event causing shut down: failure, OCP, OVP, fan failure and UVP
1Hz Amber Blinking	Power supply warning events where the power supply continues to operate: high temp, high power, high current and slow fan

## 2-6 Hard Disk Drive LEDs



RAID SKU		LED #1	Locate	HDD Fault	Rebuilding	HDD Access	HDD Present (No Access)
	Disk LED (LED	Green	ON(*1)	OFF		BLINK (*2)	OFF
No RAID	on Back Panel)	Amber	OFF	OFF		OFF	OFF
configuration (via PCH, HBA)	Removed HDD	Green	ON(*1)	OFF			
	Slot (LED on Back Panel)	Amber	OFF	OFF			
RAID		Green	ON	OFF		BLINK (*2)	OFF
configuration (via HW RAID Card or SW RAID Card)	Disk LED	Amber	OFF	ON	(Low Speed: 2 Hz)	OFF	OFF
	Removed	Green	ON(*1)	OFF	(*3)		
	HDD Slot	Amber	OFF	ON	(*3)		

LED #2	HDD Present	No HDD
Green	ON	OFF

#### NOTE:

- \*1: Depends on HBA/Utility Spec.
- \*2: Blink cycle depends on HDD's activity signal.

\*3: If HDD is pulled out during rebuilding, the disk status of this HDD is regarded as faulty.

## Chapter 3 System Hardware Installation



#### **Pre-installation Instructions**

Computer components and electronic circuit boards can be damaged by electrostatic discharge. Working on computers that are still connected to a power supply can be extremely dangerous. Follow the simple guidelines below to avoid damage to your computer or injury to yourself.

- Always disconnect the computer from the power outlet whenever you are working inside the computer case.
- If possible, wear a grounded wrist strap when you are working inside the computer case. Alternatively, discharge any static electricity by touching the bare metal system of the computer case, or the bare metal body of any other grounded appliance.
- Hold electronic circuit boards by the edges only. Do not touch the components on the board unless it is necessary to do so. Do not flex or stress the circuit board.
- Leave all components inside the static-proof packaging until you are ready to use the component
  for the installation.

## 3-1 Removing Chassis Cover

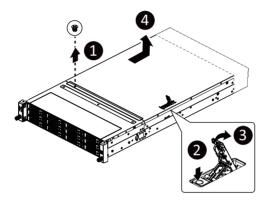


Before you remove or install the system cover

· Make sure the system is not turned on or connected to AC power.

#### Follow these instructions to remove the chassis cover:

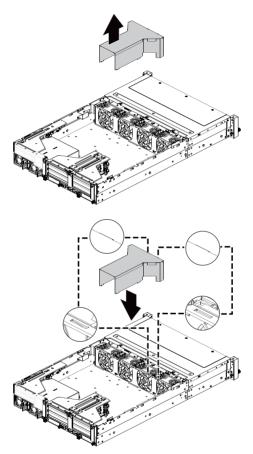
- 1. Remove the screw securing the chassis cover.
- 2. Push button to unlock the handle.
- 3. Pull the grip handle to open the panel cover.
- 4. Slide the chassis cover towards the rear and remove the chassis cover in the direction indicated.
- 5. To reinstall the chassis cover reverse steps 1-4.



## 3-2 Removing and Installing the Fan Duct

#### Follow these instructions to remove/install the fan duct:

- 1. Lift up to remove the fan duct
- 2. To install the fan duct, align the fan duct with the guiding groove. Push down the fan duct into chassis until its firmly seat.



## 3-3 Removing and installing the Heat Sink



Read the following guidelines before you begin to remove/install the heat sink:

- Always turn off the computer and unplug the power cord from the power outlet before installing the heat sink to prevent hardware damage.
- Unplug all cables from the power outlets.
- · Disconnect all telecommunication cables from their ports.
- Place the system unit on a flat and stable surface.
- · Open the system according to the instructions.

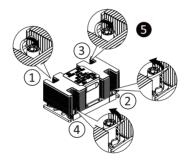


#### WARNING!

Failure to turn off the server before you start installing components may cause serious damage. Do not attempt the procedures described in the following sections unless you are a qualified service technician.

#### Follow these instructions to remove/install the heat sink:

- 1. Loosen the captive screws securing the heat sink in place in reverse order  $(4 \rightarrow 3 \rightarrow 2 \rightarrow 1)$ .
- 2. Move the rotating wires into the unlatch position.
- 3. Lift and remove the heat sink from the system.
- To reinstall the heat sink reverse steps 1-3 while ensuring that you tighten the captive screws in sequential order (1→2→3→4).





When installing the heat sink to CPU, use T30-Lobe driver to tighten 4 captive nuts in sequence as 1-4. The screw tightening torque: 8  $\pm$  0.5 kgf-cm.

To ensure the system operates properly, make sure the heatsink is seated on the processor firmly.

## 3-4 Installing the CPU

Read the following guidelines before you begin to install the CPU:

- Make sure that the motherboard supports the CPU.
- Always turn off the computer and unplug the power cord from the power outlet before installing the CPU to prevent hardware damage.
- Unplug all cables from the power outlets.
- · Disconnect all telecommunication cables from their ports.
- · Place the system unit on a flat and stable surface.
- Open the system according to the instructions.



#### WARNING!

Failure to properly turn off the server before you start installing components may cause serious damage. Do not attempt the procedures described in the following sections unless you are a qualified service technician.

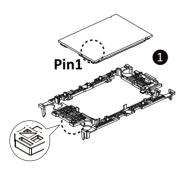
#### Follow these instructions to Install the CPU:

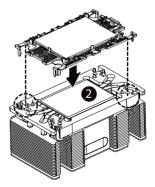
- Align and install the processor on the carrier. NOTE: Apply thermal compound evenly on the top of the CPU. Remove the protective cover from the underside of the heat sink.
- 2. Carefully flip the heat sink cover. Then install the carrier assembly on the bottom of the heat sink and make sure the gold arrow is located in the correct direction.
- 3. Remove the CPU cover.

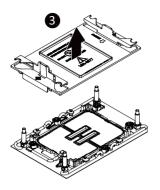
NOTE: Save the CPU cover in the event that you need to remove the CPU from the socket.

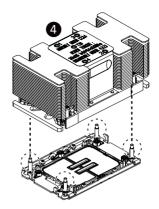
- 4. Align the heat sink with the CPU socket by the guide pins and make sure the gold arrow is located in the correct direction. Then place the heat sink onto the top of the CPU socket.
- Position the rotating wires into the latch position. Tighten the screws in sequential order (1→2→3→4).

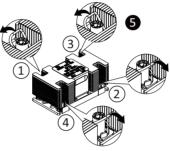
**NOTE:** When dissembling the heat sink, loosen the screws in reverse order  $(4 \rightarrow 3 \rightarrow 2 \rightarrow 1)$  and then move the rotating wires into the unlatch position.











#### **Carrier Types used for Package Types**

F	Package Type	Xeon <sup>®</sup> SP XCC	Xeon <sup>®</sup> SP MCC	Xeon <sup>®</sup> SP+HBM		
0	Carrier Code	E1A	E1B	E1C		

#### NOTE!

- The carrier code is marked on each carrier and matches a code laser marked on to the IHS(Integrated Heat Spreader) to ensure the right parts are used together
- When installing the heat sink to CPU, use T30-Lobe driver to tighten 4 captive nuts in sequence as 1-4.
- The screw tightening torque: 8 ± 0.5 kgf-cm.



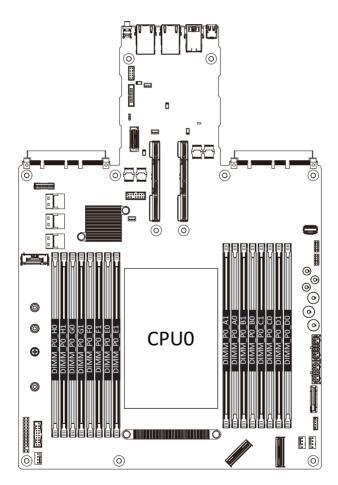
### Installing the Memory

Read the following guidelines before you begin to install the memory:

- Make sure that the motherboard supports the memory. It is recommended that memory of the same capacity, brand, speed, and chips be used.
- Always turn off the computer and unplug the power cord from the power outlet before installing the memory to prevent hardware damage.
- Memory modules have a foolproof design. A memory module can be installed in only one direction. If you are unable to insert the memory, switch the direction.

#### 3-5-1 Eight Channel Memory Configuration

This motherboard provides 16 DDR5 memory slots and supports 8-Channel Technology. After the memory is installed, the BIOS will automatically detect the specifications and capacity of the memory.

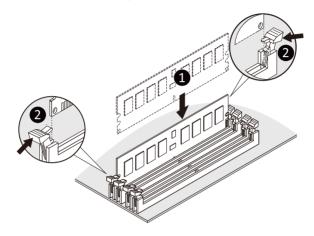


#### 3-5-2 Installing the Memory

Before installing a memory module, make sure to turn off the computer and unplug the power cord from the power outlet to prevent damage to the memory module. Be sure to install DDR5 DIMMs on this motherboard.

#### Follow these instructions to install the Memory:

- 1. Insert the DIMM memory module vertically into the DIMM slot, and push it down.
- 2. Close the plastic clip at both edges of the DIMM slots to lock the DIMM module.
- 3. Reverse the installation steps when you want to remove the DIMM module.



#### 3-5-3 DIMM Population Table

#### 4th Gen Intel Xeon Scalable Processors-SP Memory Support

Туре	Ranks Per DIMM and Data Width	DIM	M Capacity	(GB)	Speed (MT/s); Voltage (V); DIMM per Channel (DPC)				
	Duta math	16Gb	24Gb <sup>2</sup>	36Gb	1DPC <sup>1</sup> 2DPC 1.1V				
	SRx8 (RC D)	16GB	24GB	NA					
	SRx4 (RC C)	32GB	48GB	NA					
RDIMM	SRx4 (RC F) 9x4	32GB	NA	NA					
RDIMM	DRx8 (RC E)	32GB	48GB	NA	4000	4400			
	DRx4 (RC A)	64GB	96GB	128GB	4800	4400			
	DRx4 (RC B) 9x4	64GB	NA	NA	1				
	(4R/8R)x4	2H-128GB	NA	NA	]				
RDIMM 3DS	(RC A)	4H-256GB	INA	IN/A					

NOTE:

1. 1DPC applies to 1SPC or 2SPC implementations (SPC - Sockets Per Channel)

2. 24Gb XCC only w/ limited configs: 1DPC all DIMM types, 2DPC 96GB only. Only 8 and 16 DIMM configs, no fallbacks.

#### 5th Gen Intel Xeon Scalable Processors-SP Memory Support

Туре	Ranks Per DIMM and Data Width	DIM	M Capacity	(GB)	Speed (MT/s); Voltage (V); DIMM per Channel (DPC) 1DPC <sup>1</sup> 2DPC				
		16Gb	24Gb <sup>2</sup>	36Gb	1.1	V			
	SRx8 (RC D)	16GB	24GB	NA					
	SRx4 (RC C)	32GB	48GB	NA					
RDIMM	SRx4 (RC F) 9x4	NA	NA	NA	5600 <sup>3</sup>	4400 <sup>3</sup>			
RDIMM	DRx8 (RC E)	32GB	48GB	NA	0000				
	DRx4 (RC A)	64GB	96GB	128GB					
	DRx4 (RC B) 9x4	NA	NA	NA	1				
	(4R/8R)x4	2H-128GB	NA	NA	5600 <sup>4</sup>				
RDIMM 3DS	(RC A)	4H-256GB	INA	INA	0000				

NOTE:

NOTE: 1.10PC applies to 1SPC or 2SPC implementations (SPC - Sockets Per Channel) 2.24Gb 2DPC not POR w/24GB and 46GB DIMMs. 3. DDR5-5600 RDIMMs will be limited to 5600 MT/s 1DPC and 4400 MT/s 2DPC. DDR5-4800 DIMMs will be limited to 4800 MT/s 1DPC and 4400 MT/s 2DPC.

DDR5-5600 DIMMS are required for 5600 and 5200 1DPC speeds.

#### 3-5-4 Processor and Memory Module Matrix Table

Memory		CPU0 H0 H1 G0 G1 F0 F1 E0 E1 A1 A0 B1 B0 C1 C0 D1 D0														
Q'ty	HO	H1	G0	<b>G1</b>	F0	F1	E0	E1	A1	A0	B1	B0	C1	C0	D1	D0
1 DIMM										v						
2 DIMM			v							v						
4 DIMM			v				v			v				v		
6 DIMM			v		v		v			v				v		v
8 DIMM	v		v		v		v			v		v		v		v
12 DIMM	v		v	v	v		v	v	v	v		v	v	v		v
16 DIMM	v	v	v	v	v	v	v	v	v	v	v	v	v	v	v	v

## 3-6 Installing the PCI Expansion Card



Voltages can be present within the server whenever an AC power source is connected. This voltage is present even when the main power switch is in the off position. Ensure that the system is powered-down and all power sources have been disconnected from the server prior to installing a PCle card.

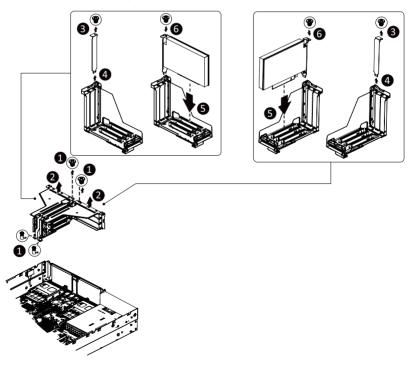
Failure to observe these warnings could result in personal injury or damage to equipment.



The PCIe riser assembly does not include a riser card or any cabling as standard. To install a PCIe card, a riser card must be installed.

#### Follow these instructions for a PCI Expansion card:

- 1. Remove the screws and loosen the thumbscrew securing the riser bracket.
- 2. Lift up the riser bracket out of system.
- 3. Remove the screw securing the slot cover from the riser bracket.
- 4. Remove the slot covers from the riser bracket.
- Orient the PCIe card with the riser guide slot and push in the direction of the arrow until the PCIe card sits in the PCIe card connector.
- 6. Secure the PCIe card with the screw.
- 7. Reverse the previous steps to install the riser bracket.



## 3-7 Installing the Mezzanine Card

#### 3-7-1 OCP 3.0

Use of the following type of OCP 3.0 NIC is recommended:

- OCP 3.0 SFF with Pull Tab
  - OCP 3.0 SFF with Ejector Latch

#### Follow these instructions to install an OCP 3.0 mezzanine card:

- 1. Remove the two screws securing the mezzanine card slot cover.
- 2. Remove the slot cover from the system.
- 3. Insert the OCP 3.0 mezzanine card into the card slot ensuring that the card is firmly connected to the connector on the motherboard.
- 4. Tighten the thumbnail screw to secure the OCP 3.0 mezzanine card in place.
- 5. Reverse the previous steps to replace the OCP 3.0 mezzanine card.





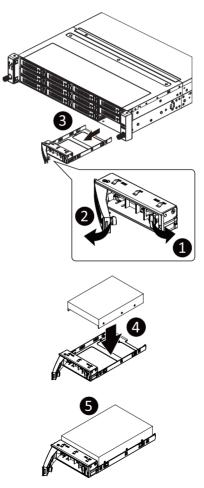
## 3-8 Installing the Hard Disk Drive

Read the following guidelines before you begin to install the HDD:

- Take note of the drive tray orientation before sliding it out.
- The tray will not fit back into the bay if inserted incorrectly.
- Make sure that the HDD is connected to the HDD connector on the backplane.

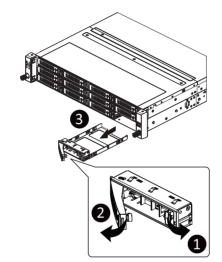
#### Follow these instructions to install a 3.5" HDD:

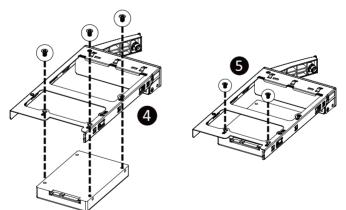
- 1. Press the release button.
- 2. Extend the locking lever.
- 3. Pull the locking lever in the direction indicated to remove the HDD tray.
- 4. Slide the hard disk drive into the HDD tray.
- 5. Reinsert the HDD tray into the slot and close the locking lever.



#### Follow these instructions to install 2.5" hard disk drive into 3.5" HDD Tray:

- 1. Press the release button.
- 2. Extend the locking lever.
- 3. Pull the locking lever in the direction indicated to remove the HDD tray.
- 4. Align the hard disk drive with the positioning screw on the HDD tray.
- 5. Secure the hard disk drive with five screws.
- 6. Reinsert the HDD tray into the slot and close the locking lever.





## 3-9 Installing the M.2 Device and Heat Sink



#### CAUTION

The position of the stand-off screw will depend on the size of the M.2 device. Refer to the size of the M.2 device and change the position of the stand-off screw accordingly.



#### WARNING:

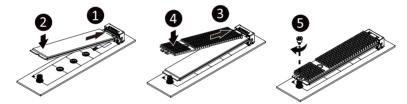
Please ensure a heatsink is attached to any M.2 device installed into the system. Installing an M.2 device without any heatsink may result in the system overheating or system performance being throttled.



To install/remove the M.2 module and Heatsink use a No. 1 Phillips-head screwdriver with a screw torque of 1.5  $\pm$  0.2 kgf\*cm

#### Follow these instructions to install the M.2 device and heat sink:

- 1. Insert the M.2 device into the M.2 connector.
- 2. Press down on the M.2 device.
- 3. Install the thermal pad of the M.2 device to the M.2 device.
- 4. Press down on the thermal pad.
- 5. Secure the M.2 device and its thermal pad to the motherboard with a single screw.
- 6. Reverse steps 1-2 to remove the M.2 device.



## 3-10 Replacing the Fan Assembly

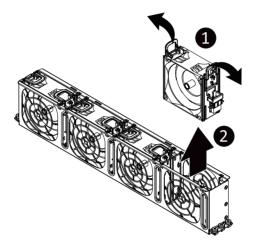


Voltages can be present within the server whenever an AC power source is connected. This
voltage is present even when the main power switch is in the off position. Ensure that the
system is powered-down and all power sources have been disconnected from the server prior to
replacing a system fan.

· Failure to observe these warnings could result in personal injury or damage to equipment.

#### Follow these instructions to replace the fan assembly:

- 1. Pull outward the fan ear.
- 2. Lift up the fan assembly from the chassis.
- 3. Reverse the previous steps to install the replacement fan assembly.



## 3-11 Replacing the Power Supply

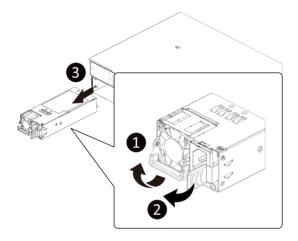


#### CAUTION!

 In order to reduce the risk of injury from electric shock, disconnect AC power from the power supply before removing the power supply from the system

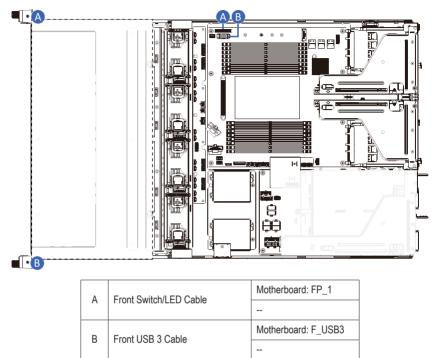
#### Follow these instructions to replace the power supply:

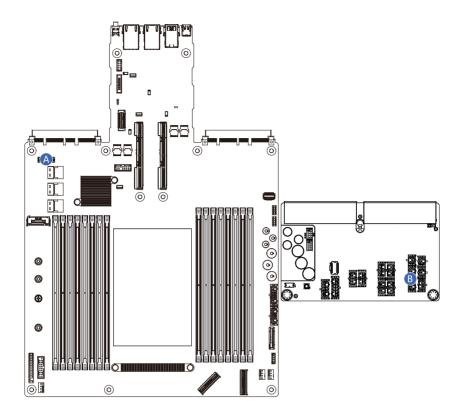
- 1. Flip and then grasp the power supply handle.
- 2. Press the retaining clip on the top side of the power supply in the direction indicated.
- 3. Pull out the power supply using the handle.
- 4. Insert the replacement power supply firmly into the chassis. Connect the AC power cord to the replacement power supply.

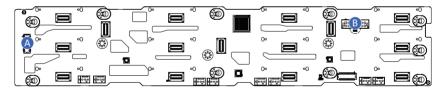


## 3-12 Cable Routing

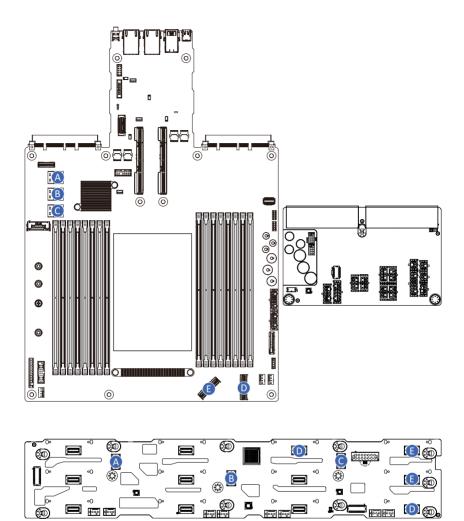
### 3-12-1 R263-S30-AAC1/ R263-S30-AAH1







A	Backplane Board Signal Cable	Motherboard: BP_1
		Backplane Board: BP_1
В	Backplane Board Power Cable	Power Board: BP_ATX1
		Backplane Board: ATX1



D.

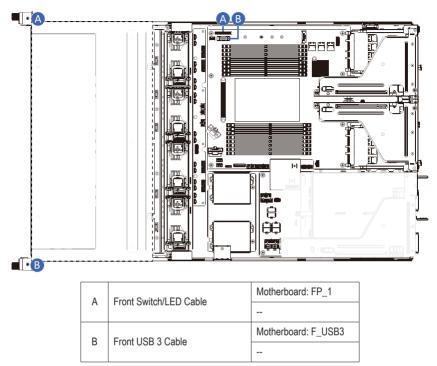
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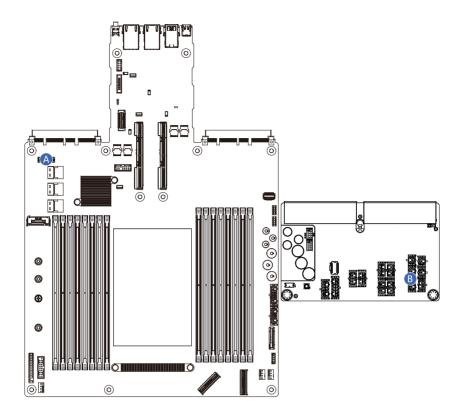
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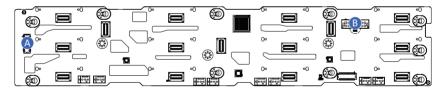
0.

A       SATA Cable       Motherboard: SL_SATA1         Backplane Board: SL_SAS0       Backplane Board: SL_SAS0         B       SATA Cable       Motherboard: SL_SATA2         Backplane Board: SL_SAS1       Backplane Board: SL_SAS1         C       SATA Cable       Motherboard: SL_SATA3         D       NVMe Cable       Motherboard: U2_P0_5CA         Backplane Board: U2_8/U2_9       Backplane Board: U2_P0_5GE         E       NVMe Cable       Motherboard: U2_P0_5GE			
B     Backplane Board: SL_SAS0       B     SATA Cable     Motherboard: SL_SATA2       Backplane Board: SL_SATA2     Backplane Board: SL_SAS1       C     SATA Cable     Motherboard: SL_SATA3       D     NVMe Cable     Motherboard: U2_P0_5CA       E     NVMe Cable     Motherboard: U2_P0_5GE	A	SATA Cable	Motherboard: SL_SATA1
B     SATA Cable			Backplane Board: SL_SAS0
Backplane Board: SL_SAS1       C     SATA Cable       Motherboard: SL_SATA3       Backplane Board: SL_SAS2       D     NVMe Cable       Motherboard: U2_P0_5CA       Backplane Board: U2_8/U_2_9       Motherboard: U2_P0_5GE	B SATA Cable	Motherboard: SL_SATA2	
C     SATA Cable       Backplane Board: SL_SAS2       D     NVMe Cable       Backplane Board: U2_P0_5CA       Backplane Board: U2_8/ U_2_9       C     Motherboard: U2_P0_5GE		Backplane Board: SL_SAS1	
Backplane Board: SL_SAS2       D     NVMe Cable       Backplane Board: U2_P0_5CA       Backplane Board: U2_8/ U_2_9       Motherboard: U2_P0_5GE	6	SATA Cabla	Motherboard: SL_SATA3
D     NVMe Cable        Backplane Board: U_2_8/ U_2_9       E     NVMe Cable   Motherboard: U2_P0_5GE		SATA Cable	Backplane Board: SL_SAS2
E     NVMe Cable         Backplane Board: U_2_8/ U_2_9   Motherboard: U2_P0_5GE		NVMe Cable	Motherboard: U2_P0_5CA
E NVMe Cable			Backplane Board: U_2_8/ U_2_9
	E	NVMe Cable	Motherboard: U2_P0_5GE
			Backplane Board: U_2_10/ U_2_11

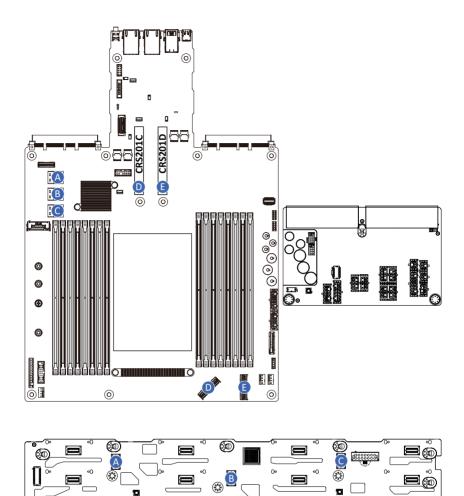
#### 3-12-2 R263-S30-AAC2/ R263-S30-AAH2

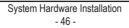






A	Backplane Board Signal Cable	Motherboard: BP_1
		Backplane Board: BP_1
В	Backplane Board Power Cable	Power Board: BP_ATX1
		Backplane Board: ATX1





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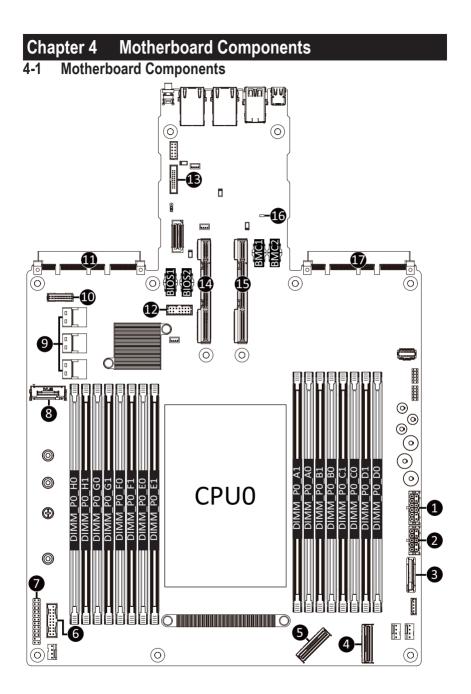
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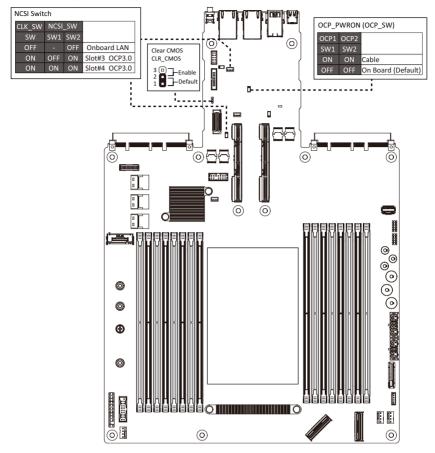
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SATA Cablo	Motherboard: SL_SATA1
SAIA CADIE	Backplane Board: SL_SAS0
B SATA Cable	Motherboard: SL_SATA2
	Backplane Board: SL_SAS1
C SATA Cable	Motherboard: SL_SATA3
	Backplane Board: SL_SAS2
Riser Cable	Motherboard: U2_P0_5GE
	Riser Card (CRS201C): MCIO_P2
Riser Cable	Motherboard: U2_P0_5CA
	Riser Card (CRS201D): MCIO_P2
	SATA Cable



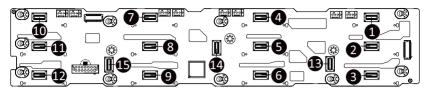
Item	Description
1	2 x 6 Pin ATX Power Connector
2	2 x 5 Pin ATX Power Connector
3	System Battery
4	MCIO Connector (U2_P0_5CA/PCIe Gen5)
5	MCIO Connector (U2_P0_5GE/PCIe Gen5)
6	Front Panel USB 3.2 Gen1 Connector
7	Front Panel Connector
8	M.2 Slot (PCIe Gen3 x4, Support NGFF-22110)
9	SlimLine Connector (SL_SATA1/SL_SATA2/SL_SATA3)
10	HDD Backplane Board Connector
11	OCP 3.0 Connetor (PCIe Gen5 x16)
12	TPM Module Connector (SPI Interface)
13	NCSI Connector
14	Riser Connector #1 (PCIe Gen5/x16 Slot)
15	Riser Connector #2 (PCIe Gen5/x16 Slot)
16	BMC Firmware Readiness LED
17	OCP 3.0 Connector (PCIe Gen5 x16)



## 4-2 Jumper Setting

## 4-3 Backplane Board Storage Connector

4-3-1 CBP20C5



Item	Description
1	SlimLine Connector (SFF-8654 4i/U_2_0)
2	SlimLine Connector (SFF-8654 4i/U_2_1)
3	SlimLine Connector (SFF-8654 4i/U_2_2)
4	SlimLine Connector (SFF-8654 4i/U_2_3)
5	SlimLine Connector (SFF-8654 4i/U_2_4)
6	SlimLine Connector (SFF-8654 4i/U_2_5)
7	SlimLine Connector (SFF-8654 4i/U_2_6)
8	SlimLine Connector (SFF-8654 4i/U_2_7)
9	SlimLine Connector (SFF-8654 4i/U_2_8)
10	SlimLine Connector (SFF-8654 4i/U_2_9)
11	SlimLine Connector (SFF-8654 4i/U_2_10)
12	SlimLine Connector (SFF-8654 4i/U_2_11)
13	SlimLine Connector (SFF-8654 4i/SL_SAS0)
14	SlimLine Connector (SFF-8654 4i/SL_SAS1)
15	SlimLine Connector (SFF-8654 4i/SL_SAS2)

# Chapter 5 BIOS Setup

BIOS (Basic Input and Output System) records hardware parameters of the system in the EFI on the motherboard. Its major functions include conducting the Power-On Self-Test (POST) during system startup, saving system parameters, loading the operating system etc. The BIOS includes a BIOS Setup program that allows the user to modify basic system configuration settings or to activate certain system features. When the power is turned off, the battery on the motherboard supplies the necessary power to the CMOS to keep the configuration values in the CMOS.

To access the BIOS Setup program, press the <DEL> key during the POST when the power is turned on.



- BIOS flashing is potentially risky, if you do not encounter any problems when using the current BIOS version, it is recommended that you don't flash the BIOS. To flash the BIOS, do it with caution. Inadequate BIOS flashing may result in system malfunction.
- It is recommended that you not alter the default settings (unless you need to) to prevent system instability or other unexpected results. Inadequately altering the settings may result in system's failure to boot. If this occurs, try to clear the CMOS values and reset the board to default values. (Refer to the Exit section in this chapter or introductions of the battery/clearing CMOS jumper in Chapter 4 for how to clear the CMOS values.)

#### **BIOS Setup Program Function Keys**

	•
<←><→>	Move the selection bar to select the screen
<↑><↓>	Move the selection bar to select an item
<+>	Increase the numeric value or make changes
<->	Decrease the numeric value or make changes
<enter></enter>	Execute command or enter the submenu
<esc></esc>	Main Menu: Exit the BIOS Setup program
	Submenus: Exit current submenu
<f1></f1>	Show descriptions of general help
<f3></f3>	Restore the previous BIOS settings for the current submenus
<f9></f9>	Load the Optimized BIOS default settings for the current submenus
<f10></f10>	Save all the changes and exit the BIOS Setup program

#### Main

This setup page includes all the items of the standard compatible BIOS.

#### Advanced

This setup page includes all the items of AMI BIOS special enhanced features.

(ex: Auto detect fan and temperature status, automatically configure hard disk parameters.)

#### Chipset

This setup page includes all the submenu options for configuring the functions of the Platform Controller Hub.

#### Server Management

Server additional features enabled/disabled setup menus.

#### Security

Change, set, or disable supervisor and user password. Configuration supervisor password allows you to restrict access to the system and BIOS Setup.

A supervisor password allows you to make changes in BIOS Setup.

A user password only allows you to view the BIOS settings but not to make changes.

#### Boot

This setup page provides items for configuration of the boot sequence.

#### Save & Exit

Save all the changes made in the BIOS Setup program to the CMOS and exit BIOS Setup. (Pressing <F10> can also carry out this task.)

Abandon all changes and the previous settings remain in effect. Pressing <Y> to the confirmation message will exit BIOS Setup. (Pressing <Esc> can also carry out this task.)

## 5-1 The Main Menu

Once you enter the BIOS Setup program, the Main Menu (as shown below) appears on the screen. Use arrow keys to move among the items and press <Enter> to accept or enter other sub-menu.

#### Main Menu Help

The on-screen description of a highlighted setup option is displayed on the bottom line of the Main Menu.

#### Submenu Help

While in a submenu, press <F1> to display a help screen (General Help) of function keys available for the menu. Press <Esc> to exit the help screen. Help for each item is in the Item Help block on the right side of the submenu.



When the system is not stable as usual, select the **Restore Defaults** item to set your system to its defaults.

The BIOS Setup menus described in this chapter are for reference only and may differ by BIOS version.

Aptio Setup – AMI Main Advanced Chipset Server Mgmt Security Boot Save & Exit				
BIOS Information Project Name Project Version Build Date and Time	MS33-DCO-000 F08 11/27/2022 16:58:58	•		
BMC Information BMC Firmware Version	13.04.07			
Processor Information CPU Brand String Max CPU Speed	Intel(R) Xeon(R) Gold 6454S			
CPU Signature Processor Core Microcode Patch	806F6 32 280000C0	++: Select Screen		
Platform Information Processor	SPR E3	t↓: Select Item K/H: Scroll Help Area Up/Down.		
PCH RC Revision	EBG - B1 91.D05	Enter: Select +/-: Change Opt. F1: General Help		
Memory Information Total Memory Usable Memory	65536 MB 65536 MB	F3: Previous Values F9: Optimized Defaults F10: Save & Exit		
Memory Frequency	4800 MHz	ESC: Exit		
Ver	sion 2.22.1287 Copyright (C) 2022 f	IMF		

Aptio Setup – AMI Main Advanced Chipset Server Mgmt Security Boot Save & Exit				
BMC Information BMC Firmware Version	13.04.07	▲ Set the Time. Use Tab to switch between Time elements.		
Processor Information CPU Brand String Max CPU Speed CPU Signature Processor Core Microcode Patch	Intel(R) Xeon(R) Gold 64545 2200 MHz 806F6 32 280000C0			
Platform Information Processor PCH RC Revision	SPR E3 EDG - D1 91.D05			
Memory Information Total Memory Usable Memory Memory Frequency	65536 MB 65536 MB 4800 MHz	K/M: Scroll Help Area Up/Down. Enter: Select +/-: Change Opt.		
Onboard LAN Information LAN1 MAC Address	D8-5E-D3-4F-CB-2E	F1: General Help F3: Previous Values F9: Optimized Defaults F10: Save & Exit		
System Date System Time	[Fri 05/30/2025] [11:36:29]	ESC: Exit		

Parameter	Description
BIOS Information	
Project Name	Displays the project name information.
Project Version	Displays version number of the BIOS setup utility.
Build Date and Time	Displays the date and time when the BIOS setup utility was created.
BMC Information <sup>(Note1)</sup>	
BMC Firmware Version <sup>(Note1)</sup>	Displays BMC firmware version information.
Processor Information	
CPU Brand String/ Max CPU Speed / CPU Signature / Processor Core / Microcode Patch	Displays the technical information for the installed processor(s).
Platform Information	
Processor/ PCH/ RC Revision	Displays the information of the installed processor(s) and PCH.
Memory Information(Note2)	
Total Memory	Displays the total memory size of the installed memory.
Usable Memory	Displays the usable memory size of the installed memory.

(Note1) Functions available on selected models.

(Note2) This section will display capacity and frequency information of the memory that the customer has installed.

Parameter	Description
Memory Frequency	Displays the frequency information of the installed memory.
Onboard LAN Information <sup>(Note3)</sup>	
LAN# MAC Address	Displays LAN MAC address information.
System Date	Sets the date following the weekday-month-day-year format.
System Time	Sets the system time following the hour-minute-second format.

(Note3) The number of LAN ports listed will depend on the motherboard / system model.

## 5-2 Advanced Menu

The Advanced Menu displays submenu options for configuring the function of various hardware components. Select a submenu item, then press <Enter> to access the related submenu screen.

Aptio Setup – AMI Main Advanced Chipset Server Mgmt Security Boot Save & Exit		
<ul> <li>Trusted Computing</li> <li>Serial Port Console Redirection</li> <li>SIO Configuration</li> <li>PCI Subsystem Settings</li> <li>USB Configuration</li> <li>Network Stack Configuration</li> <li>Note Configuration</li> <li>NWE Configuration</li> <li>Chipset Configuration</li> <li>Tls Auth Configuration</li> <li>Instel(R) I210 Gigabit Network Connection - D8:5E:D3:4F:CB:2E</li> <li>VLAN Configuration (MAC:D05ED34FCB2E)</li> </ul>	Trusted Computing Settings	
▶ Driver Health	<pre>T4: Select Item K/M: Scroll Help Area Up/Doun. Enter: Select +/-: Change Opt. F1: General Help F3: Previous Values F9: Optimized Defaults F10: Save &amp; Exit ESC: Exit</pre>	
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## 5-2-1 Trusted Computing

Advanced	Aptio Setup – AMI	
Configuration TFM v1.2 Support NO Security Device Found	(Enable)	Enables or Disables BIOS support for security device. 0.3. will not sho Security Device. TGG EFI protocol and INTIA interface will not be available.
		<pre>++: Select Screen 11: Select Item K/M: Scroll Help Area Up/Down. Enter: Select +/-: Change Opt. F1: General Help F3: Previous Values F9: Optimized Defaults F10: Save &amp; Exit ESC: Exit</pre>
Versi	ion 2.22.1287 Copyright (C) 20	D22 AMI

Parameter	Description
Configuration	
TPM v1.2 Support	Enable/Disable BIOS support for security device. OS will not show security device. TCG EFI protocol and INT1A interface will not be available. Options available: Disable, Enable. Default setting is <b>Enable</b> .

### 5-2-2 Serial Port Console Redirection

Advanced	Aptio Setup – AMI	
COM1 Console Redirection Serial Port for Out-of-Band Manage Windows Emergency Management Servi Console Redirection EMS Console Redirection Settings		Console Redirection Enable or Disable.
		<pre>++: Select Screen f1: Select Item K/M: Scroll Help Area Up/Down. Enter: Select +/-: Change Opt. F1: General Help F3: Previous Values F9: Optimized Defaults F10: Save &amp; Exit ESC: Exit</pre>
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Parameter	Description	
COM1 Console Redirection <sup>(Note)</sup>	Console redirection enables the users to manage the system from a remote location. Options available: Enabled, Disabled. Default setting is <b>Disabled</b> .	
	Press [Enter] to configure advanced items. Please note that this item is configurable when COM1 Console Redirection is set to Enabled.	
COM1 Console Redirection Settings	<ul> <li>Terminal Type <ul> <li>Selects a terminal type to be used for console redirection.</li> <li>Options available: VT100, VT100PLUS, VT-UTF8, ANSI. Default setting is VT100PLUS.</li> </ul> </li> <li>Bits per second <ul> <li>Selects the transfer rate for console redirection.</li> <li>Options available: 9600, 19200, 38400, 57600, 115200. Default setting is 115200.</li> </ul> </li> <li>Data Bits</li> </ul>	
	<ul> <li>Selects the number of data bits used for console redirection.</li> <li>Options available: 7, 8. Default setting is 8.</li> </ul>	

(Note) Advanced items prompt when this item is defined.

Parameter	Description
COM1 Console Redirection Settings (continued)	<ul> <li>Parity <ul> <li>A parity bit can be sent with the data bits to detect some transmission errors.</li> <li>Even: parity bit is 0 if the num of 1's in the data bits is even.</li> <li>Odd: parity bit is 0 if num of 1's in the data bits is odd.</li> <li>Mark: parity bit is always 1. Space: Parity bit is always 0.</li> <li>Mark and Space Parity do not allow for error detection.</li> <li>Options available: None, Even, Odd, Mark, Space. Default setting is None.</li> </ul> </li> <li>Stop Bits <ul> <li>Stop Bits</li> <li>Stop bits indicate the end of a serial data packet. (A start bit indicates the beginning). The standard setting is 1 stop bit. Communication with slow devices may require more than 1 stop bit.</li> <li>Options available: 1, 2. Default setting is 1.</li> </ul> </li> <li>Flow Control <ul> <li>Flow control can prevent data loss from buffer overflow. When sending data, if the receiving buffers are full, a 'stop' signal can be sent to stop the data flow. Once the buffers are empty, a 'start' signal can be sent to re-start the flow. Hardware flow control uses two wires to send start/stop signals.</li> <li>Options available: None, Hardware RTS/CTS. Default setting is None.</li> </ul> </li> <li>VT-UTF8 Combo Key Support <ul> <li>Enable/Disable the VT-UTF8 Combo Key Support.</li> <li>Options available: Enabled, Disabled. Default setting is Enabled.</li> </ul> </li> <li>Recorder Mode <ul> <li>When this mode enabled, only texts will be send. This is to capture Terminal data.</li> <li>Options available: Enabled, Disabled. Default setting is Disabled.</li> </ul> </li> <li>Resolution 100x31 <ul> <li>Enable/Disable extended terminal resolution.</li> <li>Options available: Chabled, Disabled. Default setting is Enabled.</li> </ul> </li> <li>Putty KeyPad <ul> <li>Selects Function Key and KeyPad on Putty.</li> <li>Options available: VT100, LINUX, XTERMR6, SC0, ESCN, VT400. Default setting is VT100.</li> </ul> </li> </ul>

Parameter	Description
Serial Port for Out-of-Band Management / Windows Emergency Management Services (EMS) Console Redirection <sup>(Note)</sup>	EMS console redirection allows the user to configure Console Redirection Settings to support Out-of-Band Serial Port management. Options available: Enabled, Disabled. Default setting is <b>Disabled</b> .
Serial Port for Out-of-Band EMS Console Redirection Settings	<ul> <li>Press [Enter] to configure advanced items.</li> <li>Please note that this item is configurable when Serial Port for Out-of-Band Management EMS Console Redirection is set to Enabled.</li> <li>Out-of-Band Mgmt Port <ul> <li>Microsoft Windows Emergency Management Service (EMS) allows for remote management of a Windows Server OS through a serial port.</li> <li>Default setting is COM1.</li> </ul> </li> <li>Terminal Type EMS <ul> <li>Selects a terminal type to be used for console redirection.</li> <li>Options available: VT100, VT100PLUS, VT-UTF8, ANSI. Default setting is VT10PLUS.</li> </ul> </li> <li>Bits per second EMS <ul> <li>Selects the transfer rate for console redirection.</li> <li>Options available: 9600, 19200, 57600, 115200. Default setting is 115200.</li> </ul> </li> <li>Flow Control EMS <ul> <li>Flow control EMS</li> <li>Flow control can prevent data loss from buffer overflow. When sending data, if the receiving buffers are full, a 'stop' signal can be sent to stop the data flow. Once the buffers are empty, a 'start' signal can be sent to re-start the flow. Hardware flow control uses two wires to send start/stop signals.</li> <li>Options available: None, Hardware RTS/CTS, Software Xon/Xoff. Default setting is None.</li> </ul> </li> </ul>

## 5-2-3 SIO Configuration

Aptio Setup - AMI Advanced	
AMI SIO Driver Version : A5.18.00 Super IO Chip Logical Device(s) Configuration [*Active*] Serial Port WARNING: Logical Devices state on the left side of the control, reflects the current Logical Device state. Changes made during Setup Session will be shown after you restart the system.	View and Set Basic properties of the SIO Logical device. Like IO Base, IRQ Range, DMA Channel and Device Mode.
	<pre>++: Select Screen 11: Select Item K/M: Scroll Help Area Uu/Down. Enter: Select +/-: Change Opt. F1: General Help F3: Previous Values F9: Optimized Defaults F10: Save &amp; Exit ESC: Exit</pre>
Version 2.22.1287 Copyright (C) 2022 AM	I

Parameter	Description
AMI SIO Driver Version	Displays the AMI SIO driver version information.
Super IO Chip Logical Device(s) Configuration	
[*Active*] Serial Port	<ul> <li>Press [Enter] to configure advanced items.</li> <li>Use This Device <ul> <li>When set to Enabled allows you to configure the serial port settings. When set to Disabled, displays no configuration for the serial port.</li> <li>Options available: Enabled, Disabled. Default setting is Enabled.</li> </ul> </li> <li>Logical Device Settings/Current: <ul> <li>Displays the serial port base I/O address and IRQ.</li> </ul> </li> <li>Possible: <ul> <li>Configures the serial port base I/O address and IRQ.</li> <li>Use Automatic Settings</li> <li>IO=3F8h; IRQ=4; DMA;</li> <li>IO=3F8h; IRQ=4; DMA;</li> <li>IO=2F8h; IRQ=4; DMA;</li> <li>IO=2E8h; IRQ=4; DMA;</li> <li>IO=2E8h; IRQ=4; DMA;</li> <li>Default setting is Use Automatic Settings.</li> </ul> </li> </ul>

### 5-2-4 PCI Subsystem Settings

PCI Bus Driver Version	A5.01.29	Enable/Disable OCP2 I/O RO
OCP2 I/O ROM	[Enabled]	
OCP2 Lanes	[Auto]	
OCP2 Max Link Speed	[Auto]	
Slot2 I/O ROM	[Enabled]	
Slot2 Lanes	[Auto]	
Slot2 Max Link Speed	[Auto]	
M2M I/O ROM	[Enabled]	
M2M Lanes	[Auto]	
M2M Max Link Speed	[Auto]	
Onboard LAN1 Controller	[Enabled]	++: Select Screen
Onboard LAN1 I/O ROM	[Enabled]	↑↓: Select Item
		K/M: Scroll Help Area
PCI Devices Common Settings:		Up/Down.
Above 4G Decoding	[Enabled]	Enter: Select
SR-IOV Support	[Enabled]	+/-: Change Opt.
		F1: General Help
		F3: Previous Values
		F9: Optimized Defaults
		F10: Save & Exit
		ESC: Exit

Parameter	Description	
PCI Bus Driver Version	Displays the PCI Bus Driver version information.	
OCP# I/O ROM <sup>(Note1)</sup>	When enabled, this setting will initialize the device expansion ROM for the related PCI-E slot. Options available: Enabled, Disabled. Default setting is <b>Enabled</b> .	
OCP# Lanes <sup>(Note1)</sup>	Change the PCIe lanes. Default setting is Auto.	
OCP# Max Link Speed <sup>(Note1)</sup>	Configure PCIe max link speed. Options available: Auto, Gen1, Gen2, Gen3, Gen4, Gen5. Default setting is <b>Auto</b> .	
SLOT# I/O ROM <sup>(Note2)</sup>	When enabled, this setting will initialize the device expansion ROM for the related PCI-E slot. Options available: Enabled, Disabled. Default setting is <b>Enabled</b> .	
SLOT# Lanes <sup>(Note2)</sup>	Change the PCIe lanes. Default setting is Auto.	
SLOT# Max Link Speed <sup>(Note2)</sup>	Configure PCIe max link speed. Options available: Auto, Gen1, Gen2, Gen3, Gen4, Gen5. Default setting is <b>Auto</b> .	

 $(Note1) \quad \mbox{This section is dependent on the available OCP Slot.}$ 

 $({\it Note2}) \quad {\it This section is dependent on the available PCIe Slot.}$ 

Parameter	Description
Onboard LAN1/ LAN2 I/O Controller <sup>(Note3)</sup>	Enable/Disable the onboard LAN controller. Options available: Enabled, Disabled. Default setting is <b>Enabled</b> .
Onboard LAN1 I/O ROM <sup>(Note3)</sup>	Enable/Disable the onboard LAN devices, and initializes device expansion ROM. Options available: Enabled, Disabled. Default setting is <b>Enabled</b> .
PCI Devices Common Settings	
Above 4G Decoding	Enable/Disable memory mapped I/O to 4GB or greater address space (Above 4G Decoding). Options available: Enabled, Disabled. Default setting is <b>Enabled</b> .
SR-IOV Support	If the system has SR-IOV capable PCIe devices, this item Enable/Disable Single Root IO Virtualization Support. Options available: Enabled, Disabled. Default setting is <b>Enabled</b> .

(Note3) This section is dependent on the available LAN controller.

## 5-2-5 USB Configuration

Advanced	Aptio Setup — AMI	
USB Configuration		This is a workaround for
USB Devices: 2 Keyboards, 1 Mouse, 3 Hubs		OSes without XHCI hand-off support. The XHCI ownership change should be
		claimed by XHCI driver.
XHCI Hand-off USB Mass Storage Driver Support	[Enabled] [Enabled]	
Port 60/64 Emulation	[Enabled]	
		++: Select Screen T4: Select Item K/M: Scroll Help Area Up/Down. Enter: Select +/-: Change Opt. F1: General Help F3: Previous Values F9: Optimized Defaults F10: Save & Exit ESC: Exit
Version 2	.22.1287 Copyright (C) 2022 AMI	

Parameter	Description
USB Configuration	
USB Devices:	Displays the USB devices connected to the system.
XHCI Hand-off	Enable/Disable the XHCI (USB 3.0) Hand-off support. Options available: Enabled, Disabled. Default setting is <b>Enabled</b> .
USB Mass Storage Driver Support <sup>(Note)</sup>	Enable/Disable the USB Mass Storage Driver Support. Options available: Enabled, Disabled. Default setting is <b>Enabled</b> .
Port 60/64 Emulation	Enables the I/O port 60h/64h emulation support. This should be enabled for the complete USB Keyboard Legacy support for non- USB aware OSes. Options available: Enabled, Disabled. Default setting is <b>Enabled</b> .

(Note) This item is present only if you attach USB devices.

## 5-2-6 Network Stack Configuration

Advanced	Aptio Setup – AMI	
Network Stack IPv4 PXE Support IPv4 HTTP Support IPv6 FXE Support IPv6 HTTP Support PXE boot wait time Media detect count	[Enabled] [Enabled] [Disabled] [Disabled] [Disabled] 0	Enable/Disable UEFI Network Stack
		<pre>++: Select Screen t4: Select Item K/M: Scroll Help Area Up/Down. Enter: Select +/-: Change Opt. F1: General Help F3: Previous Values F3: Optimized Defaults F10: Save &amp; Exit ESC: Exit</pre>

Parameter	Description
Network Stack	Enable/Disable the UEFI network stack. Options available: Enabled, Disabled. Default setting is <b>Enabled</b> .
Ipv4 PXE Support	Enable/Disable the Ipv4 PXE feature. Options available: Enabled, Disabled. Default setting is <b>Enabled</b> .
Ipv4 HTTP Support	Enable/Disable the Ipv4 HTTP feature. Options available: Enabled, Disabled. Default setting is <b>Disabled</b> .
Ipv6 PXE Support	Enable/Disable the Ipv6 PXE feature. Options available: Enabled, Disabled. Default setting is <b>Disabled</b> .
Ipv6 HTTP Support	Enable/Disable the Ipv6 HTTP feature. Options available: Enabled, Disabled. Default setting is <b>Disabled</b> .
PXE boot wait time	Wait time in seconds to press ESC key to abort the PXE boot. Press the <+> / <-> keys to increase or decrease the desired values.
Media detect count	Number of times the presence of media will be checked. Press the <+> / <-> keys to increase or decrease the desired values.

## 5-2-7 Post Report Configuration

Post Report Configuration		Post Error Message Suppor
Error Message Report		Enabled/Disabled
Post Error Message		
Halt On	[No Error]	
		++: Select Screen
		t↓: Select Item
		K/M: Scroll Help Area
		Up/Down.
		Enter: Select +/-: Change Opt.
		F1: General Help
		F3: Previous Values
		F9: Optimized Defaults
		F10: Save & Exit
		ESC: Exit

Parameter	Description
Post Report Configuration	
Error Message Report	
Post Error Message	Enable/Disable the POST Error Message support. Options available: Enabled, Disabled. Default setting is <b>Enabled</b> .
Halt On	Options available: No Error, All Error. Default setting is No Error.

## 5-2-8 NVMe Configuration

NVMe Configuration	BIOS Build-In is default
NVME OPROM Select No NVME Device Found	setting. Select Device Itself, then this NVMe page will not display any NVMe device. Unless the device doesn't have OPROM it will show.
	++: Select Screen T1: Select Item K/M: Scroll Help Area Up/Down. Enter: Select +/-: Change Opt. F1: General Help F3: Previous Values F3: Optimized Defaults F10: Save & Exit ESC: Exit

Parameter	Description
NVMe Configuration	Displays the NVMe devices connected to the system.
NVMe OPROM Select	Options available: BIOS Build-In, NVMe Device. Default setting is <b>BIOS</b> Build-In.

### 5-2-9 Chipset Configuration

Aptio Setup — AMI	
[Last State] [0x1000]	Specify what state when power is re-applied after a power failure (G3 state
(Enabled) (Enabled) (Disabled)	a power failore (bu state
	Enter: Select +/-: Change Opt. F1: General Help F3: Previous Values F9: Optimized Defaults F10: Save & Exit
	[Last State] [0×1000] [Enabled] [Enabled]

Parameter	Description
Restore on AC Power Loss <sup>(Note)</sup>	Defines the power state to resume to after a system shutdown that is due to an interruption in AC power. When set to Last State, the system will return to the active power state prior to shutdown. When set to Power Off, the system remains off after power shutdown. Options available: Last State, Power Off, Power On, Unspecified. The default setting depends on the BMC setting.
P2P Bridge IO Size	Specifies P2P Bridge IO aligned to the size. Options available: 0x100, 0x150, 0x1000. Default setting is <b>0x1000</b> .
SATA HDD Security Frozen	Enable/Disable this item to send freeze lock command to SATA HDD. Options available: Enabled, Disabled. Default setting is <b>Enabled</b> .
NVMe SSD Security Frozen	Attempt to send freeze lock command to NVMe SSDs during boot. Options available: Enabled, Disabled. Default setting is <b>Enabled</b> .
Chassis Opened Warning	Enable/Disable the chassis intrusion alert function. Options available: Enabled, Disabled, Clear. Default setting is <b>Disabled</b> .

(Note) When the power policy is controlled by BMC, please wait for 15-20 seconds for BMC to save the last power state.

## 5-2-10 TIs Auth Configuration

	Press <enter> to configure</enter>
	Server CA.
Client Cert Configuration	
	++: Select Screen
	↑↓: Select Item
	K/M: Scroll Help Area Up/Down.
	Enter: Select +/-: Change Opt.
	F1: General Help
	F3: Previous Values F9: Optimized Defaults
	F10: Save & Exit
	ESC: Exit

Parameter	Description
	Press [Enter] for configuration of advanced items.
	Enroll Cert
	<ul> <li>Press [Enter] to enroll a certificate</li> </ul>
	Enroll Cert Using File
Conver CA Configuration	Cert GUID
Server CA Configuration	Input digit character in 1111111-2222-3333-4444-1234567890ab
	format.
	<ul> <li>Commit Changes and Exit</li> </ul>
	<ul> <li>Discard Changes and Exit</li> </ul>
	Delete Cert
Client Cert Configuration	Press [Enter] for configuration of advanced items.

## 5-2-11 iSCSI Configuration

	Change the priority using
• Host iSCSI Configuration	+/- keys. Use arrow keys to select the attempt then press +/- to move the attempt up/down in the attempt order list.
	++: Select Screen tl: Select Item K/M: Scroll Help Area Up/Doun. Enter: Select +/-: Change Opt. F1: General Help F3: Previous Values F9: Optimized Defaults F10: Save & Exit ESC: Exit

Parameter	Description	
Attempt Priority	<ul> <li>Press [Enter] configure advanced items.</li> <li>Attempt Priority <ul> <li>Use arrow keys to select the attempt, then press +/- keys to move the attempt up/down in the attempt order list.</li> </ul> </li> <li>Commit Changes and Exit</li> </ul>	
Host iSCSI Configuration	<ul> <li>Press [Enter] to configure advanced items.</li> <li>iSCSI Initiator Name <ul> <li>Only IQN format is accepted. Range: from 4 to 223</li> </ul> </li> <li>Add an Attempt</li> <li>Delete Attempts</li> <li>Change Attempt Order</li> </ul>	

## 5-2-12 Intel(R) Ethernet Controller X710 for 10GBASE-T

Advanced	Aptio Setup – AMI	
▶ NIC Configuration		Click to configure the network device port.
Blink LEDs	0	
UEFI Driver Adapter PBA Device Name	Intel(R) PRO/1000 8.5.21 PCI-E 140724–006 Intel(R) I210 Gigabit	
Chip Type PCI Device ID PCI Address	Network Connection Intel i210 1533 01:00:00	
Link Status	[Disconnected]	++: Select Screen
MAC Address Virtual MAC Address	D8:5E:D3:4F:C8:2E 00:00:00:00:00:00	1: Select Item K/M: Scroll Help Area Up/Doun. Enter: Select +/-: Change Opt. F1: General Help F3: Previous Values F9: Optimized Defaults F10: Save & Exit ESC: Exit
Ve	rsion 2.22.1287 Copyright (C) 2022 /	AMI
	Aptio Setup – AMI	
Advanced		
Link Speed Wake On LAN	[Auto Negotiated] [Enabled]	Specifies the port speed used for the selected boot protocol.
		++: Select Screen 14: Select Item K/M: Scroll Help Area Up/Down. Enter: Select +/-: Change Opt. F1: General Help F3: Previous Values
		F9: Optimized Defaults F10: Save & Exit ESC: Exit

Parameter	Description
NIC Configuration	<ul> <li>Press [Enter] to configure advanced items.</li> <li>Link Speed <ul> <li>Allows for automatic link speed adjustment.</li> <li>Options available: Auto Negotiated, 10 Mbps Half, 10 Mbps Full, 100 Mbps Half, 100 Mbps Full. Default setting is Auto Negotiated.</li> </ul> </li> <li>Wake On LAN <ul> <li>Enables power on of the system via LAN. Note that configuring Wake on LAN in the operating system does not change the value of this setting, but does override the behavior of Wake on LAN in OS controlled power states.</li> <li>Options available: Enabled, Disabled. Default setting is Enabled.</li> </ul> </li> </ul>
Blink LEDs	Identifies the physical network port by blinking the associated LED. Press the numeric keys to adjust desired values (up to 15 seconds).
UEFI Driver	Displays the technical specifications for the Network Interface Controller.
Adapter PBA	Displays the technical specifications for the Network Interface Controller.
Device Name	Displays the technical specifications for the Network Interface Controller.
Chip Type	Displays the technical specifications for the Network Interface Controller.
PCI Device ID	Displays the technical specifications for the Network Interface Controller.
PCI Address	Displays the technical specifications for the Network Interface Controller.
Link Status	Displays the technical specifications for the Network Interface Controller.
MAC Address	Displays the technical specifications for the Network Interface Controller.
Virtual MAC Address	Displays the technical specifications for the Network Interface Controller.

### 5-2-13 VLAN Configuration

Create new VLAN VLAN ID	0	VLAN ID of new VLAN or existing VLAN, valid valu
Priority Add VLAN	0	is 0~4094
Configured VLAN List Remove VLAN		
		++: Select Screen
		t∔: Select Item K/M: Scroll Help Area Up/Down.
		Enter: Select
		+/-: Change Opt. F1: General Help F3: Previous Values
		F9: Optimized Defaults F10: Save & Exit
		ESC: Exit

Parameter	Description
Enter Configuration Menu	<ul> <li>Press [Enter] to configure advanced items.</li> <li>Create new VLAN</li> <li>VLAN ID <ul> <li>Sets VLAN ID for a new VLAN or an existing VLAN.</li> <li>Press the &lt;+&gt; / &lt;-&gt; keys to increase or decrease the desired values.</li> <li>The valid range is from 0 to 4094.</li> </ul> </li> <li>Priority <ul> <li>Sets 802.1Q Priority for a new VLAN or an existing VLAN.</li> <li>Press the &lt;+&gt; / &lt;-&gt; keys to increase or decrease the desired values.</li> <li>The valid range is from 0 to 7.</li> </ul> </li> <li>Add VLAN <ul> <li>Press [Enter] to create a new VLAN or update an existing VLAN.</li> </ul> </li> <li>Configured VLAN List</li> <li>Remove VLAN <ul> <li>Press [Enter] to remove an existing VLAN.</li> </ul> </li> </ul>

#### 5-2-14 Driver Health

	Provides Health Status for the Drivers/Controllers
	++: Select Screen
	14: Select Item
	K/M: Scroll Help Area Up/Down.
	Enter: Select
	+/-: Change Opt. F1: General Help
	F3: Previous Values
	F9: Optimized Defaults
	F10: Save & Exit ESC: Exit

Parameter	Description
Driver Health	Displays driver health status of the devices/controllers if installed

## 5-3 Chipset Menu

Chipset Setup menu displays submenu options for configuring the function of Platform Controller Hub(PCH). Select a submenu item, then press <Enter> to access the related submenu screen.

Aptio Setup - AMI		
Main Advanced Chipset Server Mgmt Security Boot Save & Exit		
<ul> <li>Processor Configuration</li> <li>Common RefCode Configuration</li> <li>UPI Configuration</li> <li>Memory Configuration</li> <li>IIO Configuration</li> <li>Advanced Power Management Configuration</li> <li>PCH-IO Configuration</li> <li>Miscellaneous Configuration</li> <li>Server ME Configuration</li> <li>Runtime Error Logging</li> <li>Power Policy</li> </ul>	Displays and provides options to change the Processor Settings	
	++: Select Screen T1: Select Item K/M: Scroll Help Area Up/Down. Enter: Select +/-: Change Opt. F1: General Help F3: Previous Values F9: Optimized Defaults F10: Save & Exit ESC: Exit	
Version 2.22.1287 Copyright (C) 2022 AMI		

### 5-3-1 Processor Configuration

Chipset	Aptio Setup - AMI	
Processor Configuration		▲ Change Per-Socket Settings
Per–Socket Configuration		
Processor Socket	Socket 0	
Processor ID	000806F6*	
Processor Die Type	XCC	
Processor Frequency	2.200GHz	
Processor Max Ratio	16H	
Processor Min Ratio	08H	
Microcode Revision	2B0000C0	
L1 Cache RAM(Per Core)	80KB	
L2 Cache RAM(Per Core)	2048KB	
L3 Cache RAM(Per Package)	61440KB	
Processor O Version	Intel(R) Xeon(R) Gold 6	++: Select Screen
	454S	↑↓: Select Item
		K/M: Scroll Help Area
Enable LP [Global]	[ALL LPS]	Up/Down.
Hardware Prefetcher	[Enable]	Enter: Select
L2 RFO Prefetch Disable	[Disable]	+/-: Change Opt.
Adjacent Cache Prefetch DCU Streamer Prefetcher	[Enable] [Enable]	F1: General Help F3: Previous Values
DCU IP Prefetcher	[Enable]	F9: Optimized Defaults
Extended APIC	[Enable]	F10: Save & Exit
Enable Intel(R) TXT	[Disable]	ESC: Exit
VMX	[Enable]	
Versi	on 2.22.1287 Copyright (C) 2022	AMI
Chipset	Aptio Setup - AMI	
		Displays and provides
Chipset L2 Cache RAM(Per Core) L3 Cache RAM(Per Package)	Aptio Setup - AMI 2048KB 61440KB	▲ Displays and provides
L2 Cache RAM(Per Core) L3 Cache RAM(Per Package)	2048KB	
L2 Cache RAM(Per Core) L3 Cache RAM(Per Package)	2048KB 61440KB	option to change the
L2 Cache RAM(Per Core) L3 Cache RAM(Per Package) Processor 0 Version	2048KB 61440KB Intel(R) Xeon(R) Gold 6	option to change the
L2 Cache RAM(Per Core) L3 Cache RAM(Per Package) Processor O Version Enable LP [Global]	2048KB 61440KB Intel(R) Xeon(R) Gold 6 454S	option to change the
L2 Cache RAM(Per Core) L3 Cache RAM(Per Package) Processor 0 Version Enable LP [Global] Hardware Prefetcher L2 RFO Prefetch Disable	2048KB 61440KB Intel(R) Xeon(R) Gold 6 454S [ALL LPS] [Enable] [Disable]	option to change the
L2 Cache RAM(Per Core) L3 Cache RAM(Per Package) Processor 0 Version Enable LP [Global] Hardware Prefetcher L2 RFO Prefetch Disable Adjacent Cache Prefetch	2048KB 61440KB Intel(R) Xeon(R) Gold 6 454S [ALL LPS] [Enable] [Disable] [Enable]	option to change the
L2 Cache RAM(Per Core) L3 Cache RAM(Per Package) Processor 0 Version Enable LF [Global] Hardware Prefetcher L2 RFO Prefetch Disable Adjacent Cache Prefetch DCU Streamer Prefetcher	2048KB 61440KB Intel(R) Xeon(R) Gold 6 454S [ALL LPS] [Enable] [Disable] [Enable] [Enable]	option to change the
L2 Cache RAM(Per Core) L2 Cache RAM(Per Package) Processor 0 Version Enable LF [Global] Hardware Prefetcher L2 RFO Prefetcher L2 RFO Prefetcher DCU Streamer Prefetcher DCU JP Prefetcher	2048KB 61440KB Intel(R) Xeon(R) Gold 6 454S [ALL LPS] [Enable] [Disable] [Enable] [Enable] [Enable]	option to change the
L2 Cache RAM(Per Core) L3 Cache RAM(Per Package) Processor 0 Version Enable LP [Global] Hardware Prefetcher L2 RFO Prefetch Disable Adjacent Cache Prefetch DCU JP Prefetcher DCU JP Prefetcher DCU JP Prefetcher Extended APIC	2048KB 61440KB Intel(R) Xeon(R) Gold 6 454S [ALL LPs] [Enable] [Disable] [Enable] [Enable] [Enable] [Enable] [Enable]	option to change the
L2 Cache RAM(Per Core) L3 Cache RAM(Per Package) Processor 0 Version Enable LP [Global] Hardware Prefetcher L2 RFO Prefetch Disable Adjacent Cache Prefetch DCU Streamer Prefetcher DCU IP Prefetcher Extended APIC Enable Intel(R) TXT	2049KB 61440KB Intel(R) Xeon(R) Gold 6 454S [ALL LPs] [Enable] [Disable] [Enable] [Enable] [Enable] [Enable] [Enable] [Enable] [Disable]	option to change the Processor CFR Settings
L2 Cache RAM(Per Core) L3 Cache RAM(Per Package) Processor 0 Version Enable LP [Global] Hardware Prefetcher L2 RFD Prefetcher L2 RFD Prefetcher DCU Streamer Prefetcher DCU JP Prefetcher Extended APIC Enable Intel(R) TXT VMX	2048KB 61440KB Intel(R) Xeon(R) Gold 6 454S [ALL LPS] [Enable] [Enable] [Enable] [Enable] [Enable] [Disable] [Disable] [Disable]	option to change the Processor CFR Settings ++: Select Screen
L2 Cache RAM(Per Core) L3 Cache RAM(Per Package) Processor 0 Version Enable LP [Global] Hardware Prefetchen L2 RFO Prefetchen L2 RFO Prefetchen DCU JP Prefetchen DCU JP Prefetchen DCU JP Prefetchen Extended APIC Enable Intel(R) TXT VMX Enable SMX	2048KB 61440KB Intel(R) Xeon(R) Gold 6 454S [ALL LPs] [Enable] [Disable] [Enable] [Enable] [Enable] [Enable] [Disable] [Enable] [Disable]	option to change the Processor CFR Settings ++: Select Screen 14: Select Item
L2 Cache RAM(Per Core) L3 Cache RAM(Per Package) Processor O Version Enable LP [Global] Hardware Prefetcher L2 RFO Prefetch Disable Adjacent Cache Prefetch DCU Streamer Prefetcher DCU JT Prefetcher Extended APIC Enable Intel(R) TXT YMX Enable SMX AGS-MI	2048KB 61440KB Intel(R) Xeon(R) Gold 6 454S [ALL LPS] [Enable] [Enable] [Enable] [Enable] [Enable] [Disable] [Disable] [Disable]	option to change the Processor CFR Settings ++: Select Screen
L2 Cache RAM(Per Core) L3 Cache RAM(Per Package) Processor O Version Enable LP [Global] Hardware Prefetcher L2 RFO Prefetch Disable Adjacent Cache Prefetch DCU Streamer Prefetcher DCU JT Prefetcher Extended APIC Enable Intel(R) TXT YMX Enable SMX AGS-MI	2049KB 61440KB Intel(R) Xeon(R) Gold 6 454S [ALL LPS] [Enable] [Disable] [Enable] [Enable] [Enable] [Enable] [Disable] [Disable] [Disable] [Disable] [Enable]	<pre>option to change the Processor CFR Settings ++: Select Screen fl: Select Item K/M: Scroll Help Area Up/Down. Enter: Select</pre>
L2 Cache RAM(Per Core) L3 Cache RAM(Per Package) Processor O Version Enable LP [Global] Hardware Prefetchen L2 RFO Prefetch Disable Adjacent Cache Prefetch DCU IP Prefetcher Extended APIC Enable Intel(R) TXT VMX Enable SMX AES-NI Debug Consent TME, TME-MT, TDX	2049KB 61440KB Intel(R) Xeon(R) Gold 6 454S [ALL LPS] [Enable] [Disable] [Enable] [Enable] [Enable] [Enable] [Disable] [Disable] [Disable] [Disable] [Disable]	<pre>option to change the Processor CFR Settings  ++: Select Screen 14: Select Item K/M: Scroll Help Area Up/Down. Enter: Select +/-: Change Opt.</pre>
L2 Cache RAM(Per Core) L3 Cache RAM(Per Package) Processon 0 Version Enable LP [Global] Hardware Prefetcher L2 FFO Prefetch Disable Adjacent Cache Prefetch DCU JP Prefetcher DCU JP Prefetcher Extended APIC Enable Intel(R) TXT VMX Enable SMX AES-NI Debug Consent TME, TME-MT, TDX	2049KB 61440KB Intel(R) Xeon(R) Gold 6 4545 [ALL LPS] [Enable] [Disable] [Enable] [Enable] [Enable] [Disable] [Disable] [Enable] [Disable] [Enable] [Disable] [Disable]	<pre>option to change the Processor DFR Settings  ++: Select Screen 14: Select Item K/M: Scroll Help Area Up/Down. Enter: Select +/-: Change Opt. Fi: General Help</pre>
L2 Cache RAM(Per Core) L3 Cache RAM(Per Package) Processor 0 Version Enable LP [Global] Hardware Prefetcher L2 RF0 Prefetch Disable Adjacent Cache Prefetch DCU JP Prefetcher DCU JP Prefetcher DCU JP Prefetcher Extended APIC Enable Intel(R) TXT VMX Enable SMX AES-NI Debug Consent TME, TME-HT, TDX Memory Encryption (TME)	2049KB 61440KB Intel(R) Xeon(R) Gold 6 454S [ALL LPS] [Enable] [Enable] [Enable] [Enable] [Enable] [Enable] [Enable] [Enable] [Disable] [Disable] [Disable] [Disable]	<pre>option to change the Processor CFR Settings  ++: Select Screen 1: Select Item K/M: Scroll Help Area Up/Doum. Enter: Select +/-: Change Opt. F1: General Help F3: Previous Values</pre>
L2 Cache RAM(Per Core) L3 Cache RAM(Per Package) Processor 0 Version Enable LP [Global] Hardware Prefetcher L2 RFO Prefetch Disable Adjacent Cache Prefetch DCU JP Prefetcher DCU JP Prefetcher Extended APIC Enable Intel(R) TXT VMX Enable SMX AES-NI Debug Consent TME, TME-MT, TDX Memory Encryption (TME) SGX setup configuration precondi met. Please check TME, MirrorMC	2048KB 61440KB Intel(R) Xeon(R) Gold 6 454S [ALL LPS] [Enable] [Enable] [Enable] [Enable] [Enable] [Enable] [Disable] [Enable] [Disable] [Disable] [Disable] [Disable] [Disable] [Disable] [Disable] [Disable] [Enable] [Disable] [Enable] [Disable] [Enable] [Disable] [D	<pre>option to change the Processor DFR Settings ++: Select Screen 11: Select Item K/M: Scroll Help Area Up/Down. Enter: Select +/-: Change Opt. F1: General Help F3: Previous Values F9: Optimized Defaults F10: Save &amp; Exit</pre>
L2 Cache RAM(Per Core) L3 Cache RAM(Per Package) Processor 0 Version Enable LP [Global] Handware Prefetcher L2 RFO Prefetcher DCU Streamer Prefetch DCU Streamer Prefetcher DCU IP Prefetcher Extended APIC Enable Intel(R) TXT VMX Enable SMX AES-NI Debug Consent TME, TME-HT, TDX Memory Encryption (TME) SKX setup configuration precondi	2048KB 61440KB Intel(R) Xeon(R) Gold 6 454S [ALL LPS] [Enable] [Enable] [Enable] [Enable] [Enable] [Enable] [Disable] [Enable] [Disable] [Disable] [Disable] [Disable] [Disable] [Disable] [Disable] [Disable] [Enable] [Disable] [Enable] [Disable] [Enable] [Disable] [D	<pre>option to change the Processor CFR Settings  +*: Select Screen 14: Select Item K/M: Scroll Help Area Up/Doun. Enter: Select +/-: Change Opt. Fi: General Help F3: Previous Values F9: Optimized Defaults</pre>

Parameter	Description
Processor Configuration	
Pre-Socket Configuration	<ul> <li>Press [Enter] to configure advanced items.</li> <li>CPU Socket 0 Configuration <ul> <li>Core Disable Bitmap(Hex)</li> <li>Number of Cores to enable. 0 means all cores. FFFFFFF means to disable all cores. The maximum value depends on the number of CPUs available. Press the numeric keys to adjust desired values.</li> </ul> </li> </ul>
Processor Socket / Processor ID / Processor Die Type / Processor Frequency / Processor Max Ratio / Processor Min Ratio / Microcode Revision / L1 Cache RAM(Per Core) / L2 Cache RAM(Per Core) / L3 Cache RAM(Per Package) / Processor # Version	Displays the technical specifications for the installed processor(s).
Enable LP [Global]	Enables Logical processor (Software Method to Enable/Disable Logical Processor threads). Options available: ALL LPs, Single LP. Default setting is <b>ALL LPs</b> .
Hardware Prefetcher	Select whether to enable the speculative prefetch unit of the processor. Options available: Enable, Disable. Default setting is <b>Enable</b> .
L2 RF0 Prefetch Disable	Options available: Enable, Disable. Default setting is <b>Disable</b> .
Adjacent Cache Prefetch	When enabled, cache lines are fetched in pairs. When disabled, only the required cache line is fetched. Options available: Enable, Disable. Default setting is <b>Enable</b> .
DCU Streamer Prefetcher	Enable/Disable DCU streamer prefetcher. Options available: Enable, Disable. Default setting is <b>Enable</b> .
DCU IP Prefetcher	Enable/Disable DCU IP Prefetcher. Options available: Enable, Disable. Default setting is <b>Enable</b> .
Extended APIC	Enable/Disable extended APIC support. Note: The VT-d will be enabled automatically when x2APIC is enabled. Options available: Enable, Disable. Default setting is <b>Enable</b> .
Enable Intel(R) TXT	Enable/Disable the Intel Trusted Execution Technology support function. Options available: Enable, Disable. Default setting is <b>Disable</b> .
VMX	Enable/Disable the Vanderpool Technology. This will take effect after rebooting the system. Options available: Enable, Disable. Default setting is <b>Enable</b> .
Enable SMX	Enable/Disable the Safer Mode Extensions (SMX) support function. Options available: Enable, Disable. Default setting is <b>Disable</b> .
AES-NI	Enable/Disable the AES-NI support. Options available: Enable, Disable. Default setting is <b>Enable</b> .
Debug Consent	Options available: Enable, Disable. Default setting is <b>Disable</b> .

Parameter	Description
Memory Encryption (TME) <sup>(Note)</sup>	Enable/Disable memory encryption (TME). Options available: Enabled, Disabled. Default setting is <b>Disabled</b> .
Total Memory Encryption Multi-Tenant (TME-MT)	Options available: Enabled, Disabled. Default setting is <b>Disabled</b> .
Processor CFR Configuration	<ul> <li>Press [Enter] to configure advanced items.</li> <li>Provision S3M CFR <ul> <li>Options available: Disable, Enable. Default setting is Enable.</li> </ul> </li> <li>Manual Commit S3M FW CFR <ul> <li>Options available: Disable, Enable, Auto. Default setting is Auto.</li> </ul> </li> <li>Provision PUcode CFR <ul> <li>Options available: Disable, Enable. Default setting is Enable.</li> </ul> </li> <li>Manual Commit PUcode CFR <ul> <li>Options available: Enable, Disable, Auto. Default setting is Auto.</li> </ul> </li> <li>Socket0 CFR Revision Info <ul> <li>Displays CFR Revision information of the socket.</li> </ul> </li> </ul>

#### 5-3-2 Common RefCode Configuration



Parameter	Description
Common RefCode Configuration	
Numa	Enable/Disable Non uniform Memory Access(NUMA). Default setting is <b>Enable</b> .
Virtual Numa	Divide physical NUMA nodes into evenly sized virtual NUMA nodes in ACPI table. This may improve Windows performance on CPUs with more than 64 logical processors. Options available: Enable, Disable. Default setting is <b>Disable</b> .

### 5-3-3 UPI Configuration

UPI Configuration	Displays and provides
UPI General Configuration	option to change the UPI General Settings
	++: Select Screen 14: Select Item K/M: Scroll Help Area Up/Down. Enter: Select +/-: Change Opt. F1: General Help F3: Previous Values F9: Optimized Defaults F10: Save & Exit ESC: Exit

Parameter	Description
UPI General Configuration	<ul> <li>Press [Enter] to configure advanced items.</li> <li>UPI Status <ul> <li>Press [Enter] to view the Uncore status.</li> </ul> </li> <li>Link Frequency Select <ul> <li>Selects the UPI link frequency.</li> <li>Options available: 12.8GT/s, 14.4GT/s, 16.0GT/s, Auto, Use Per Link Setting. Default setting is Auto.</li> </ul> </li> <li>SNC <ul> <li>Enable/Disable Sub NUMA Cluster function.</li> <li>Options available: Auto, Disable, Enable SNC2 (2-clusters), Enable SNC4 (4-clusters). Default setting is Auto.</li> </ul> </li> <li>Stale AtoS <ul> <li>Enable/Disable Stale A to S directory optimization.</li> <li>Options available: Disable, Enable, Auto. Default setting is Auto.</li> </ul> </li> <li>LLC dead line alloc <ul> <li>Enable/Disable fill dead lines in LLC.</li> <li>Options available: Disable, Enable, Auto. Default setting is Enable.</li> </ul> </li> <li>MMIO High Base <ul> <li>Options available: 56T, 40T, 32T, 24T, 16T, 4T, 2T, 1T, 512G, 3584T. Default setting is 32T.</li> </ul> </li> </ul>

Parameter	Description	
	•	MMIO High Granularity Size
		<ul> <li>Selects the allocation size used to assign mmioh resources.</li> </ul>
UPI General Configuration		- Options available: 1G, 4G, 16G, 64G, 256G, 1024G. Default setting is
(continued)		64G.
	•	Clock Modulation Enabled
		- Options available: Disable, Enable, Auto. Default setting is Auto.

# 5-3-4 Memory Configuration

Integrated Memory Controller (iMC)		Enforces Plan Of Record restrictions for DDR frequency programming.
Enforce DDR Memory Frequency POR Memory Frequency Enable ADR Legacy ADR Mode Minimum System Memory Size ADR Data Save Mode Assert ADR on Reset Assert ADR on S5	(POR) [Auto] [Enable] [Auto] [208] [NVDIMMS] [Disabled] [Disabled]	
Ager Memory Timing Memory Topology Memory Map Memory RAS Configuration	[BIOS Build-in]	<pre>++: Select Screen f4: Select Item K/M: Scroll Help Area Up/Doun. Enter: Select +/-: Change Opt. F1: General Help F3: Previous Values F3: Optimized Defaults F10: Save &amp; Exit ESC: Exit</pre>

Parameter	Description
Integrated Memory Controller (iMC)	
Enforce DDR Memory Frequency POR	When set to Enable, the system enforces Plan Of Record restrictions for DDR frequency programming. Options available: POR, Disable. Default setting is <b>POR</b> .
Memory Frequency	Configures the maximum memory frequency. If Enforce POR is disabled, user will be able to run at higher frequencies than the memory support (limited by processor support). Default setting is <b>Auto</b> .
Enable ADR	Enables the detecting and enabling of ADR (Asynchronous DRAM Refresh) function. Options available: Enable, Disable. Default setting is <b>Enable</b> .
Legacy ADR Mode	Enable/Disable the Legacy ADR Mode. Options available: Enable, Disable, Auto. Default setting is <b>Auto</b> .
Minimum System Memory Size	Configures the minimum memory size. Options available: 2GB, 4GB, 6GB, 8GB. Default setting is <b>2GB</b> .
ADR Data Save Mode	Specifies the Data Save Mode for ADR. Batterybacked or Type 01 NVDIMM. Options available: Disable, Batterybacked DIMMs, NVDIMMs, Copy to Flash. Default setting is <b>NVDIMMs</b> .
Assert ADR on Reset	Enable/Disable Assert ADR on Reset. Options available: Enabled, Disabled. Default setting is <b>Disabled</b> .

Parameter	Description	
Assert ADR on S5	Enable/Disable Assert ADR on S5. Options available: Enabled, Disabled. Default setting is <b>Disabled</b> .	
Get Memory Timing	Auto is the detected SPD value and use it, otherwise use BIOS Build-in. Options available: Auto, BIOS Build-in. Default setting is <b>BIOS Build-in</b> . Press [Enter] to view memory topology with DIMM population	
Memory Topology	Press [Enter] to view memory topology with DIMM population information.	
Memory Map <sup>(Note1)</sup>	<ul> <li>Press [Enter] to configure advanced items.</li> <li>Volatile Memory Mode <ul> <li>Selects 1LM or 2LM mode for volatile memory.</li> <li>Options available: 1LM, 2LM. Default setting is 2LM.</li> </ul> </li> </ul>	
Memory RAS Configuration	<ul> <li>Press [Enter] to configure advanced items.</li> <li>Mirror Mode<sup>(Note2)</sup> <ul> <li>Mirror Mode will set entire 1LM memory in system to be mirrored, consequently reducing the memory capacity by half. Enables the Mirror Mode will disable the XPT Prefetch.</li> <li>Options available: Disabled, Full Mirror Mode, Partial Mirror Mode. Default setting is <b>Disabled</b>.</li> </ul> </li> <li>Partial Mirror 1 Size (GB) <ul> <li>Selects multiplier of 1GB for the size of the SAD to be created.</li> </ul> </li> <li>Correctable Error Threshold <ul> <li>Correctable Error Threshold (0x01-0x7fff) used for sparing, and leaky bucket.</li> <li>Press the &lt;+&gt; / &lt;&gt;&gt; keys to increase or decrease the desired values.</li> </ul> </li> <li>Trigger SW Error Threshold<sup>(Note2)</sup> <ul> <li>Enable/Disable Sparing trigger SW Error Match Threshold.</li> <li>Options available: Disabled, Enabled. Default setting is <b>Disabled</b>.</li> </ul> </li> <li>SW Per Bank Threshold <ul> <li>SW Per Bank Threshold (1-0x7FFF) used for DDR bank level error.</li> <li>Press the &lt;+&gt; / &lt;&gt;&gt; keys to increase or decrease the desired values.</li> </ul> </li> <li>SW Correctable Error Time Window <ul> <li>SW Correctable Error Time Window</li> <li>SW Correctable Error Time window based interface in hour (0-24).</li> <li>Press the &lt;+&gt; / &lt;&gt;&gt; keys to increase or decrease the desired values.</li> </ul> </li> </ul>	

(Note1) Advanced items prompt when HBM CPU is installed.

(Note2) Advanced items prompt when this item is defined.

Parameter	Description
	Leaky bucket time window based interface Hour     Leaky bucket time window based interface hour used for DDR
	<ul> <li>(0-24).</li> <li>Press the &lt;+&gt; / &lt;-&gt; keys to increase or decrease the desired values.</li> </ul>
	<ul> <li>Leaky bucket time window based interface Minute</li> <li>Leaky bucket time window based interface minute used for DDR (0-60).</li> </ul>
	<ul> <li>Press the &lt;+&gt; / &lt;-&gt; keys to increase or decrease the desired values.</li> </ul>
	Leaky bucket low bit
	<ul> <li>Configures leaky bucket low bit (0x1 - 0x29).</li> </ul>
	<ul> <li>Press the &lt;+&gt; / &lt;-&gt; keys to increase or decrease the desired values.</li> </ul>
	Leaky bucket high bit
	<ul> <li>Configures leaky bucket high bit (0x1 - 0x29).</li> </ul>
Memory RAS Configuration (continued)	<ul> <li>Press the &lt;+&gt; / &lt;-&gt; keys to increase or decrease the desired values.</li> </ul>
	ADDDC Sparing <sup>(Note)</sup>
	<ul> <li>Enable/Disable ADDDC Sparing.</li> </ul>
	<ul> <li>Options available: Disabled, Enabled. Default setting is Disabled.</li> </ul>
	Enable ADDDC Error Injection
	<ul> <li>Options available: Disabled, Enabled. Default setting is Enabled.</li> </ul>
	Patrol Scrub
	<ul> <li>Options available: Disabled, Enable at End of POST. Default setting is Enable at End of POST.</li> </ul>
	Patrol Scrub Interval
	<ul> <li>Selects the number of hours (1-24) required to complete full scrub. A value of zero means auto.</li> </ul>
	DDR5 ECS
	<ul> <li>Options available: Disabled, Enabled, Enable ECS with Result Collection. Default setting is <b>Enabled</b>.</li> </ul>

### 5-3-5 IIO Configuration

IIO Configuration	Press <enter> to bring up the Intel Virtualization</enter>
Intel VT for Directed I/O (VT-d) Intel VMD technology	for Directed I/O (VT−d) Configuration menu.
	++: Select Screen †1: Select Item K/M: Scroll Help Area Up/Down. Enter: Select
	+/-: Change Opt. F1: General Help F3: Previous Values F9: Optimized Defaults F10: Save & Exit ESC: Exit

Parameter	Description
IIO Configuration	
Intel® VT for Directed I/O (VT-d)	<ul> <li>Press [Enter] to configure advanced items.</li> <li>Intel® VT for Directed I/O <ul> <li>Enable/Disable the Intel VT for Directed I/O (VT-d) support function by reporting the I/O device assignment to VMM through DMAR ACPI Tables.</li> <li>Options available: Enable, Disable. Default setting is Enable.</li> </ul> </li> <li>ACS Control <ul> <li>Enable: Programs ACS only to Chipset PCIe Root Ports Bridges.</li> <li>Default setting is Enable.</li> </ul> </li> <li>Cache Allocation <ul> <li>Options available: Enable, Disable. Default setting is Enable.</li> </ul> </li> <li>Opt-Out Illegal MSI Mitigation <ul> <li>Enable/Disable Opt-Out Illegal 0xFEE Platform Mitigation.</li> <li>Options available: Disable, Enable. Default setting is Disable.</li> </ul> </li> <li>DMA Control Opt-In Flag <ul> <li>Enable/Disable DMA_CTRL_PLATFORM_OPT_IN_FLAG in DMAR table in ACPI. Not compatible with Direct Device Assignment (DDA).</li> <li>Options available: Enable, Disable. Default setting is Disable.</li> </ul> </li> </ul>

Parameter	Description
	<ul> <li>Interrupt Remapping         <ul> <li>Enable/Disable the interrupt remapping support function.</li> <li>Options available: Auto, Enable, Disable. Default setting is Auto</li> </ul> </li> <li>x2APIC Opt Out         <ul> <li>Options available: Enable, Disable. Default setting is Disable.</li> </ul> </li> <li>Pre-boot DMA Protection         <ul> <li>Options available: Enable, Disable. Default setting is Disable.</li> </ul> </li> </ul>
Intel® VMD technology	Press [Enter] to configure advanced items.         Intel® VMD Configuration         Enable/Disable Intel® VMD technology.         Options available: Enable, Disable. Default setting is Disable.         Intel® VMD for Non-Hotplug NVMe <sup>(Note)</sup> Enable/Disable Intel® VMD for Non-Hotplug NVMe.         Options available: Enable, Disable. Default setting is Disable.

### 5-3-6 Advanced Power Management Configuration

Advanced Power Management Configuration	P State Control
CPU P State Control Hardware PM State Control CPU C State Control Package C State Control CPU - Advanced PM Tuning	Configuration Sub Menu, include Turbo, XE and etc.
	<pre>++: Select Screen 14: Select Item K/M: Scroll Help Area Uo/Down. Enter: Select +/-: Change Opt. F1: General Help F3: Previous Values F9: Optimized Defaults F10: Save &amp; Exit ESC: Exit</pre>

Parameter	Description
CPU P State Control	<ul> <li>Press [Enter] to configure advanced items.</li> <li>SpeedStep (Pstates) <ul> <li>Conventional Intel SpeedStep Technology switches both voltage and frequency in tandem between high and low levels in response to processor load.</li> <li>Options available: Enable, Disable. Default setting is Enable.</li> </ul> </li> <li>Turbo Mode <ul> <li>When this item is enabled, the processor will automatically ramp up the clock speed of 1-2 of its processing cores to improve its performance. When this item is disabled, the processor will not overclock any of its core.</li> <li>Options available: Enable, Disable. Default setting is Enable.</li> </ul> </li> </ul>
Hardware PM State Control	<ul> <li>Press [Enter] to configure advanced items.</li> <li>Hardware P-States <ul> <li>When this item is disabled, the processor hardware chooses a P-state based on OS Request (Legacy P-States).</li> <li>In Native mode, the processor hardware chooses a P-state based on OS guidance.</li> <li>In Out of Band mode, the processor hardware autonomously chooses a P-state (with no OS guidance).</li> <li>Options available: Disable, Native Mode, Out of Band Mode, Native Mode with No Legacy Support. Default setting is Native Mode.</li> </ul> </li> </ul>

Parameter	Description
CPU C State Control	<ul> <li>Press [Enter] to configure advanced items.</li> <li>Enable Monitor MWAIT <ul> <li>Allows Monitor and MWAIT instructions.</li> <li>Options available: Disable, Enable, Auto. Default setting is Auto.</li> </ul> </li> <li>CPU C6 Report <ul> <li>Enable/Disable CPU C6(ACPI C3) report to OS.</li> <li>Options available: Disable, Enable, Auto. Default setting is Auto.</li> </ul> </li> <li>Enhanced Halt State (C1E) <ul> <li>Core C1E auto promotion control. Takes effect after reboot.</li> <li>Options available: Enable, Disable. Default setting is Enable.</li> </ul> </li> </ul>
Package C State Control	<ul> <li>Press [Enter] to configure advanced items.</li> <li>Package C State <ul> <li>Configures the state for the C-State package limit.</li> <li>Options available: C0/C1 state, C2 state, C6(non Retention) state, C6(Retention) state, No Limit, Auto. Default setting is Auto.</li> </ul> </li> </ul>
CPU - Advanced PM Tuning	<ul> <li>Press [Enter] to configure advanced items.</li> <li>Energy Perf BIAS <ul> <li>Press [Enter] to configure advanced items.</li> <li>Power Performance Tuning</li> <li>Options available: OS Controls EPB, BIOS Controls EPB, PECI Controls EPB. Default setting is OS Controls EPB.</li> <li>Energy_PERF_BIAS_CFG mode<sup>[Note]</sup></li> <li>Options available: Performance, Balanced Performance, Balanced Power, Power. Default setting is Balanced Performance.</li> </ul> </li> </ul>

### 5-3-7 PCH Configuration

Aptio Setup – A <mark>Chipset</mark>	IMI
PCH-IO Configuration	Device Options Settings
	++: Select Screen 14: Select Item K/M: Scroll Help Area Up/Down. Enter: Select +/-: Change Opt. F1: General Help F3: Previous Values F9: Optimized Defaults F10: Save & Exit ESC: Exit
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Parameter	Description
PCH-IO Configuration	
SATA And RST Configuration/ SATA Controller And RST Configuration	<ul> <li>Press [Enter] to configure advanced items.</li> <li>SATA Configuration <ul> <li>Enable/Disable SATA controller.</li> <li>Options available: Enabled, Disabled. Default setting is Enabled.</li> </ul> </li> <li>SATA Mode Selection <ul> <li>Configures on chip SATA type.</li> <li>AHCI Mode: When set to AHCI, the SATA controller enables its AHCI functionality. Then the RAID function is disabled and cannot be access the RAID setup utility at boot time.</li> <li>RAID Mode: When set to RAID, the SATA controller enables both its RAID and AHCI functions. You will be allowed to access the RAID setup utility at boot time.</li> <li>Options available: AHCI, RAID. Default setting is AHCI.</li> </ul> </li> <li>RAID Device ID<sup>(Note)</sup> <ul> <li>Choose RAID Device ID.</li> <li>Options available: Client, Alternate, Server. Default setting is Server.</li> </ul> </li> <li>SATA Port 0/1/2/3/4/5/6/7 <ul> <li>The category identifies SATA hard drives that are installed in the computer. System will automatically detect HDD type.</li> </ul> </li> </ul>

Parameter	Description
SATA And RST Configuration/ SATA Controller And RST Configuration (continued)	<ul> <li>Port 0/1/2/3/4/5/6/7 <ul> <li>Enable/Disable Port 0/1/2/3/4/5/6/7 device.</li> <li>Options available: Enabled, Disabled. Default setting is Enabled.</li> </ul> </li> <li>Hot Plug (for Port 0/1/2/3/4/5/6/7) <ul> <li>Enable/Disable HDD Hot-Plug function.</li> <li>Options available: Enabled, Disabled. Default setting is Enabled.</li> </ul> </li> <li>Spin Up Device (for Port 0/1/2/3/4/5/6/7) <ul> <li>On an edge detect from 0 to 1, the PCH starts a COM reset initialization to the device.</li> <li>Options available: Enabled, Disabled. Default setting is Disabled.</li> </ul> </li> </ul>
SATA And RST Configuration/ sSATA Controller And RST Configuration	<ul> <li>SATA Configuration         <ul> <li>Enable/Disable SATA controller.</li> <li>Options available: Enabled, Disabled. Default setting is Enabled.</li> </ul> </li> <li>SATA Mode Selection         <ul> <li>Configures on chip SATA type.</li> <li>AHCI Mode: When set to AHCI, the SATA controller enables its AHCI functionality. Then the RAID function is disabled and cannot be access the RAID setup utility at boot time.</li> <li>RAID Mode: When set to RAID, the SATA controller enables both its RAID and AHCI functions. You will be allowed to access the RAID setup utility at boot time.</li> <li>Options available: AHCI, RAID. Default setting is AHCI.</li> </ul> </li> <li>RAID Device ID<sup>(Note)</sup> <ul> <li>Choose RAID Device ID.</li> <li>Options available: Client, Alternate, Server. Default setting is Server.</li> </ul> </li> <li>SATA Port 4/5/6/7         <ul> <li>The category identifies sSATA hard drives that are installed in the computer. System will automatically detect HDD type.</li> </ul> </li> <li>SATA Port 4/5/6/7         <ul> <li>Enable/Disable Port 4/5/6/7 device.</li> <li>Options available: Enabled, Disabled. Default setting is Enabled.</li> </ul> </li> <li>Hot Plug (for Port 4/5/6/7)         <ul> <li>Enable/Disable HDD Hot-Plug function.</li> <li>Options available: Enabled, Disabled. Default setting is Enabled.</li> </ul> </li> <li>Spin Up Device (for Port 4/5/6/7)         <ul> <li>On an edge detect from 0 to 1, the PCH starts a COM reset initialization to the device.</li> <li>Options available: Enabled, Disabled. Default setting is Disabled.</li> </ul> </li> </ul>

### 5-3-8 Miscellaneous Configuration

Miscellaneous Configuration		Select active Video type
Active Video External SSC – CK440	(Auto) [SSC Dff]	
		<pre>**: Select Screen f1: Select Item K/M: Scroll Help Area Up/Doun. Enter: Select +/-: Change Opt. F1: General Help F3: Previous Values F9: Optimized Defaults F10: Save &amp; Exit ESC: Exit</pre>

Parameter	Description
Miscellaneous Configuration	
	Selects the active video type.
Active Video	Options available: Auto, Onboard Device, PCIE Device, Specific PCIE
	Device. Default setting is Auto.
	Enables Spread spectrum - only affects external clock generator.
External SSC - CK440	Options available: SSC Off, SSC = -0.3%, SSC = -0.5%, Hardware.
	Default setting is SSC Off.

### 5-3-9 Server ME Configuration

Chipset		
General ME Configuration Oper, Firmware Version ME Firmware Status #1 ME Firmware Status #2 Current State Error Code Recovery Cause	18:6.0.3.256 0x00000355 0x88500006 Operational No Error N/A	
		<ul> <li>★: Select Screen</li> <li>11: Select Item</li> <li>K/M: Scroll Help Area</li> <li>Up/Doun.</li> <li>Enter: Select</li> <li>+/-: Change Opt.</li> <li>F1: General Help</li> <li>F3: Previous Values</li> <li>F9: Optimized Defaults</li> <li>F10: Save &amp; Exit</li> <li>ESC: Exit</li> </ul>

Parameter	Description
General ME Configuration	
Oper. Firmware Version	Displays the operational firmware version.
ME Firmware Status #1/#2	Displays ME Firmware status information.
Current State	Displays ME Firmware current status information.
Error Code	Displays ME Firmware status error code.
Recovery Cause	Displays ME Firmware recovery cause.

### 5-3-10 Runtime Error Logging Settings

Chipset		
Runtime Error Logging 	(Enable) (Disable)	System Error Enable/Disable setup options.
Whea Settings Memory Error Enabling PCIe Error Enabling		
		<pre>++: Select Screen 11: Select Item K/N: Scroll Help Area Up/Down. Enter: Select +/-: Change Opt. F1: General Help</pre>
		F3: Previous Values F9: Optimized Defaults F10: Save & Exit ESC: Exit

Parameter	Description
Runtime Error Logging	
Curtan Error	Enable/Disable system error logging function.
System Errors	Options available: Enable, Disable. Default setting is Enable.
CAN/ Error Injection Cunnert	Enable/Disable software injection error logging function.
S/W Error Injection Support	Options available: Enable, Disable. Default setting is <b>Disable</b> .
	Press [Enter] to configure advanced items.
Whea Settings	WHEA (Windows Hardware Error Architecture) Support
whea Settings	<ul> <li>Enable/Disable WHEA Support.</li> </ul>
	<ul> <li>Options available: Enable, Disable. Default setting is Enable.</li> </ul>
	Press [Enter] to configure advanced items.
	Memory Corrected Error
	<ul> <li>Enable/Disable Memory Corrected Error.</li> </ul>
Memory Error Enabling	<ul> <li>Options available: Enable, Disable. Default setting is Enable.</li> </ul>
	Uncorrected Error disable Memory
	<ul> <li>Enable/Disable the Memory that triggers Uncorrected Error.</li> </ul>
	<ul> <li>Options available: Enable, Disable. Default setting is <b>Disable</b>.</li> </ul>

Parameter	Description
PCle Error Enabling	<ul> <li>Press [Enter] to configure advanced items.</li> <li>PCIE Error <ul> <li>Enable/Disable PCIE error.</li> <li>Options available: Enable, Disable. Default setting is <b>Disable</b>.</li> </ul> </li> <li>Uncorrected Error<sup>(Note)</sup> <ul> <li>Enables and escalates Uncorrectable/Recoverable Errors to error pins.</li> <li>Options available: Enable, Disable. Default setting is <b>Enable</b>.</li> </ul> </li> <li>Fatal Error Enable<sup>(Note)</sup> <ul> <li>Enables and escalates Fatal Errors to error pins.</li> <li>Options available: Enable, Disable. Default setting is <b>Enable</b>.</li> </ul> </li> <li>Fatal Error Enable<sup>(Note)</sup> <ul> <li>Enables and escalates Fatal Errors to error pins.</li> <li>Options available: Enable, Disable. Default setting is <b>Enable</b>.</li> </ul> </li> <li>Assert NMI on SERR<sup>(Note)</sup> <ul> <li>Enable/Disable BIOS generates a non-maskable interrupt (NMI) and logs an error when a system error (SERR) occurs.</li> <li>Options available: Enabled, Disabled. Default setting is <b>Enabled</b>.</li> </ul> </li> <li>Assert NMI on PERR<sup>(Note)</sup> <ul> <li>Enable/Disable BIOS generates a non-maskable interrupt (NMI) and logs an error when a system error (SERR) occurs.</li> <li>Options available: Enabled, Disabled. Default setting is <b>Enabled</b>.</li> </ul> </li> <li>Assert NMI on PERR<sup>(Note)</sup> <ul> <li>Enable/Disable BIOS generates a non-maskable interrupt (NMI) and logs an error when a processor bus parity error (PERR) occurs.</li> <li>Options available Enabled, Disabled. Default setting is <b>Enabled</b>.</li> </ul> </li> </ul>

### 5-3-11 Power Policy

Chipset	Aptio Setup – AMI	
Power Policy Quick Settings SpeedStep (Pstates) Turbo Mode CPU C6 report Enhanced Halt State (C1E) Package C State Enable LP [0lobal] Handware Prefetcher Adjacent Cache Prefetch DCU Streamer Prefetcher Intel VT for Directed I/0	[Standard] [Enable] [Auto] [Auto] [Auto] [Auto] [ALL LPs] [Enable] [Enable] [Enable] [Enable] [Enable]	Select a Power Policy Quick Setting(The following items will be set based on the selected power policy)
		++: Select Screen 1: Select Item K/M: Scroll Help Area Up/Doun. Enter: Select +/-: Change Opt. F1: General Help F3: Previous Values F9: Optimized Defaults F10: Save & Exit ESC: Exit

Parameter	Description
	Selects a Power Policy Quick Setting.
Power Policy Quick Settings	Options available: Standard, Best Performance, Energy Efficient. Default
	setting is Standard.
	Conventional Intel SpeedStep Technology switches both voltage and
SpeedStep (Pstates)	frequency in tandem between high and low levels in response to processor
SpeedStep (1 states)	load.
	Options available: Enable, Disable. Default setting is <b>Enable</b> .
	When this item is enabled, the processor will automatically ramp up the
Turbo Mode	clock speed of 1-2 of its processing cores to improve its performance.
TUIDO MODE	When this item is disabled, the processor will not overclock any of its core.
	Options available: Enable, Disable. Default setting is Enable.
	Enable/Disable the BIOS to enable the report from the CPU C6 state (ACPI
CPU C6 report	C3) to the OS.
	Options available: Disable, Enable, Auto. Default setting is Auto.
	Enable/Disable the C1E support for lower power consumption. Takes effect
Enhanced Halt State (C1E)	after reboot.
	Options available: Enable, Disable. Default setting is <b>Enable</b> .
	Configures the C-State package limit.
Package C State	Options available: C0/C1 state, C2 state, C6(non Retention) state,
	C6(Retention) state, No Limit, Auto. Default setting is Auto.

Parameter	Description
	Enables Logical processor (Software Method to Enable/Disable Logical
Enable LP [Global]	Processor threads).
	Options available: ALL LPs, Single LP. Default setting is ALL LPs.
Hardware Prefetcher	Options available: Enable, Disable. Default setting is <b>Enable</b> .
Adjacent Cache Prefetch	Options available: Enable, Disable. Default setting is <b>Enable</b> .
DCU Streamer Prefetcher	Options available: Enable, Disable. Default setting is <b>Enable</b> .
Intel® VT for Directed I/O	Enable/Disable the Intel VT for Directed I/O (VT-d) support function by reporting the I/O device assignment to VMM through DMAR ACPI Tables. Options available: Enable, Disable. Default setting is <b>Enable</b> .

### 5-4 Server Management Menu

Main Advanced Chipset Server Mg	Aptio Setup – AMI mt Security Boot Save & Exit	
FR8-2 Timer FR8-2 Timer fimeout FR8-2 Timer Policy OS Watchdog Timer OS Witd Timer Timeout OS Witd Timer Policy Wait BMC Ready System Event Log > View FRU information > BMC VLAN Configuration	[Enabled] 6 [Do Nothing] [Disabled] 10 [Reset] [2 minutes]	Enable or Disable FRB-2 timer(PDST timer)
<ul> <li>BHC network configuration</li> <li>IPv6 BHC Network Configuration</li> </ul>		<pre>++: Select Screen 11: Select Item K/M: Scroll Help Area Up/Down. Enter: Select +/-: Change Opt. F1: General Help F3: Previous Values F9: Optimized Defaults F10: Save &amp; Exit ESC: Exit</pre>
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Parameter	Description
FRB-2 Timer	Enable/Disable FRB-2 timer (POST timer). Options available: Enabled, Disabled. Default setting is <b>Enabled</b> .
FRB-2 Timer <sup>(Note1)</sup> timeout	Configures the FRB2 Timer timeout. The value is between 1 to 30 minutes. Default setting is <b>6 minutes</b> .
FRB-2 Timer Policy <sup>(Note1)</sup>	Configures the FRB2 Timer policy. Options available: Do Nothing, Reset, Power Down, Power Cycle. Default setting is <b>Do Nothing</b> .
OS Watchdog Timer	Enable/Disable OS Watchdog Timer function. Options available: Enabled, Disabled. Default setting is <b>Disabled</b> .
OS Wtd Timer Timeout <sup>(Note2)</sup>	Configures OS Watchdog Timer. The value is between 1 to 30 minutes. Default setting is <b>10 minutes</b> .
OS Wtd Timer Policy <sup>(Note2)</sup>	Configure OS Watchdog Timer Policy. Options available: Reset, Do Nothing, Power Down, Power Cycle. Default setting is <b>Reset</b> .
Wait BMC Ready	POST wait BMC ready and reboot system. Options available: Disabled, 2 minutes, 4 minutes, 6 minutes. Default setting is <b>2 minutes</b> .

(Note1) This item is configurable when FRB-2 Timer is set to Enabled.

(Note2) This item is configurable when OS Watchdog Timer is set to Enabled.

Parameter	Description
System Event Log	Press [Enter] to configure advanced items.
View FRU Information	Press [Enter] to view the FRU information.
BMC VLAN Configuration	Press [Enter] to configure advanced items.
BMC network Configuration	Press [Enter] to configure advanced items.
IPv6 BMC Network Configuration	Press [Enter] to configure advanced items.

## 5-4-1 System Event Log

Enabling/Disabling Options		Change this to enable or
		disable event logging for
Erasing Settings		error/progress codes during boot.
Erase SEL	[No]	during boot.
When SEL is Full	[Do Nothing]	
Custom EFI Logging Options		
Log EFI Status Codes	[Ennon code]	
		++: Select Screen
effect until computer is	restarteu.	
		↔: Select Screen 1↓: Select Item
		K/M: Scroll Help Area
		Up/Down.
		Enter: Select
		+/-: Change Opt.
		+/-: Change Opt. F1: General Help
		+/-: Change Opt. F1: General Help F3: Previous Values
		+/-: Change Opt. F1: General Help F3: Previous Values F9: Optimized Defaults
		+/-: Change Opt. F1: General Help F3: Previous Values

Parameter	Description
Enabling / Disabling Options	
SEL Components	Change this item to enable or disable all features of System Event Logging during boot. Options available: Enabled, Disabled. Default setting is <b>Enabled</b> .
Erasing Settings	
Erase SEL	Choose options for erasing SEL. Options available: No, Yes, On next reset, Yes, On every reset. Default setting is <b>No</b> .
When SEL is Full	Choose options for reactions to a full SEL. Options available: Do Nothing, Erase Immediately, Delete Oldest Record. Default setting is <b>Do Nothing</b> .
Custom EFI Logging Options	
Log EFI Status Codes	Enable/Disable the logging of EFI Status Codes (if not already converted to legacy). Options available: Disabled, Both, Error code, Progress code. Default setting is <b>Error code</b> .

#### 5-4-2 View FRU Information

The FRU page is a simple display page for basic system ID information, as well as System product information. Items on this window are non-configurable.

FRU Information		
System Manufacturer System Product Name System Version Board Manufacturer Board Product Name Board Product Name Board Part Number Chassis Manufacturer Chassis Part Number Chassis Serial Number	GIGABYTE R163-S32-AAC1-000 01234567890123456789AB GIGABYTE MS33-DCO-000 123456789AB 0123456789AB 01234567890123456789AB GIGABYTE 01234567 01234567890123456789AB	+: Select Screen 11: Select Item K/M: Scroll Help Area Up/Down. Enter: Select +/-: Change Opt. F1: General Help F3: Previous Values F9: Optimized Defaults F10: Save & Exit ESC: Exit

### 5-4-3 BMC VLAN Configuration

BMC VLAN Configuration		VLAN ID of new VLAN or existing VLAN, valid valu
BMC VLAN ID BMC VLAN Priority	0	IS 0~4094, 0 is disable VLAN
		<pre>++: Select Screen 14: Select Item K/M: Scroll Help Area Up/Doun. Enter: Select +/-: Change Opt. F1: General Help F3: Previous Values</pre>
		F9: Optimized Defaults F10: Save & Exit ESC: Exit

Parameter	Description
BMC VLAN Configuration	
BMC VLAN ID	Select to configure BMC VLAN ID. The valid range is from 0 to 4094. When set to 0, BMC VLAN ID will be disabled.
BMC VLAN Priority	Select to configure BMC VLAN Priority. The valid range is from 0 to 7. When BMC VLAN ID is set to 0, BMC VLAN Priority will not be selected.

### 5-4-4 BMC Network Configuration

BMC network configuration	Select to configure LAN
Select NCSI and Dedicated LAN Lan channel 1 Configuration Address source Station IP address Subnet mask Router IP address Station MAC address	channel parameters statically or dynamically(DHCP). Do nothing option will not modify any BMC network parameters during BIOS phase
Real-time get BMC network address	++: Select Screen 11: Select Item K/M: Scroll Help Area Up/Doun. Enter: Select +/-: Change Opt. F1: General Help F3: Previous Values F9: Optimized Defaults F10: Save & Exit ESC: Exit

Parameter	Description
BMC network configuration	
Select NCSI and Dedicated LAN	Options available: Do Nothing, Model1(Dedicated), Model2(NCSI), Mode3(Failover). Default setting is <b>Do Nothing</b> .
Lan Channel 1	
Configuration Address source	Selects to configure LAN channel parameters statically or dynamically (DHCP). Options available: Unspecified, Static, DynamicBmcDhcp. Default setting is <b>DynamicBmcDhcp</b> .
Station IP address	Displays IP Address information.
Subnet mask	Displays Subnet Mask information. Please note that the IP address must be in three digitals, for example, 192.168.000.001.
Router IP address	Displays the Router IP Address information.
Station MAC address	Displays the MAC Address information.
Real-time get BMC network address	Press [Enter] will set LAN mode and Address source and then get IP, Subnet, Gateway and MAC address.

#### 5-4-5 IPv6 BMC Network Configuration



Parameter	Description
IPv6 BMC network configuration	
IPv6 BMC Lan Channel 1	
IPv6 BMC Lan Option	Enable/Disable IPv6 BMC LAN channel function. When this item is disabled, the system will not modify any BMC network during BIOS phase. Options available: Unspecified, Disable, Enable. Default setting is Enable.
IPv6 BMC Lan IP Address Source	Selects to configure LAN channel parameters statically or dynamically (by BIOS or BMC). Options available: Unspecified, Static, Dynamic-Obtained by BMC running DHCP. Default setting is <b>Dynamic-Obtained by BMC running DHCP</b> .
IPv6 BMC Lan IP Address/ Prefix Length	Check if the IPv6 BMC LAN IP address matches those displayed on the screen.

## 5-5 Security Menu

The Security menu allows you to safeguard and protect the system from unauthorized use by setting up access passwords.

Main Advanced Chipset Server Mg	Aptio Setup – AMI mt Security Boot Save & Exit	
Password Description		Secure Boot configuration
If ONLY the Administrator's passwor then this only limits access to Set only asked for when entering Setup. If ONLY the User's password is set.		
is a power on password and must be entered to boot or enter Setup. In Setup the User will have Administrator rights. The password length must be		
in the following range:		
Minimum length Maximum length	3 20	↔: Select Screen ↑↓: Select Item
Administrator Password User Password		K/M: Scroll Help Area Up/Down. Enter: Select +/-: Change Opt. F1: General Help
▶ Secure Boot		F3: Previous Values F9: Optimized Defaults F10: Save & Exit ESC: Exit
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There are two types of passwords that you can set:

Administrator Password

Entering this password will allow the user to access and change all settings in the Setup Utility.

User Password

Entering this password will restrict a user's access to the Setup menus. To enable or disable this field, a Administrator Password must first be set. A user can only access and modify the System Time, System Date, and Set User Password fields.

Parameter	Description
Administrator Password	Press [Enter] to configure the administrator password.
User Password	Press [Enter] to configure the user password.
Secure Boot	Press [Enter] to configure advanced items.

#### 5-5-1 Secure Boot

The Secure Boot feature is applicable if supported by your Operating System. If your Operating System is not supporting Secure Boot, the system will hang when starting the Operating System.

System Mode	Setup	Secure Boot feature is Active if Secure Boot is
		Enabled,
	Not Active	Platform Key(PK) is enrolled and the System i
Secure Boot Mode	[Custom]	in User mode.
Restore Factory Keys		The mode change requires
Reset To Setup Mode		platform reset
Key Management		
		++: Select Screen
		14: Select Item
		K/M: Scroll Help Area
		Up/Down.
		Enter: Select
		+/-: Change Opt.
		F1: General Help F3: Previous Values
		F9: Optimized Defaults
		F10: Save & Exit
		ESC: Exit

Parameter	Description
System Mode	Displays if the system is in User mode or Setup mode.
Secure Boot	Enable/ Disable the Secure Boot function. Options available: Enabled, Disabled. Default setting is <b>Disabled</b> .
Secure Boot Mode <sup>(Note)</sup>	Secure Boot requires all the applications that are running during the booting process to be pre-signed with valid digital certificates. This way, the system knows all files being loaded before the Operating System loads to the login screen have not been tampered with. When set to Standard, it will automatically load the Secure Boot keys form the BIOS databases. When set to Custom, you can customize the Secure Boot settings and manually load its keys from the BIOS database. Options available: Standard, Custom. Default setting is <b>Custom</b> .
Restore Factory Keys	Forces the system to user mode and installs factory default Secure Boot key database.
Reset To Setup Mode	Reset the system to Setup Mode.

(Note) Advanced items prompt when this item is set to Custom.

Parameter	Description
Key Management	<ul> <li>Press [Enter] to configure advanced items.</li> <li>Please note that this item is configurable when Secure Boot Mode is set to Custom. <ul> <li>Factory Key Provision</li> <li>Allows to provision factory default Secure Boot keys when system is in Setup Mode.</li> <li>Options available: Enabled, Disabled. Default setting is Disabled.</li> </ul> </li> <li>Restore Factory Keys <ul> <li>Installs all factory default keys. It will force the system in User Mode.</li> <li>Options available: Yes, No.</li> </ul> </li> <li>Reset To Setup Mode <ul> <li>Reset To Setup Mode.</li> <li>Options available: Yes, No.</li> </ul> </li> <li>Reset the system to Setup Mode. <ul> <li>Options available: Yes, No.</li> </ul> </li> <li>Enroll Efi Image <ul> <li>Press [Enter] to enroll SHA256 hash of the binary into Authorized Signature Database (db).</li> </ul> </li> <li>Export Secure Boot variables <ul> <li>Copy NVRAM content of Secure Boot variables to files in a root folder on a file system device.</li> </ul> </li> <li>Secure Boot variable <ul> <li>Displays the current status of the variables used for secure boot.</li> </ul> </li> <li>Platform Key (PK) <ul> <li>Displays the current status of the Platform Key (PK).</li> <li>Press [Enter] to configure a new PK.</li> <li>Options available: Update.</li> </ul> </li> <li>Key Exchange Keys (KEK) <ul> <li>Displays the current status of the Key Exchange Key Database (KEK).</li> <li>Press [Enter] to configure a new KEK or load additional KEK from storage devices.</li> <li>Options available: Update, Append.</li> </ul> </li> <li>Authorized Signatures (DB) <ul> <li>Displays the current status of the Authorized Signature Database.</li> <li>Press [Enter] to configure a new DB or load additional DB from storage devices.</li> <li>Options available: Update, Append.</li> </ul> </li> <li>Forbidden Signatures (DBX) <ul> <li>Displays the current status of the Forbidden Signature Database.</li> <li>Press [Enter] to configure a new bB or load additional DB from storage devices.</li> <li>Options availabl</li></ul></li></ul>

Parameter	Description	
Key Management (continued)	<ul> <li>Authorized TimeStamps (DBT)         <ul> <li>Displays the current status of the Authorized TimeStamps Database.</li> <li>Press [Enter] to configure a new DBT or load additional DBT from storage devices.</li> <li>Options available: Update, Append.</li> </ul> </li> <li>OsRecovery Signatures         <ul> <li>Displays the current status of the OsRecovery Signature Database.</li> <li>Press [Enter] to configure a new OsRecovery Signature or load additional OsRecovery Signature from storage devices.</li> <li>Options available: Update, Append.</li> </ul> </li> </ul>	

## 5-6 Boot Menu

The Boot menu allows you to set the drive priority during system boot-up. BIOS setup will display an error message if the legacy drive(s) specified is not bootable.

Aptio Setup – AMI Main Advanced Chipset Server Mgmt Security <mark>Boot</mark> Save & Exit		
Boot Configuration Setup Prompt Timeout		Set the default timeout before system boot. A
Bootup NumLock State	I [0n]	value of 65535 will
Quiet Boot	[Enabled]	disable the timeout
		completely.
Endless Retry Boot	[Disable]	
2		
Setup Flash		
Dump full Setup Data Dump non-default Setup Data		
Restore Setup Data		
FIXED BOOT ORDER Priorities	and the second sec	↔+: Select Screen
Boot Option #1	[USB Device]	↑↓: Select Item
Boot Option #2	[CD/DVD]	K/M: Scroll Help Area
Boot Option #3	[Network:UEFI: PXE IPv4 Intel(R) I210 Gigabit	Up/Down. Enter: Select
	Network Connection	+/-: Change Opt.
	D8:5E:D3:4F:CB:2E1	F1: General Help
Boot Option #4	[UEFI AP:UEFI: Built-in	F3: Previous Values
Manageri (Manageri) 1914	EFI Shell]	F9: Optimized Defaults
Boot Option #5	[Hard Disk]	F10: Save & Exit
		ESC: Exit
UEFI NETWORK Drive BBS Priorities		

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Parameter	Description
Boot Configuration	
Setup Prompt Timeout	Number of seconds to wait for setup activation key. 65535 (0xFFFF) means indefinite waiting. Press the numeric keys to input the desired values.
Bootup NumLock State	Enable/Disable the Bootup NumLock function. Options available: On, Off. Default setting is <b>On</b> .
Quiet Boot	Enable/Disable showing the logo during POST. Options available: Enabled, Disabled. Default setting is <b>Enabled</b> .
Endless Retry Boot	Options available: Disable, Enable. Default setting is <b>Disable</b> .
Setup Flash	Press [Enter] to run setup flash.
Dump full Setup Data	Press [Enter] to dump full setup data to file.
Dump non-default Setup Data	Press [Enter] to dump non-default setup data to file.
Restore Setup Data	Press [Enter] to restore setup data from file.

Parameter	Description	
FIXED BOOT ORDER Priorities		
Boot Option #1 / #2 / #3 / #4 / #5	Press [Enter] to configure the boot order priority. By default, the server searches for boot devices in the following sequence: 1. Hard drive. 2. CD-COM/DVD drive. 3. USB device. 4. Network. 5. UEFI.	
UEFI Network Drive BBS Priorities	Press [Enter] to configure the boot priority.	
UEFI Application Boot Priorities	Press [Enter] to configure the boot priority.	

## 5-7 Save & Exit Menu

The Save & Exit menu displays the various options to quit from the BIOS setup. Highlight any of the exit options then press <Enter>.

Aptio Setup – AMI Main Advanced Chipset Server Mgmt Security Boot <mark>Save &amp; E</mark>	xit
Save Options Save & Exit Discard changes & exit Save Changes and Reset Discard Changes and Reset Save Changes Discard Changes	Exit system setup after saving the changes.
Default Options Restore Defaults Save the User Default Values Restore the User Default Values Boot Device Priority UEFI: PXE IPv4 Intel(R) I210 Gigabit Network Connection D8:5E:03:4F:C8:2E UEFI: Built-in EFI Shell Launch EFI Shell	++: Select Screen 14: Select Item K/M: Scroll Help Area Up/Down. Enter: Select +/-: Change Opt. F1: General Help F3: Previous Values F9: Optimized Defaults F10: Save & Exit ESC: Exit

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Parameter	Description
Save Options	
Save and Exit	Saves changes made and closes the BIOS setup. Options available: Yes, No.
Discard changes and exit	Discards changes made and exits the BIOS setup. Options available: Yes, No.
Save Changes and Reset	Restarts the system after saving the changes made. Options available: Yes, No.
Discard Changes and Reset	Restarts the system without saving any changes. Options available: Yes, No.
Save Changes	Saves changes done so far to any of the setup options. Options available: Yes, No.
Discard Changes	Discards changes made and closes the BIOS setup. Options available: Yes, No.
Default Options	

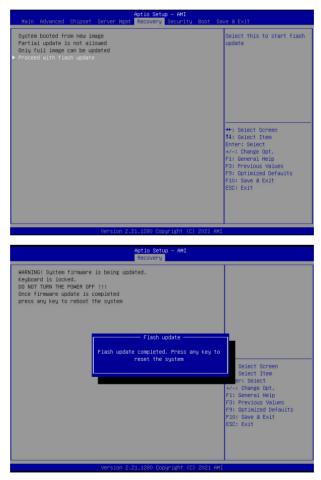
Parameter	Description
Restore Defaults	Loads the default settings for all BIOS setup parameters. Setup Defaults are quite demanding in terms of resources consumption. If you are using low-speed memory chips or other kinds of low-performance components and you choose to load these settings, the system might not function properly. Options available: Yes, No.
Save the User Default Values	Saves the changes made as the user default settings. Options available: Yes, No.
Restore the User Default Values	Loads the user default settings for all BIOS setup parameters. Options available: Yes, No.
Boot Device Priority	Press [Enter] to configure the device as the boot-up drive.
Launch EFI Shell	Attempts to Launch EFI Shell application (Shell.efi) from one of the available file system devices.

## 5-8 BIOS Recovery

The system has an embedded recovery technique. In the event that the BIOS becomes corrupt the boot block can be used to restore the BIOS to a working state. To restore your BIOS, please follow the instructions listed below:

Recovery Instruction:

- 1. Copy the XXX.rom to USB diskette.
- 2. Setting BIOS Recovery jump to enabled status.
- 3. Boot into BIOS recovery.
- 4. Run Proceed with flash update.
- 5. BIOS updated.



## 5-9 BIOS POST Beep code (AMI standard)

#### 5-9-1 PEI Beep Codes

# of Beeps	Description
1	Memory not Installed.
1	Memory was installed twice (InstallPeiMemory routine in PEI Core called twice)
2	Recovery started
3	DXEIPL was not found
3	DXE Core Firmware Volume was not found
4	Recovery failed
4	S3 Resume failed
7	Reset PPI is not available

#### 5-9-2 DXE Beep Codes

# of Beeps	Description
1	Invalid password
4	Some of the Architectural Protocols are not available
5	No Console Output Devices are found
5	No Console Input Devices are found
6	Flash update is failed
7	Reset protocol is not available
8	Platform PCI resource requirements cannot be met