

GIGABYTE™

MS74-HB0

Motherboard - Intel® Xeon® 6 Processors - E-ATX DP

User Manual

Rev. 1.0

Copyright

© 2024 Giga Computing TECHNOLOGY CO., LTD. All rights reserved.

The trademarks mentioned in this manual are legally registered to their respective owners.

Disclaimer

Information in this manual is protected by copyright laws and is the property of GIGABYTE.

Changes to the specifications and features in this manual may be made by Giga Computing without prior notice. No part of this manual may be reproduced, copied, translated, transmitted, or published in any form or by any means without Giga Computing's prior written permission.

Documentation Classifications

In order to assist in the use of this product, Giga Computing provides the following types of documentation:

- User Manual: detailed information & steps about the installation, configuration and use this product (e.g. motherboard, server barebones), covering hardware and BIOS.
- User Guide: detailed information about the installation & use of an add-on hardware or software component (e.g. BMC firmware, rail-kit) compatible with this product.
- Quick Installation Guide: a short guide with visual diagrams that you can reference easily for installation purposes of this product (e.g. motherboard, server barebones).

Please see the support section of the online product page to check the current availability of these documents

For More Information

For related product specifications, the latest firmware and software, and other information, please visit our website at: <http://www.gigabyte.com/enterprise>.

For GIGABYTE distributors and resellers, additional sales & marketing materials are available from our reseller portal: <http://reseller.b2b.gigabyte.com>

For further technical assistance, please contact your GIGABYTE representative or visit <http://esupport.gigabyte.com/> to create a new support ticket.

For any general sales or marketing enquires, you may message GIGABYTE server directly by email: server.grp@gigabyte.com.

⚠ WARNING

- **INGESTION HAZARD:** This product contains a button cell or coin battery.
- **DEATH** or serious injury can occur if ingested.
- A swallowed button cell or coin battery can cause **Internal Chemical Burns** in as little as **2 hours**.
- **KEEP** new and used batteries **OUT OF REACH of CHILDREN**
- **Seek immediate medical attention** if a battery is suspected to be swallowed or inserted inside any part of the body.



- Battery type: CR2032, voltage rating: +3VDC.
- Non-rechargeable batteries are not to be recharged.
- Remove and immediately recycle or dispose of used batteries, batteries from equipment not used for an extended period of time according to local regulations and keep away from children. Do NOT dispose of batteries in household trash or incinerate.
- Even used batteries may cause severe injury or death.
- Do not force discharge, recharge, disassemble, heat above (manufacturer's specified temperature rating) or incinerate. Doing so may result in injury due to venting, leakage or explosion resulting in chemical burns.
- For treatment information, call a local poison control center.
- The product contains non-replaceable batteries.

Table of Contents

Block Diagram	6
Chapter 1 Hardware Installation	7
1-1 Installation Precautions	7
1-2 Product Specifications	8
1-3 Installing and Removing the CPU	11
1-4 Installing and Removing Memory	13
1-4-1 4-Channel Memory Configuration	13
1-4-2 Installing and Removing a Memory Module	14
1-4-3 DIMM Population Table	14
Processor and Memory Module Matrix Table	15
1-5 Installing the M.2 SSD Module	16
1-6 Back Panel Connectors	17
1-7 Internal Connectors	19
1-8 Jumper Settings	29
Chapter 5 BIOS Setup	30
5-1 The Main Menu	32
5-2 Advanced Menu	35
5-2-1 Trusted Computing	36
5-2-2 Serial Port Console Redirection	37
5-2-3 SIO Configuration	40
5-2-4 PCI Subsystem Settings	41
5-2-5 USB Configuration	43
5-2-6 Network Stack Configuration	44
5-2-7 Post Report Configuration	45
5-2-8 KMIP Server Configuration	46
5-2-9 NVMe Configuration	47
5-2-10 Chipset Configuration	49
5-2-11 Tls Auth Configuration	51
5-2-12 iSCSI Configuration	54
5-2-13 Intel(R) I210 Gigabit Network Connection	55
5-2-14 VLAN Configuration	57
5-3 Chipset Menu	58
5-3-1 Processor Configuration	59
5-3-2 Common RefCode Configuration	62
5-3-3 UPI Configuration	63
5-3-4 Memory Configuration	66
5-3-5 IIO Configuration	72

5-3-6	Advanced Power Management Configuration	77
5-3-7	Miscellaneous Configuration	83
5-3-8	Runtime Error Logging Settings	84
5-3-9	Power Policy.....	86
5-4	Server Management Menu.....	88
5-4-1	System Event Log	90
5-4-2	View FRU Information	91
5-4-3	BMC VLAN Configuration.....	92
5-4-4	BMC Network Configuration	93
5-4-5	IPv6 BMC Network Configuration	94
5-5	Security Menu	95
5-5-1	Secure Boot	96
5-6	Boot Menu.....	99
5-7	Save & Exit Menu.....	101
5-8	BIOS Recovery	103
5-9	BIOS POST Beep code (AMI standard).....	104
5-9-1	PEI Beep Codes.....	104
5-9-2	DXE Beep Codes	104

The diagram illustrates the system architecture for Intel Xeon 6 processors, showing two CPU0 and CPU1 units connected via a 4x UPI interface. Each CPU is an Intel Xeon 6 processor (LGA4710 Socket).

Memory and Storage:

- Memory:** 8-Channel DDR5, 8 x DIMMs. RDIMM: Up to 6400 MT/s. MCR DIMM: Up to 8000 MT/s.
- Storage:** SATAIII x6, SATAIII x6, 3 x SlimSAS.

Connectivity and Expansion:

- PCIe:** PCIe5.0 x16 (Slot1, Slot2, Slot3), PCIe5.0 x4, PCIe3.0 x2.
- USB:** USB 3.2 Gen1, USB 3.2 Gen2, USB host controller, USB 3.2 x1, USB 3.2 x2.
- Network:** 2 x 10G LAN, 2 x 10G LAN, MLAN, RTL2111FD, RGMII.
- Other:** TPM, SPI Flash 64MB, SPI Flash 64MB, VGA, COM.

System Components:

- BMC:** BMC ASPEED AST2600.
- Switch:** Switch.
- CSIO:** CSIO.











Chapter 1 Hardware Installation






1-1 Installation Precautions




The motherboard contains numerous delicate electronic circuits and components which can become damaged as a result of electrostatic discharge (ESD). Prior to installation, carefully read the user's manual and follow these procedures:

- Prior to installation, do not remove or break motherboard S/N (Serial Number) sticker or warranty sticker provided by your dealer. These stickers are required for warranty validation.
- Always remove the AC power by unplugging the power cord from the power outlet before installing or removing the motherboard or other hardware components.
- When connecting hardware components to the internal connectors on the motherboard, make sure they are connected tightly and securely.
- When handling the motherboard, avoid touching any metal leads or connectors.
- It is best to wear an electrostatic discharge (ESD) wrist strap when handling electronic components such as a motherboard, CPU or memory. If you do not have an ESD wrist strap, keep your hands dry and first touch a metal object to eliminate static electricity.
- Prior to installing the motherboard, please have it on top of an antistatic pad or within an electrostatic shielding container.
- Before unplugging the power supply cable from the motherboard, make sure the power supply has been turned off.
- Before turning on the power, make sure the power supply voltage has been set according to the local voltage standard.
- Before using the product, please verify that all cables and power connectors of your hardware components are connected.
- To prevent damage to the motherboard, do not allow screws to come in contact with the motherboard circuit or its components.
- Make sure there are no leftover screws or metal components placed on the motherboard or within the computer casing.
- Do not place the computer system on an uneven surface.
- Do not place the computer system in a high-temperature environment.
- Turning on the computer power during the installation process can lead to damage to system components as well as physical harm to the user.
- If you are uncertain about any installation steps or have a problem related to the use of the product, please consult a certified computer technician.

1-2 Product Specifications

 CPU	<ul style="list-style-type: none"> ◆ Intel® Xeon® 6 Processors <ul style="list-style-type: none"> - Intel® Xeon® 6700E-Series Processors - Intel® Xeon® 6700P-Series Processors (available Q1'25) - Intel® Xeon® 6500P-Series Processors (available Q1'25) ◆ Dual processor, TDP up to 350W <p>[Note] If only 1 CPU is installed, some PCIe or memory functions might be unavailable.</p>
 Form Factor	<ul style="list-style-type: none"> ◆ ATX ◆ 304.8W x 244D (mm)
 Socket	<ul style="list-style-type: none"> ◆ 2 x LGA 4710 ◆ Socket E2
 Chipset	<ul style="list-style-type: none"> ◆ System on Chip
 Memory Type	<ul style="list-style-type: none"> ◆ 16 x DIMM slots ◆ DDR5 memory supported ◆ 8-Channel memory architecture ◆ MCR DIMM supported ^[1] ◆ RDIMM: Up to 6400 MT/s ◆ MCR DIMM: Up to 8000 MT/s <p>^[1] MCR DIMMs are only supported with Intel® Xeon® 6 Processors with P-cores.</p>
 Integrated Network	<ul style="list-style-type: none"> ◆ 2 x 10Gb/s LAN (1 x Intel® X710-AT2) ◆ 2 x 1Gb/s LAN (2 x Intel® I210-AT) <ul style="list-style-type: none"> - Support NCSI function ◆ 1 x 10/100/1000 Mbps Management LAN
 Integrated Video Controller	<ul style="list-style-type: none"> ◆ Integrated in Aspeed® AST2600 <ul style="list-style-type: none"> - 1 x VGA port
 Integrated Audio Controller	<ul style="list-style-type: none"> ◆ N/A
 Storage Interface	<ul style="list-style-type: none"> ◆ SlimSAS: <ul style="list-style-type: none"> - 3 x SlimSAS 4i for 12 x SATA 6Gb/s, from CPU_0 ◆ M.2: <ul style="list-style-type: none"> - 2 x M.2 (2280/22110), PCIe Gen5 x4, from CPU_0
 Support RAID Function	<ul style="list-style-type: none"> ◆ Onboard VROC key header

	Expansion Slots	<ul style="list-style-type: none"> ♦ Slot_6: PCIe x16 (Gen5 x16), from CPU_0 ♦ Slot_5: PCIe x16 (Gen5 x16), from CPU_0 ♦ Slot_4: PCIe x16 (Gen5 x16), from CPU_0 ♦ Slot_3: PCIe x16 (Gen5 x16), from CPU_1 ♦ Slot_2: PCIe x16 (Gen5 x16), from CPU_1 ♦ Slot_1: PCIe x16 (Gen5 x16), from CPU_1
	On-Board Connectors	<ul style="list-style-type: none"> ♦ 1 x 24-pin ATX main power connector ♦ 2 x 8-pin ATX 12V power connectors ♦ 2 x 6-pin ATX 12V power connectors ♦ 2 x CPU fan headers ♦ 5 x System fan headers ♦ 1 x USB 3.2 Gen1 x2 header ♦ 2 x M.2 slots ♦ 3 x SlimSAS connectors ♦ 1 x VROC connector ♦ 1 x NCSI connector ♦ 1 x Front panel header ♦ 1 x Backplane board header ♦ 1 x PMBus header ♦ 1 x IPMB header ♦ 1 x TPM header ♦ 1 x PRoT connector (only enabled on RoT SKU)
	Rear I/O Connectors	<ul style="list-style-type: none"> ♦ 2 x USB 3.2 Gen1 ports (Type-A) ♦ 1 x VGA port ♦ 1 x RJ45 serial port ♦ 4 x RJ45 ports ♦ 1 x MLAN port ♦ 1 x ID button with LED
	Security Modules	<ul style="list-style-type: none"> ♦ 1 x TPM header with SPI interface - Optional TPM2.0 kit: CTM012 ♦ 1 x PRoT connector (only enabled on RoT SKU)
	OS Driver Supported	<ul style="list-style-type: none"> ♦ Please refer to OS compatibility table in support page

	Server Management	<ul style="list-style-type: none"> ◆ Aspeed® AST2600 Baseboard Management Controller ◆ GIGABYTE Management Console web interface ◆ ◆ Dashboard ◆ HTML5 KVM ◆ Sensor Monitor (Voltage, RPM, Temperature, CPU Status ...etc.) ◆ Sensor Reading History Data ◆ FRU Information ◆ SEL Log in Linear Storage / Circular Storage Policy ◆ Hardware Inventory ◆ Fan Profile ◆ System Firewall ◆ Power Consumption ◆ Power Control ◆ Advanced power capping ◆ LDAP / AD / RADIUS Support ◆ Backup & Restore Configuration ◆ Remote BIOS/BMC/CPLD Update ◆ Event Log Filter ◆ User Management ◆ Media Redirection Settings ◆ PAM Order Settings ◆ SSL Settings ◆ SMTP Settings
	PSU Connectors	<ul style="list-style-type: none"> ◆ 1 x 24-pin ATX main power connector ◆ 2 x 8-pin ATX 12V power connectors ◆ 2 x 6-pin ATX 12V power connectors
	Operating Properties	<ul style="list-style-type: none"> ◆ Operating temperature: 10°C to 40°C ◆ Operating humidity: 8-80% (non-condensing) ◆ Non-operating temperature: -40°C to 60°C ◆ Non-operating humidity: 20%-95% (non-condensing)
GIGABYTE reserves the right to make any changes to the product specifications and product-related information without prior notice.		

1-3 Installing and Removing the CPU



Read the following guidelines before you begin to install the CPU:

- Make sure that the motherboard supports the CPU.
- Always turn off the computer and unplug the power cord from the power outlet before installing the CPU to prevent hardware damage.
- Unplug all cables from the power outlets.
- Disconnect all telecommunication cables from their ports.
- Place the system unit on a flat and stable surface.
- Open the system according to the instructions.

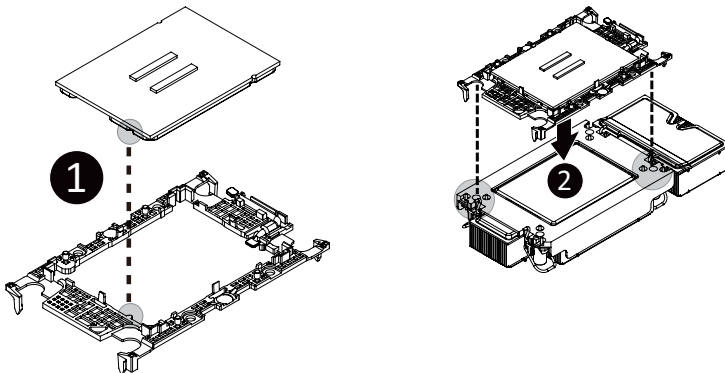
WARNING!

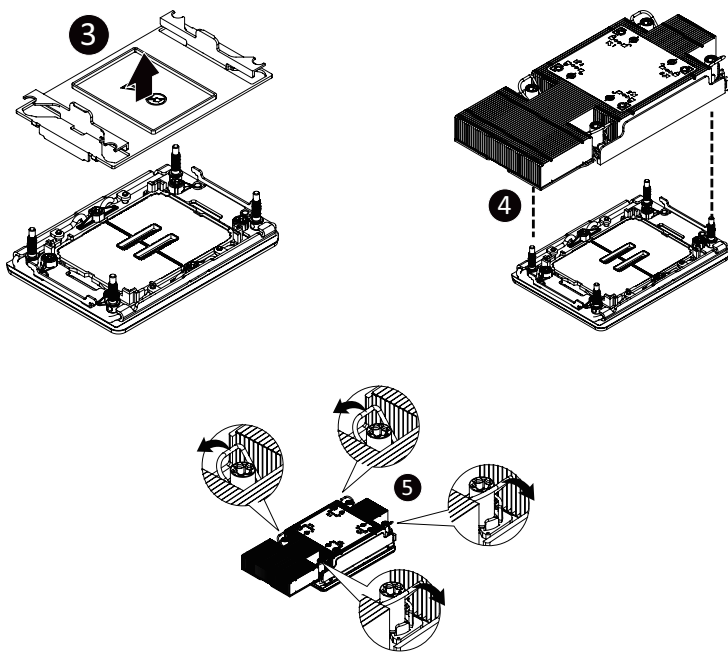
Failure to properly turn off the server before you start installing components may cause serious damage. Do not attempt the procedures described in the following sections unless you are a qualified service technician.

Follow these instructions to Install the CPU:

1. Align and install the processor on the carrier.
NOTE: Apply thermal compound evenly on the top of the CPU. Remove the protective cover from the underside of the heat sink.
2. Carefully flip the heat sink cover. Then install the carrier assembly on the bottom of the heat sink and make sure the gold arrow is located in the correct direction.
3. Remove the CPU cover.
NOTE: Save the CPU cover in the event that you need to remove the CPU from the socket.
4. Align the heat sink with the CPU socket by the guide pins and make sure the gold arrow is located in the correct direction. Then place the heat sink onto the top of the CPU socket.
5. Position the rotating wires into the latch position. Tighten the screws in a sequential order (1→2→3→4).

NOTE: When disassembling the heat sink, loosen the screws in reverse order (4→3→2→1) and then move the rotating wires into the unlatch position.





Carrier Types used for Package Types

Package Type	Granite Rapids-SP XCC	Granite Rapids-SP HCC Granite Rapids-SP LCC Sierra Forest-SP Clearwater Forest-SP
Carrier Code	E2A	E2B
Shim?	No	Yes
Integrated TIM Break Lever	Yes	Yes

NOTE!

- The carrier code is marked on each carrier and matches a code laser marked on to the IHS(Integrated Heat Spreader) to ensure the right parts are used together
- When installing the heat sink to CPU, use T30-Lobe driver to tighten 4 captive nuts in sequence as 1-4.
- The screw tightening torque: 8 ± 0.5 kgf-cm.

1-4 Installing and Removing Memory

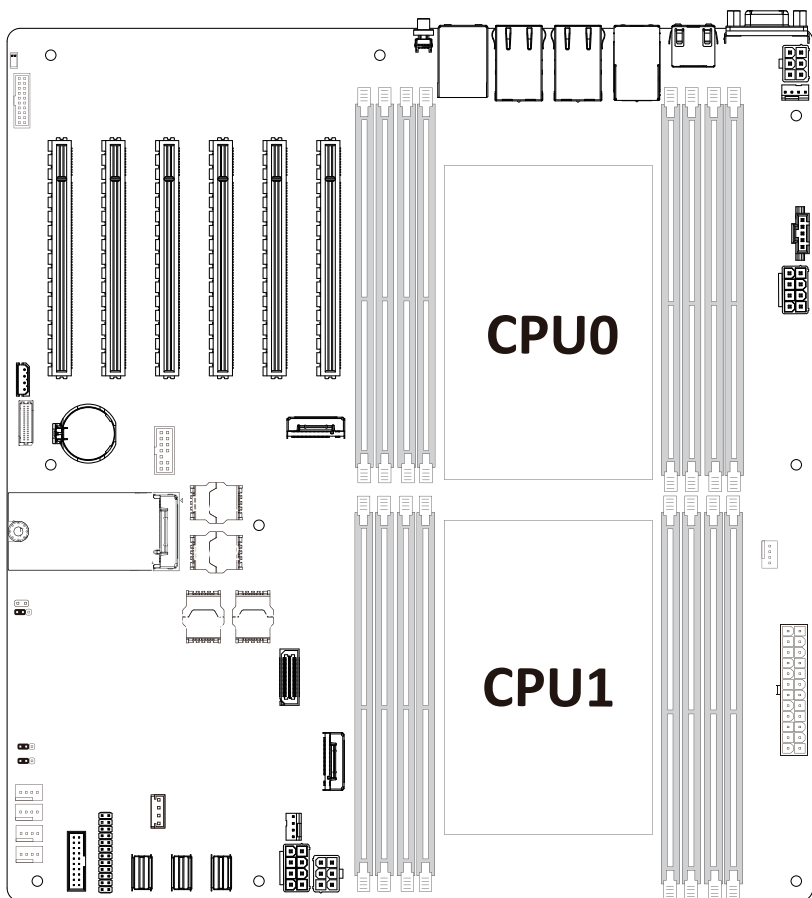


Read the following guidelines before you begin to install the memory:

- Make sure that the motherboard supports the memory. It is recommended to use memory of the same capacity, brand, speed, and chips.
- Always turn off the computer and unplug the power cord from the power outlet before installing the memory to prevent hardware damage.
- Memory modules have a foolproof design. A memory module can be installed in only one direction. If you are unable to insert the memory, switch the direction.

1-4-1 4-Channel Memory Configuration

This motherboard provides 16 DDR5 memory slots and supports 8-Channel Technology. After the memory is installed, the BIOS will automatically detect the specifications and capacity of the memory.



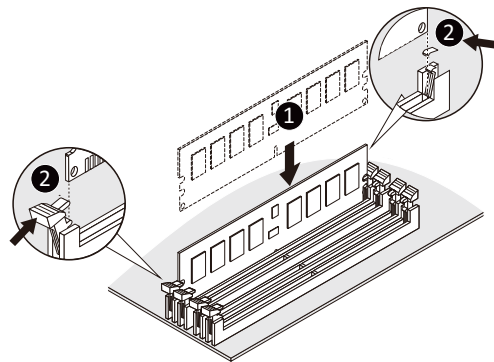
1-4-2 Installing and Removing a Memory Module



Before installing a memory module, make sure to turn off the computer and unplug the power cord from the power outlet to prevent damage to the memory module. Be sure to install DDR5 DIMMs on this motherboard.

Follow these instructions to install a DIMM module:

1. Insert the DIMM memory module vertically into the DIMM slot and push it down.
2. Close the plastic clip at both edges of the DIMM slots to lock the DIMM module.
3. Reverse the installation steps when you want to remove the DIMM module.



1-4-3 DIMM Population Table

Type	Ranks Per DIMM and Data Width	DIMM Capacity (GB)	Speed (MT/s); Voltage (V); DIMM per Channel (DPC)	
			1DPC*	2DPC*
		16Gb	1.1V	
RDIMM	SRx8 (RC D)	16GB	4800	4400
	SRx4 (RC C)	32GB		
	DRx8 (RC E)	32GB		
	DRx4 (RC A)	64GB		
RDIMM 3DS	(4R/8R)x4	2H-128GB		
	(RC A)	4H-256GB		

*1DPC applies to 1SPC or 2SPC implementations (SPC - Sockets Per Channel)

5th Gen Intel Xeon Scalable Processors-SP Memory Support

Type	Ranks Per DIMM and Data Width	DIMM Capacity (GB)			Speed (MT/s); Voltage (V); DIMM per Channel (DPC)	
					1DPC ¹	2DPC
		16Gb	24Gb ²	36Gb	1.1V	
RDIMM	SRx8 (RC D)	16GB	24GB	NA	5600 ³	4400 ³
	SRx4 (RC C)	32GB	48GB	NA		
	SRx4 (RC F) 9x4	NA	NA	NA		
	DRx8 (RC E)	32GB	48GB	NA		
	DRx4 (RC A)	64GB	96GB	128GB		
	DRx4 (RC B) 9x4	NA	NA	NA		
RDIMM 3DS	(4R/8R)x4 (RC A)	2H-128GB 4H-256GB	NA	NA	5600 ⁴	

- NOTE:
- 1. 1DPC applies to 1SPC or 2SPC implementations (SPC - Sockets Per Channel)
 - 2. 24Gb 2DPC not POR w/ 24GB and 48GB DIMMs.
 - 3. DDR5-5600 RDIMMs will be limited to 5600 MT/s 1DPC and 4400 MT/s 2DPC. DDR5-4800 DIMMs will be limited to 4800 MT/s 1DPC and 4400 MT/s 2DPC.
 - 4. DDR5-5600 DIMMs are required for 5600 and 5200 1DPC speeds.

Processor and Memory Module Matrix Table

- NOTE!
- Xeon 6700E/6500E-series do not support 12 DIMMs Configuration.

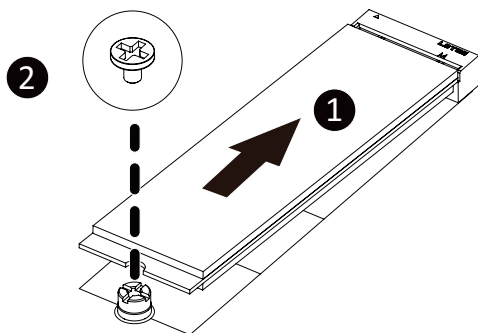
Memory Q'ty for each CPU	CPU0															
	H0	H1	G0	G1	F0	F1	E0	F1	A1	A0	B1	B0	C1	C0	D1	D0
	CPU1															
	P0	P1	O0	O1	N0	N1	M0	M1	I1	I0	J1	J0	K1	K0	L1	L0
1 DIMM										V						
4 DIMM			V				V			V				V		
	V				V							V				V
8 DIMM	V		V		V		V		V		V		V		V	
			V	V			V	V	V				V	V		
	V	V			V	V					V	V			V	V
12 DIMM	V		V	V	V		V	V	V	V		V	V	V		V
16 DIMM	V	V	V	V	V	V	V	V	V	V	V	V	V	V	V	V

1-5 Installing the M.2 SSD Module

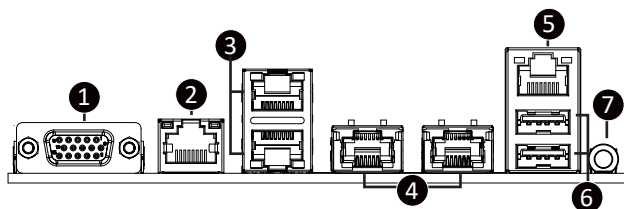
Follow the steps below to install a M.2 SSD module on your motherboard.

Step1. Insert the M.2 SSD module into the slot.

Step2. Secure it with the screw, tightening as necessary to fasten the M.2 SSD module in place.



1-6 Back Panel Connectors



1 VGA Port

Connect to a monitor device.

2 Serial Port (RJ45 Type)

Connect to serial-based mouse or data processing devices.

3 LAN Port #3 / LAN Port #4

The Gigabit Ethernet LAN port provides Internet connection at up to 10 GbE data rate. See the section below for a description of the states of the LAN port LEDs.

4 LAN Port #2 / LAN Port #1

The Gigabit Ethernet LAN port provides Internet connection at up to 1 GbE data rate. See the section below for a description of the states of the LAN port LEDs.

5 Server Management 10/100/1000 LAN Port

The LAN port provides Internet connection with data transfer speeds of 10/100/1000Mbps. This port is the dedicated LAN port for Server Management.

6 USB 3.2 Gen1 Ports

The USB port supports the USB 3.2 specification. Use this port for USB devices such as a USB keyboard/mouse, USB printer, USB flash drive etc.

7 ID Button with LED

When the system identification is active, the ID LED on the front/ back panel glows blue.

Connection/
Speed LED Link/Activity LED



LAN Port

10/100/1000 LAN LED:

State	Description
Yellow On	1 Gbps data rate
Green On	100 Mbps data rate
Off	10 Mbps data rate

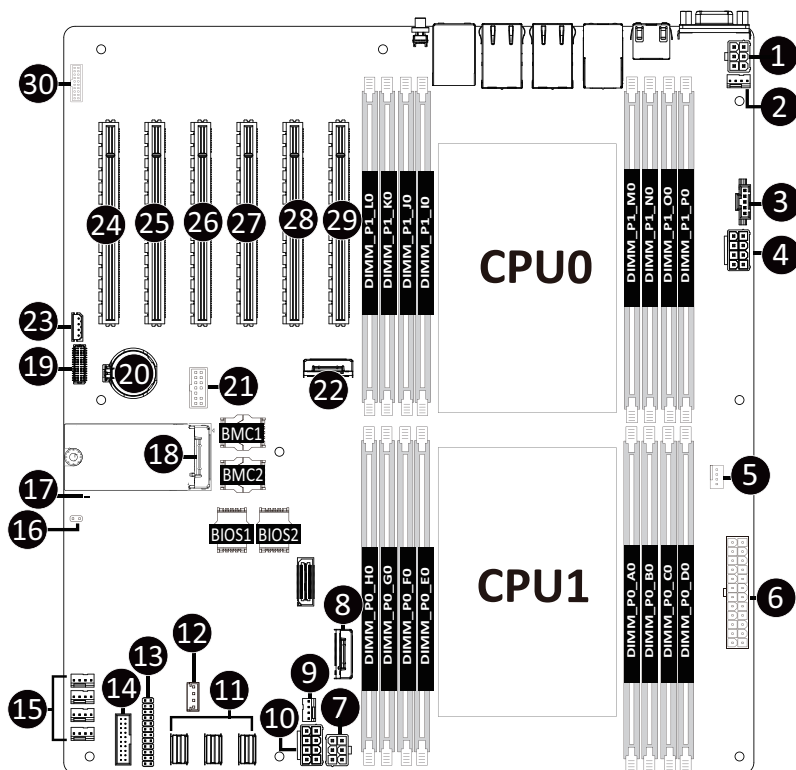
ID Button / LED:

State	Description
Blue on	System identification is active
Off	System identification is disable



- When removing the cable connected to a back panel connector, first remove the cable from your device and then remove it from the motherboard.
- When removing the cable, pull it straight out from the connector. Do not rock it side to side to prevent an electrical short inside the cable connector.

1-7 Internal Connectors



No.	Code	Description	No.	Code	Description
1	P12V_PCIE2	2x3 Pin 12V Power Connector	16	CASE_OPEN	Case Open Intrusion Header
2	CPU0_FAN	CPU0 Fan Connector	17	LED_BMC	BMC Firmware Readiness LED
3	PMBUS	PMBus Connector	18	PROT.Conn.	PRoT Module Connector
4	P12V_AUX	2x4 Pin 12V Power Connector	19	BP_1	HDD Backplane Board Connector
5	SYS_FAN1	System Fan Connector #1/#2/#3/#4	20	BAT	Battery Socket
6	ATX	2x12 Pin Main Power Connector	21	SPI_TPM	TPM Connector
7	P12V_PCIE1	2x3 Pin 12V Power Connector	22	M2_1	M.2 Slot (PCIe Gen5 x2, Support NGFF-2280/22110)
8	M2_0	M.2 Slot (PCIe Gen5 x2, Support NGFF-2280/22110)	23	IPMB	Intelligent Platform Management Bus Connector
9	CPU1_FAN	CPU1 Fan Connector	24	PCIE_1	PCIe x16 Slot (Gen5 x16)
10	P12V_AUX1	2x4 Pin 12V Power Connector	25	PCIE_2	PCIe x16 Slot (Gen5 x16)
11	SL_SATA3/SATA1/SATA2	Slimline Connector #3/#1/#2 (SATA 6Gb/s Signal)	26	PCIE_3	PCIe x16 Slot (Gen5 x16)
12	SW_RAID	NVMe RAID Upgrade Key (VROC Module Connector)	27	PCIE_4	PCIe x16 Slot (Gen5 x16)
13	FP_1	Front Panel Header	28	PCIE_5	PCIe x16 Slot (Gen5 x16)
14	F_USB3	Front Panel USB 3.2 Gen1 Connector	29	PCIE_6	PCIe x16 Slot (Gen5 x16)
15	FAN3/FAN2/FAN4/FAN5	System Fan Connector #3/#2/#4/#5	30	CN_NCSI	NCSI Connector



Read the following guidelines before connecting external devices:

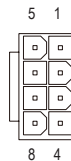
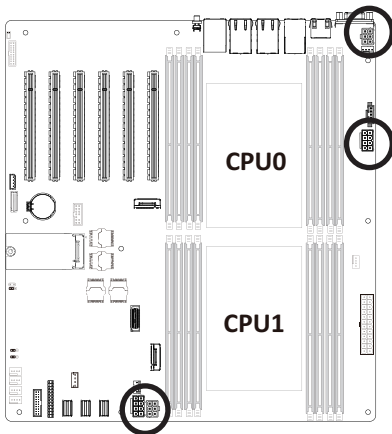
- First make sure your devices are compliant with the connectors you wish to connect.
- Before installing the devices, be sure to turn off the devices and your computer. Unplug the power cord from the power outlet to prevent damage to the devices.
- After installing the device and before turning on the computer, make sure the device cable has been securely attached to the connector on the motherboard.

1/4/6/7/10) P12V_PCIE2/P12V_AUX/ATX/P12V_PCIE1/P12V_AUX1 (2x3, 2x4 12V Power Connector and 2x12 Main Power Connector)

With the use of the power connector, the power supply can supply enough stable power to all the components on the motherboard. Before connecting the power connector, first make sure the power supply is turned off and all devices are properly installed. The power connector possesses a foolproof design. Connect the power supply cable to the power connector in the correct orientation. The 12V power connector mainly supplies power to the CPU. If the 12V power connector is not connected, the computer will not start.



To meet expansion requirements, it is recommended that a power supply that can withstand high power consumption be used (500W or greater). If a power supply is used that does not provide the required power, the result can lead to an unstable or unbootable system.



P12V_AUX1/ P12V_AUX2

Pin No.	Definition
1	GND
2	GND
3	GND
4	GND
5	+12V
6	+12V
7	+12V
8	+12V



P12V_PCIE2

Pin No.	Definition
1	+12V
2	+12V
3	+12V
4	GND
5	GND
6	GND

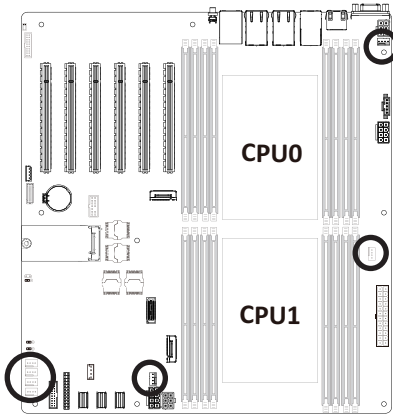


ATX

Pin No.	Definition	Pin No.	Definition
1	3.3V	13	3.3V
2	3.3V	14	-12V
3	GND	15	GND
4	+5V	16	PS_ON
5	GND	17	GND
6	+5V	18	GND
7	GND	19	GND
8	Power Good	20	NC
9	5VSB	21	+5V
10	+12V	22	+5V
11	+12V	23	+5V
12	3.3V	24	GND

2/5/9/15) CPU0_FAN/SYS_FAN1/CPU1_FAN/SYS_FAN3/SYS_FAN2/SYS_FAN4/SYS_FAN5
(CPU Fan/System Fan Headers)

The motherboard has one 4-pin CPU fan header (CPU_FAN), and two 4-pin (SYS_FAN) system fan headers. Most fan headers possess a foolproof insertion design. When connecting a fan cable, be sure to connect it in the correct orientation (the black connector wire is the ground wire). The motherboard supports CPU fan speed control, which requires the use of a CPU fan with fan speed control design. For optimum heat dissipation, it is recommended that a system fan be installed inside the chassis.



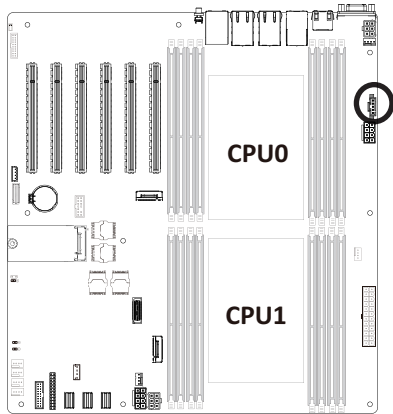
Pin No.	Definition
1	GND
2	+12V
3	Sense
4	Speed Control



- Be sure to connect fan cables to the fan headers to prevent your CPU and system from overheating. Overheating may result in damage to the CPU or the system may hang.
- These fan headers are not configuration jumper blocks. Do not place a jumper cap on the headers.

3) PMBus Connector

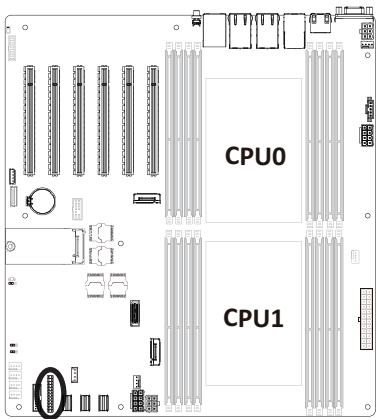
The Power Management Bus (PMBus) is a variant of the System Management Bus (SMBus) which is targeted at digital management of power supplies.



Pin No.	Definition
1	PMBus Clock
2	PMBus Data
3	PMBus Alert
4	GND
5	3.3V Sense

13) FP_1 (Front Panel Header)

Connect the power switch, reset switch, speaker, chassis intrusion switch/sensor and system status indicator on the chassis to this header according to the pin assignments below. Note the positive and negative pins before connecting the cables.



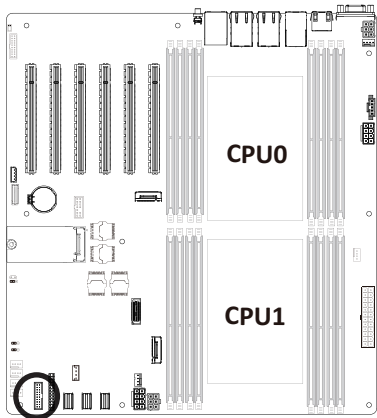
Pin No.	Definition	Pin No.	Definition
1	Power LED+	2	5V Standby
3	No Pin	4	ID LED+
5	Power LED-	6	ID LED-
7	HDD LED+	8	System Status LED+
9	HDD LED-	10	System Status LED-
11	Power Button	12	LAN1 Active LED+
13	GND	14	LAN1 Link LED-
15	Reset Button	16	SMBus Data
17	GND	18	SMBus Clock
19	ID Button	20	Case Open
21	GND	22	LAN2 Active LED+
23	NMI Switch	24	LAN2 Link LED-



The front panel design may differ by chassis. A front panel module mainly consists of power switch, reset switch, power LED, hard drive activity LED, speaker etc. When connecting your chassis front panel module to this header, make sure the wire assignments and the pin assignments are matched correctly.

14) F_USB3 (Front Panel USB 3.2 Gen1 Connector)

The connectors conform to USB 3.2 specification. Each USB header can provide two USB ports via an optional USB bracket. For purchasing the optional USB bracket, please contact the local dealer.

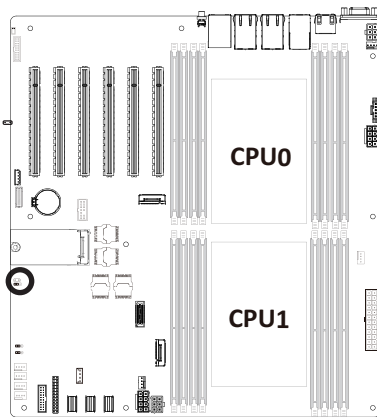


USB 3.2 Connector

Pin No.	Definition	Pin No.	Definition
1	Power	11	IntA_P2_D+
2	IntA_P1_SSRX-	12	IntA_P2_D-
3	IntA_P1_SSRX+	13	GND
4	GND	14	IntA_P2_SSTX+
5	IntA_P1_SSTX-	15	IntA_P2_SSTX-
6	IntA_P1_SSTX+	16	GND
7	GND	17	IntA_P2_SSRX+
8	IntA_P1_D-	18	IntA_P2_SSRX-
9	IntA_P1_D+	19	Power
10	NC	20	No Pin

16) CASE_OPEN (Case Open Intrusion Alert Header)

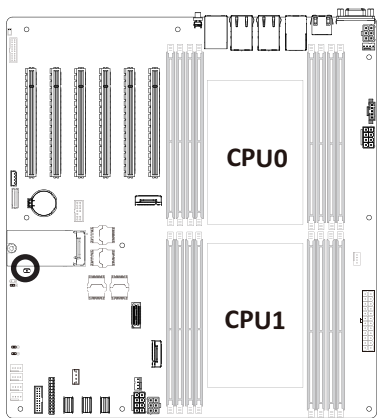
This motherboard provides a chassis detection feature that detects if the chassis cover has been removed. This function requires a chassis with chassis intrusion detection design.



- Open: Normal Operation (Default)
- Closed: Active Chassis Intrusion Alert

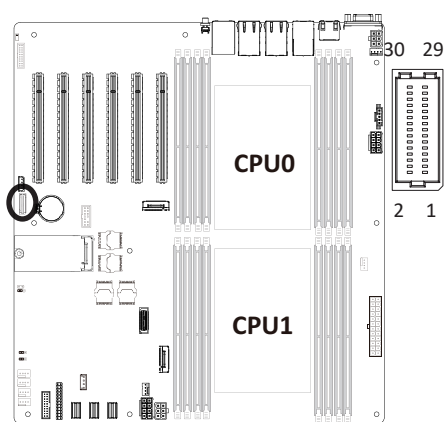
17) LED_BMC (BMC Firmware Readiness LED)

This motherboard provides a chassis detection feature that detects if the chassis cover has been removed. This function requires a chassis with chassis intrusion detection design.



State	Description
On	BMC firmware is initial
Blink	BMC firmware is ready
Off	AC loss

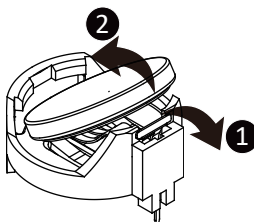
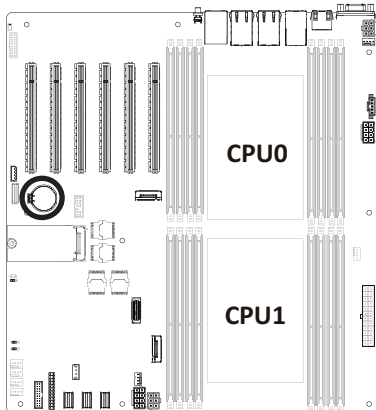
19) BP_1 (HDD Backplane Board Connector)



Pin No.	Definition	Pin No.	Definition
1	HP_ALERT_L	2	BPMI DIN/OUT
3	GND	4	BPMI DOUT/IN
5	BPMI_LOAD	6	GND
7	BPMI_CLK	8	PLD_Program_EN
9	GLED_AMB_N	10	GLED_GRN_N
11	FAN_IRQ_N	12	Reserved
13	BP_SCL	14	GND
15	BP_SDA	16	BP_RST_N
17	SMB_U2_TMP_SCL	18	GND
19	SMB_U2_TMP_SDA	20	I2C_DEV_RST
21	PH_HP_SCL0	22	GND
23	PH_HP_SDA0	24	GND
25	PH_HP_SCL1	26	GND
27	PH_HP_SDA1	28	GND
29	P3V3_AUX	30	P3V3_AUX

20) BAT (Battery Socket)

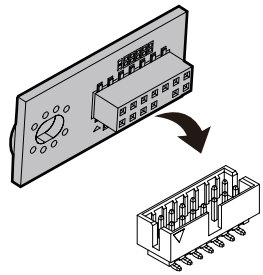
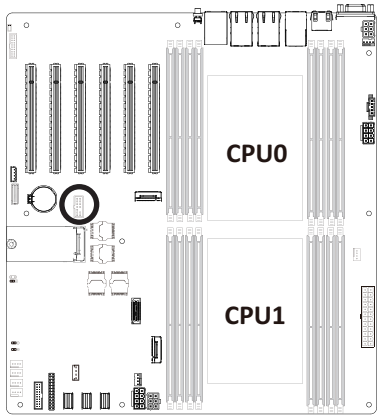
The battery provides power to keep the values (such as BIOS configurations, date, and time information) in the CMOS when the computer is turned off. Replace the battery when the battery voltage drops to a low level, or the CMOS values may not be accurate or may be lost.



- Always turn off your computer and unplug the power cord before replacing the battery.
- Replace the battery with an equivalent one. Danger of explosion if the battery is replaced with an incorrect model.
- Contact the place of purchase or local dealer if you are not able to replace the battery by yourself or uncertain about the battery model.
- Used batteries must be handled in accordance with local environmental regulations.

21) TPM (Trusted Platform Module Connector)

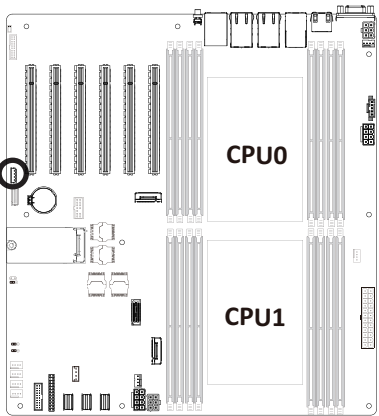
Trusted Platform Module (TPM) is an international standard for a secure cryptoprocessor, a dedicated microcontroller designed to secure hardware through integrated cryptographic keys.



Pin No.	Definition	Pin No.	Definition
1	Clock	8	No Connect
2	P_3V3_AUX	9	No Connect
3	LPC_RST	10	No Pin
4	No Connect	11	No Connect
5	SPI_MISO	12	GND
6	IRQ_SPI	13	SPI_CS_N
7	SPI_MOSI	14	GND

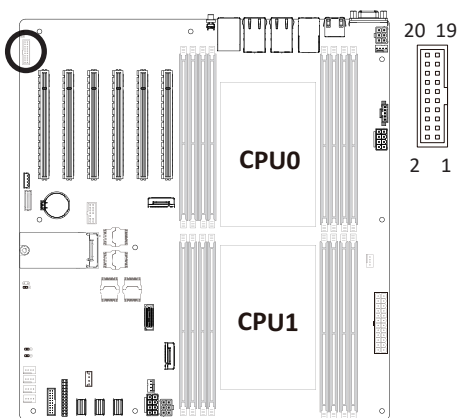
23) IPMB (IPMB Connector)

The IPMB connector is used to connect Intelligent Platform Management Bus (IPMB) devices in a computer system for remote monitoring and management capabilities..



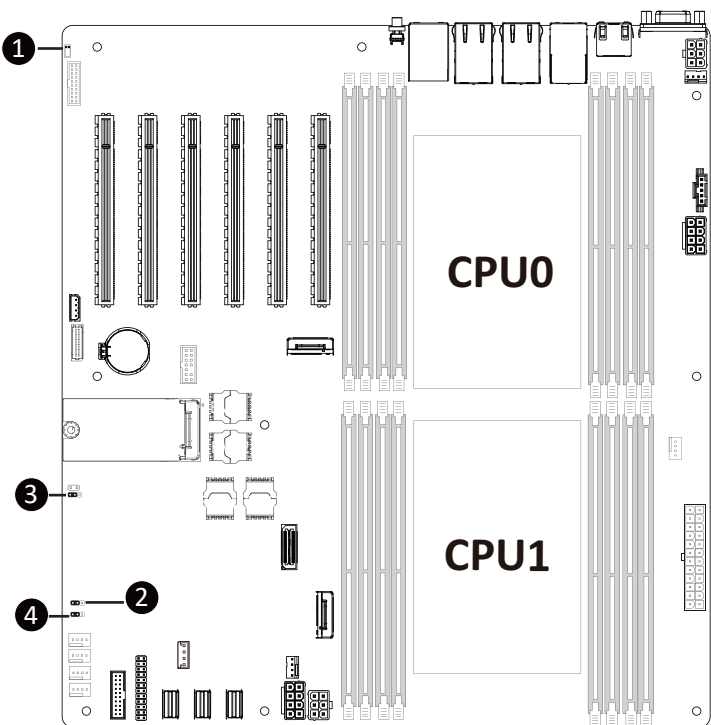
Pin No.	Definition
1	Clock
2	GND
3	Data
4	VCC

30) CN_NCSI (NCSI Connector)



Pin No.	Definition	Pin No.	Definition
1	NCSI_CLK	2	GND
3	NCSI_RX_D0	4	GND
5	NCSI_RX_D1	6	GND
7	NCSI_CRS_DV	8	GND
9	NCSI_RX_ER	10	GND
11	P3V3_AUX	12	GND
13	NCSI_TX_D1	14	GND
15	NCSI_TX_D0	16	GND
17	NCSI_TX_EN	18	GND
19	NCSI_PRESENT	20	P3V3_AUX

1-8 Jumper Settings



No.	Jumper Name	Jumper Setting		
1	NCSI_SW	SW1	SW2	
		OFF	ON	Onboard 10GbE LAN
		OFF	OFF	Onboard 1GbE LAN
		ON	-	CN_NCSI
2	BIOS Recovery (BIOS_RCVR)	1-2: Enable 2-3: Default		
3	Clear CMOS (CLR_CMOS)	1-2: Nomal operation (Default) 2-3: Clear CMOS data (Enable)		
4	Password Clear (BIOS_PWD)	1-2: Default 2-3: Enable		

Chapter 5 BIOS Setup

BIOS (Basic Input and Output System) records hardware parameters of the system in the EFI on the motherboard. Its major functions include conducting the Power-On Self-Test (POST) during system startup, saving system parameters, loading the operating system etc. The BIOS includes a BIOS Setup program that allows the user to modify basic system configuration settings or to activate certain system features. When the power is turned off, the battery on the motherboard supplies the necessary power to the CMOS to keep the configuration values in the CMOS.

To access the BIOS Setup program, press the key during the POST when the power is turned on.



- BIOS flashing is potentially risky, if you do not encounter any problems when using the current BIOS version, it is recommended that you don't flash the BIOS. To flash the BIOS, do it with caution. Inadequate BIOS flashing may result in system malfunction.
- It is recommended that you not alter the default settings (unless you need to) to prevent system instability or other unexpected results. Inadequately altering the settings may result in system's failure to boot. If this occurs, try to clear the CMOS values and reset the board to default values. (Refer to the **Exit** section in this chapter or introductions of the battery/clearing CMOS jumper in Chapter 4 for how to clear the CMOS values.)

BIOS Setup Program Function Keys

<<-><->>	Move the selection bar to select the screen
<↑><↓>	Move the selection bar to select an item
<+>	Increase the numeric value or make changes
<->	Decrease the numeric value or make changes
<Enter>	Execute command or enter the submenu
<Esc>	Main Menu: Exit the BIOS Setup program Submenus: Exit current submenu
<F1>	Show descriptions of general help
<F3>	Restore the previous BIOS settings for the current submenus
<F9>	Load the Optimized BIOS default settings for the current submenus
<F10>	Save all the changes and exit the BIOS Setup program

■ **Main**

This setup page includes all the items of the standard compatible BIOS.

■ **Advanced**

This setup page includes all the items of AMI BIOS special enhanced features.

(ex: Auto detect fan and temperature status, automatically configure hard disk parameters.)

■ **Chipset**

This setup page includes all the submenu options for configuring the functions of the Platform Controller Hub.

■ **Server Management**

Server additional features enabled/disabled setup menus.

■ **Security**

Change, set, or disable supervisor and user password. Configuration supervisor password allows you to restrict access to the system and BIOS Setup.

A supervisor password allows you to make changes in BIOS Setup.

A user password only allows you to view the BIOS settings but not to make changes.

■ **Boot**

This setup page provides items for configuration of the boot sequence.

■ **Save & Exit**

Save all the changes made in the BIOS Setup program to the CMOS and exit BIOS Setup. (Pressing <F10> can also carry out this task.)

Abandon all changes and the previous settings remain in effect. Pressing <Y> to the confirmation message will exit BIOS Setup. (Pressing <Esc> can also carry out this task.)

5-1 The Main Menu

Once you enter the BIOS Setup program, the Main Menu (as shown below) appears on the screen. Use arrow keys to move among the items and press <Enter> to accept or enter other sub-menu.

Main Menu Help

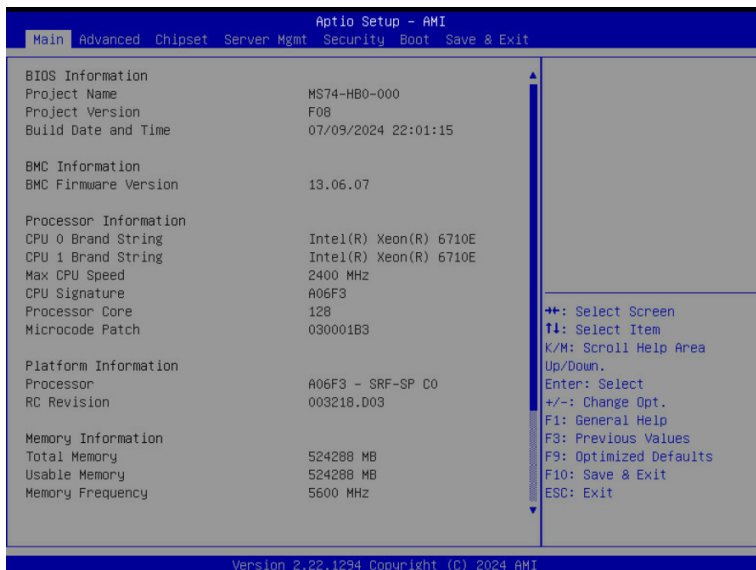
The on-screen description of a highlighted setup option is displayed on the bottom line of the Main Menu.

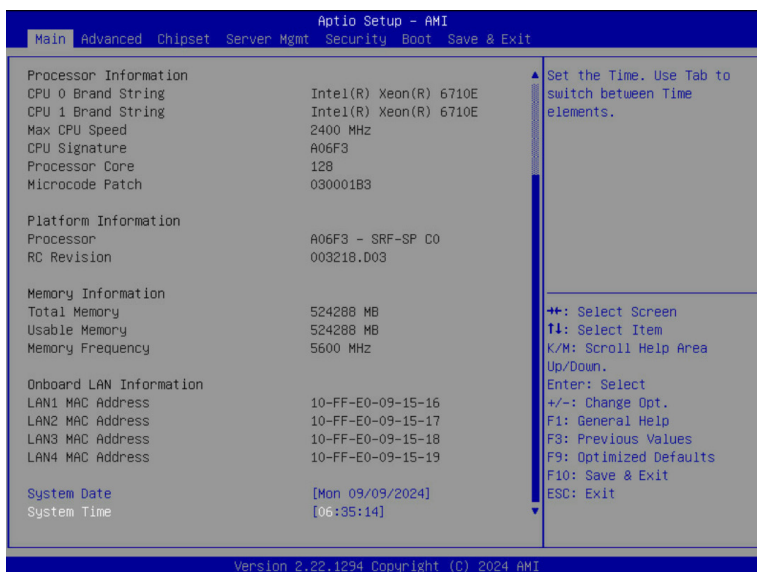
Submenu Help

While in a submenu, press <F1> to display a help screen (General Help) of function keys available for the menu. Press <Esc> to exit the help screen. Help for each item is in the Item Help block on the right side of the submenu.



- When the system is not stable as usual, select the **Restore Defaults** item to set your system to its defaults.
- The BIOS Setup menus described in this chapter are for reference only and may differ by BIOS version.





Parameter	Description
BIOS Information	
Project Name	Displays the project name information.
Project Version	Displays version number of the BIOS setup utility.
Build Date and Time	Displays the date and time when the BIOS setup utility was created.
BMC Information ^(Note1)	
BMC Firmware Version ^(Note1)	Displays BMC firmware version information.
Processor Information	
CPU Brand String/ Max CPU Speed / CPU Signature / Processor Core / Microcode Patch	Displays the technical information for the installed processor(s).
Platform Information	
Processor/ PCH/ RC Revision	Displays the information of the installed processor(s) and PCH.
Memory Information ^(Note2)	
Total Memory	Displays the total memory size of the installed memory.
Usable Memory	Displays the usable memory size of the installed memory.

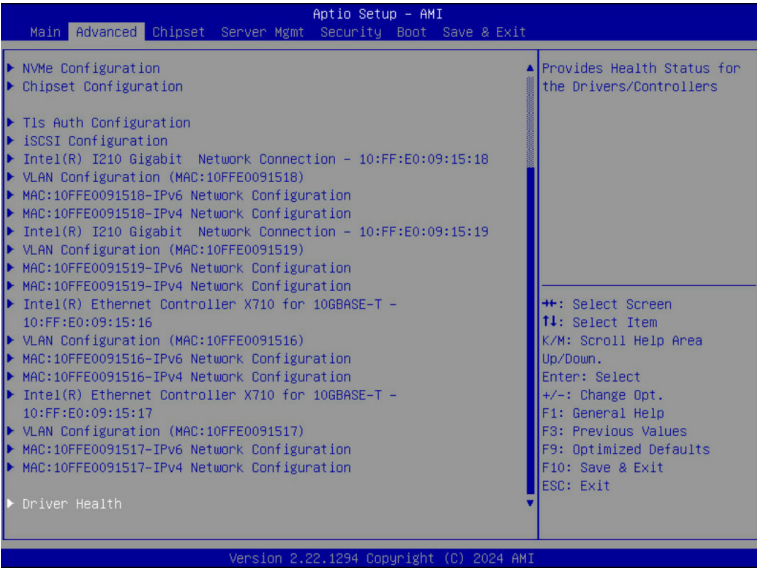
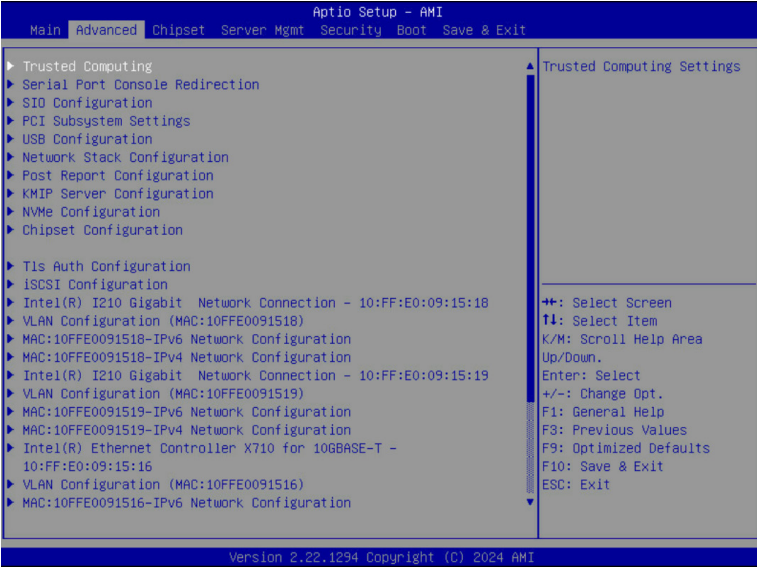
(Note1) Functions available on selected models.

(Note2) This section will display capacity and frequency information of the memory that the customer has installed.

Parameter	Description
Memory Frequency	Displays the frequency information of the installed memory.
System Date	Sets the date following the weekday-month-day-year format.
System Time	Sets the system time following the hour-minute-second format.

5-2 Advanced Menu

The Advanced Menu displays submenu options for configuring the function of various hardware components. Select a submenu item, then press <Enter> to access the related submenu screen.

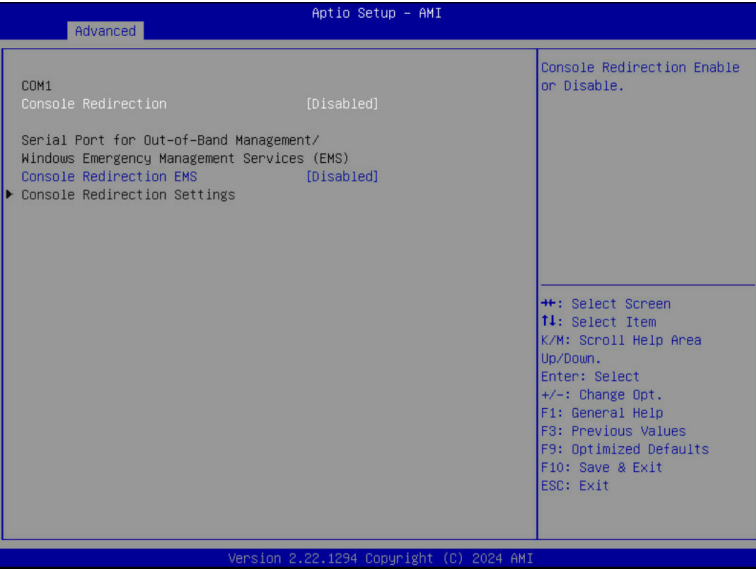


5-2-1 Trusted Computing



Parameter	Description
Configuration	
TPM v1.2 Support	Enable/Disable BIOS support for security device. OS will not show security device. TCG EFI protocol and INT1A interface will not be available. Options available: Disable, Enable. Default setting is Enable .

5-2-2 Serial Port Console Redirection



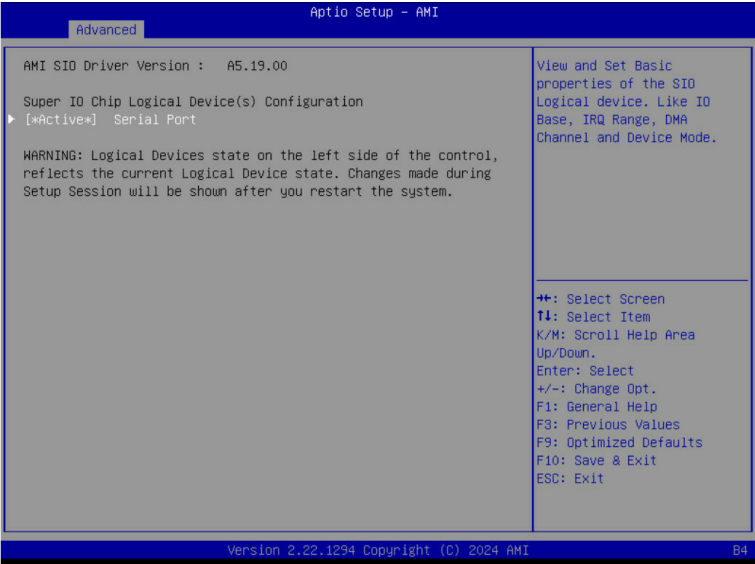
Parameter	Description
COM1 Console Redirection ^(Note)	Console redirection enables the users to manage the system from a remote location. Options available: Enabled, Disabled. Default setting is Disabled .
COM1 Console Redirection Settings	Press [Enter] to configure advanced items. Please note that this item is configurable when COM1 Console Redirection is set to Enabled. <ul style="list-style-type: none">◆ Terminal Type<ul style="list-style-type: none">– Selects a terminal type to be used for console redirection.– Options available: VT100, VT100PLUS, VT-UTF8, ANSI. Default setting is VT100PLUS.◆ Bits per second<ul style="list-style-type: none">– Selects the transfer rate for console redirection.– Options available: 9600, 19200, 38400, 57600, 115200. Default setting is 115200.◆ Data Bits<ul style="list-style-type: none">– Selects the number of data bits used for console redirection.– Options available: 7, 8. Default setting is 8.

(Note) Advanced items prompt when this item is defined.

Parameter	Description
COM1 Console Redirection Settings (continued)	<ul style="list-style-type: none"> ◆ Parity <ul style="list-style-type: none"> – A parity bit can be sent with the data bits to detect some transmission errors. – Even: parity bit is 0 if the num of 1's in the data bits is even. – Odd: parity bit is 0 if num of 1's in the data bits is odd. – Mark: parity bit is always 1. Space: Parity bit is always 0. – Mark and Space Parity do not allow for error detection. – Options available: None, Even, Odd, Mark, Space. Default setting is None. ◆ Stop Bits <ul style="list-style-type: none"> – Stop bits indicate the end of a serial data packet. (A start bit indicates the beginning). The standard setting is 1 stop bit. Communication with slow devices may require more than 1 stop bit. – Options available: 1, 2. Default setting is 1. ◆ Flow Control <ul style="list-style-type: none"> – Flow control can prevent data loss from buffer overflow. When sending data, if the receiving buffers are full, a 'stop' signal can be sent to stop the data flow. Once the buffers are empty, a 'start' signal can be sent to re-start the flow. Hardware flow control uses two wires to send start/stop signals. – Options available: None, Hardware RTS/CTS. Default setting is None. ◆ VT-UTF8 Combo Key Support <ul style="list-style-type: none"> – Enable/Disable the VT-UTF8 Combo Key Support. – Options available: Enabled, Disabled. Default setting is Enabled. ◆ Recorder Mode <ul style="list-style-type: none"> – When this mode enabled, only texts will be send. This is to capture Terminal data. – Options available: Enabled, Disabled. Default setting is Disabled. ◆ Resolution 100x31 <ul style="list-style-type: none"> – Enable/Disable extended terminal resolution. – Options available: Enabled, Disabled. Default setting is Enabled. ◆ Putty KeyPad <ul style="list-style-type: none"> – Selects Function Key and KeyPad on Putty. – Options available: VT100, LINUX, XTERMR6, SC0, ESCN, VT400. Default setting is VT100.

Parameter	Description
Serial Port for Out-of-Band Management / Windows Emergency Management Services (EMS) Console Redirection ^(Note)	<p>EMS console redirection allows the user to configure Console Redirection Settings to support Out-of-Band Serial Port management.</p> <p>Options available: Enabled, Disabled. Default setting is Disabled.</p>
Serial Port for Out-of-Band EMS Console Redirection Settings	<p>Press [Enter] to configure advanced items.</p> <p>Please note that this item is configurable when Serial Port for Out-of-Band Management EMS Console Redirection is set to Enabled.</p> <ul style="list-style-type: none"> ◆ Out-of-Band Mgmt Port <ul style="list-style-type: none"> – Microsoft Windows Emergency Management Service (EMS) allows for remote management of a Windows Server OS through a serial port. – Default setting is COM1. ◆ Terminal Type EMS <ul style="list-style-type: none"> – Selects a terminal type to be used for console redirection. – Options available: VT100, VT100PLUS, VT-UTF8, ANSI. Default setting is VT100PLUS. ◆ Bits per second EMS <ul style="list-style-type: none"> – Selects the transfer rate for console redirection. – Options available: 9600, 19200, 57600, 115200. Default setting is 115200. ◆ Flow Control EMS <ul style="list-style-type: none"> – Flow control can prevent data loss from buffer overflow. When sending data, if the receiving buffers are full, a 'stop' signal can be sent to stop the data flow. Once the buffers are empty, a 'start' signal can be sent to re-start the flow. Hardware flow control uses two wires to send start/stop signals. – Options available: None, Hardware RTS/CTS, Software Xon/Xoff. Default setting is None.

5-2-3 SIO Configuration



Parameter	Description
AMI SIO Driver Version	Displays the AMI SIO driver version information.
Super IO Chip Logical Device(s) Configuration	
	Press [Enter] to configure advanced items.
	<ul style="list-style-type: none">◆ Use This Device<ul style="list-style-type: none">– When set to Enabled allows you to configure the serial port settings. When set to Disabled, displays no configuration for the serial port.– Options available: Enabled, Disabled. Default setting is Enabled.◆ Logical Device Settings/Current:<ul style="list-style-type: none">– Displays the serial port base I/O address and IRQ.◆ Possible:<ul style="list-style-type: none">– Configures the serial port base I/O address and IRQ.
[*Active*] Serial Port	Use Automatic Settings IO=3F8h; IRQ=4; DMA; IO=3F8h; IRQ=4; DMA; IO=2F8h; IRQ=4; DMA; IO=3E8h; IRQ=4; DMA; IO=2E8h; IRQ=4; DMA; Default setting is Use Automatic Settings .

5-2-4 PCI Subsystem Settings

Aptio Setup - AMI		
Advanced		
PCI Bus Driver Version	A5.01.32	Enable/Disable PCIe_1 I/O ROM
PCIe_1 I/O ROM	[Enabled]	
PCIe_1 Lanes	[Auto]	
PCIe_1 Max Link Speed	[Auto]	
PCIe_2 I/O ROM	[Enabled]	
PCIe_2 Lanes	[Auto]	
PCIe_2 Max Link Speed	[Auto]	
PCIe_3 I/O ROM	[Enabled]	
PCIe_3 Lanes	[Auto]	
PCIe_3 Max Link Speed	[Auto]	
PCIe_4 I/O ROM	[Enabled]	
PCIe_4 Lanes	[Auto]	
PCIe_4 Max Link Speed	[Auto]	
PCIe_5 I/O ROM	[Enabled]	++: Select Screen t1: Select Item K/M: Scroll Help Area Up/Down. Enter: Select +/-: Change Opt. F1: General Help F3: Previous Values F9: Optimized Defaults F10: Save & Exit ESC: Exit
PCIe_5 Lanes	[Auto]	
PCIe_5 Max Link Speed	[Auto]	
PCIe_6 I/O ROM	[Enabled]	
PCIe_6 Lanes	[Auto]	
PCIe_6 Max Link Speed	[Auto]	
Version 2.22.1294 Copyright (C) 2024 AMI		

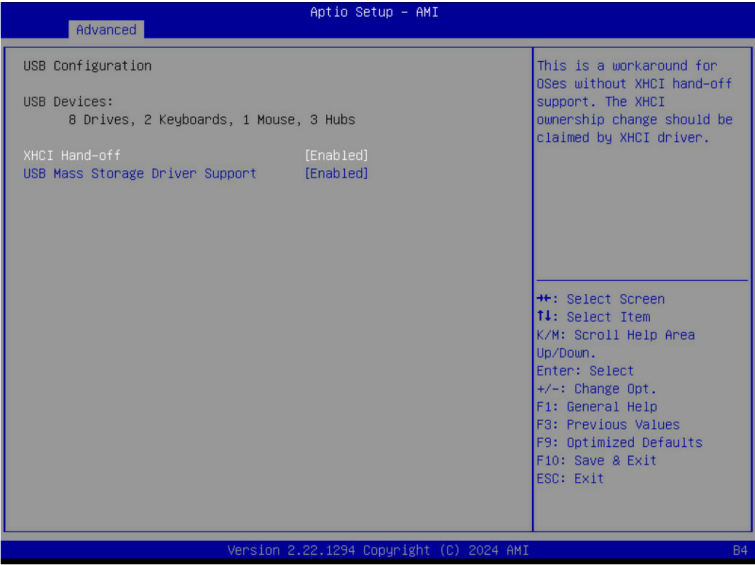
Aptio Setup - AMI		
Advanced		
PCIe_6 I/O ROM	[Enabled]	If system has SR-IOV capable PCIe Devices, this option Enables or Disables Single Root IO Virtualization Support.
PCIe_6 Lanes	[Auto]	
PCIe_6 Max Link Speed	[Auto]	
M2_0 I/O ROM	[Enabled]	
M2_0 Lanes	[Auto]	
M2_0 Max Link Speed	[Auto]	
M2_1 I/O ROM	[Enabled]	
M2_1 Lanes	[Auto]	
M2_1 Max Link Speed	[Auto]	
Onboard LAN3 Controller	[Enabled]	
Onboard LAN4 Controller	[Enabled]	
Onboard LAN1 I/O ROM	[Enabled]	
Onboard LAN2 I/O ROM	[Enabled]	++: Select Screen t1: Select Item K/M: Scroll Help Area Up/Down. Enter: Select +/-: Change Opt. F1: General Help F3: Previous Values F9: Optimized Defaults F10: Save & Exit ESC: Exit
Onboard LAN3 I/O ROM	[Enabled]	
Onboard LAN4 I/O ROM	[Enabled]	
PCI Devices Common Settings:		
Re-Size BAR Support	[Disabled]	
SR-IOV Support	[Enabled]	
Version 2.22.1294 Copyright (C) 2024 AMI		

Parameter	Description
PCI Bus Driver Version	Displays the PCI Bus Driver version information.
SLOT# I/O ROM ^(Note1)	When enabled, this setting will initialize the device expansion ROM for the related PCI-E slot. Options available: Enabled, Disabled. Default setting is Enabled .
SLOT# Lanes ^(Note1)	Change the PCIe lanes. Default setting is Auto .
SLOT# Max Link Speed ^(Note1)	Configure PCIe max link speed. Options available: Auto, Gen1, Gen2, Gen3, Gen4, Gen5. Default setting is Auto .
M2M I/O ROM ^(Note2)	Enable/Disable M2M devices, and initializes device expansion ROM. Options available: Enabled, Disabled. Default setting is Enabled .
M2M Lanes ^(Note2)	Change the M2M PCIe lanes. Options available: Auto, x4, x2x2. Default setting is Auto .
M2M Max Link Speed ^(Note2)	Configure M2M max link speed. Options available: Auto, Gen1, Gen2, Gen3, Gen4, Gen5. Default setting is Auto .
PCI Devices Common Settings	
Above 4G Decoding	Enable/Disable memory mapped I/O to 4GB or greater address space (Above 4G Decoding). Options available: Enabled, Disabled. Default setting is Enabled .
SR-IOV Support	If the system has SR-IOV capable PCIe devices, this item Enable/Disable Single Root IO Virtualization Support. Options available: Enabled, Disabled. Default setting is Enabled .

(Note1) This section is dependent on the available PCIe Slot.

(Note2) This section is dependent on the available M.2 Slot.

5-2-5 USB Configuration



Parameter	Description
USB Configuration	
USB Devices:	Displays the USB devices connected to the system.
XHCI Hand-off	Enable/Disable the XHCI (USB 3.0) Hand-off support. Options available: Enabled, Disabled. Default setting is Enabled .
USB Mass Storage Driver Support ^(Note)	Enable/Disable the USB Mass Storage Driver Support. Options available: Enabled, Disabled. Default setting is Enabled .
Port 60/64 Emulation	Enables the I/O port 60h/64h emulation support. This should be enabled for the complete USB Keyboard Legacy support for non-USB aware OSes. Options available: Enabled, Disabled. Default setting is Enabled .

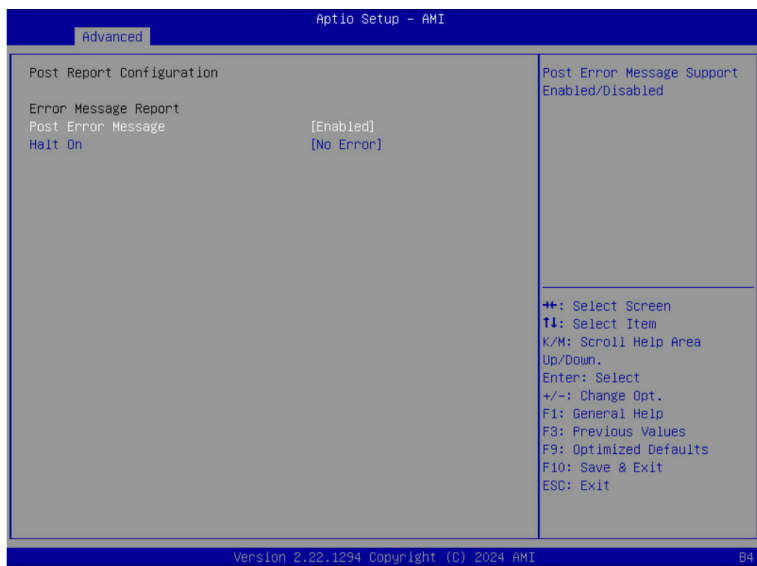
(Note) This item is present only if you attach USB devices.

5-2-6 Network Stack Configuration



Parameter	Description
Network Stack	Enable/Disable the UEFI network stack. Options available: Enabled, Disabled. Default setting is Enabled .
Ipv4 PXE Support	Enable/Disable the Ipv4 PXE feature. Options available: Enabled, Disabled. Default setting is Enabled .
Ipv4 HTTP Support	Enable/Disable the Ipv4 HTTP feature. Options available: Enabled, Disabled. Default setting is Disabled .
Ipv6 PXE Support	Enable/Disable the Ipv6 PXE feature. Options available: Enabled, Disabled. Default setting is Disabled .
Ipv6 HTTP Support	Enable/Disable the Ipv6 HTTP feature. Options available: Enabled, Disabled. Default setting is Disabled .
PXE boot wait time	Wait time in seconds to press ESC key to abort the PXE boot. Press the <+> / <-> keys to increase or decrease the desired values.
Media detect count	Number of times the presence of media will be checked. Press the <+> / <-> keys to increase or decrease the desired values.

5-2-7 Post Report Configuration



Parameter	Description
Post Report Configuration	
Error Message Report	
Post Error Message	Enable/Disable the POST Error Message support. Options available: Enabled, Disabled. Default setting is Enabled .
Halt On	Options available: No Error, All Error. Default setting is No Error .

5-2-8 KMIP Server Configuration

Advanced

Aptio Setup - AMI

KMIP Server IP address		Enter IP4 address in dotted-decimal notation Example: 192.168.10.12
KMIP TCP Port number	5696	
Time Zone	[GMT +8]	
Client Credentials		
[Enabled]		
Client UserName		
Client Password		
KMS TLS Certificate Size		
▶ CA Certificate		0
▶ Client Certificate		0
▶ Client Private Key		0

↔: Select Screen

T: Select Item

K/M: Scroll Help Area

Up/Down.

Enter: Select

+/-: Change Opt.

F1: General Help

F3: Previous Values

F9: Optimized Defaults

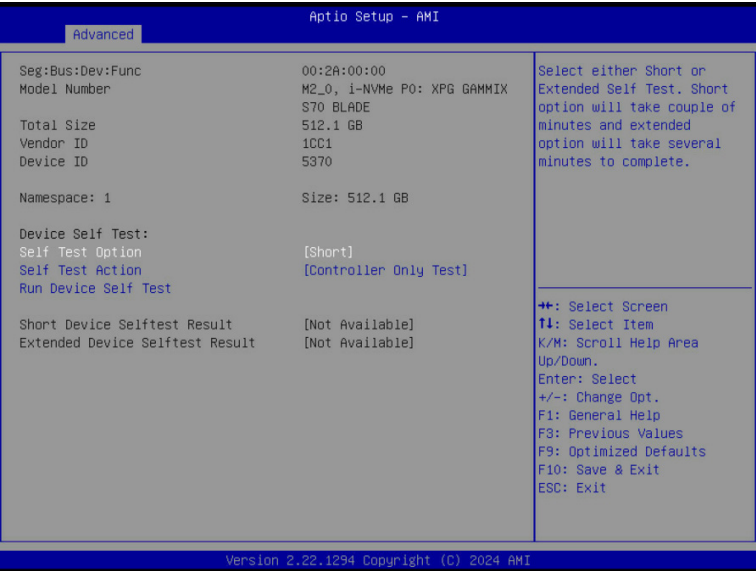
F10: Save & Exit

ESC: Exit

Version 2.22.1294 Copyright (C) 2024 AMI

Parameter	Description
KMIP Server IP address	
KMIP TCP Port Number	
Time Zone	Enter the correct timem zone for this server. Default setting is GMT+8 .
Client Credentials	Use User and password credentials to authenticate the Client. Options available: Enabled, Disabled, Clear. Default setting is Enabled .
Client UserName	Enter Client identify: UserName. Name Length: 0-63 characters.
Client Password	Enter Client identify: Password. Password Length: 0-31 characters.
KMS TLC Certificate / Size	
CA Certificate	Enroll factory defaults or load the KMS TLS certificates from the file.
Client Certificate	Enroll factory defaults or load the KMS TLS certificates from the file.
Client Private Key	Enroll factory defaults or load the KMS TLS certificates from the file.

5-2-9 NVMe Configuration



Parameter	Description
NVMe Configuration	Displays the NVMe devices connected to the system.

5-2-10 Chipset Configuration



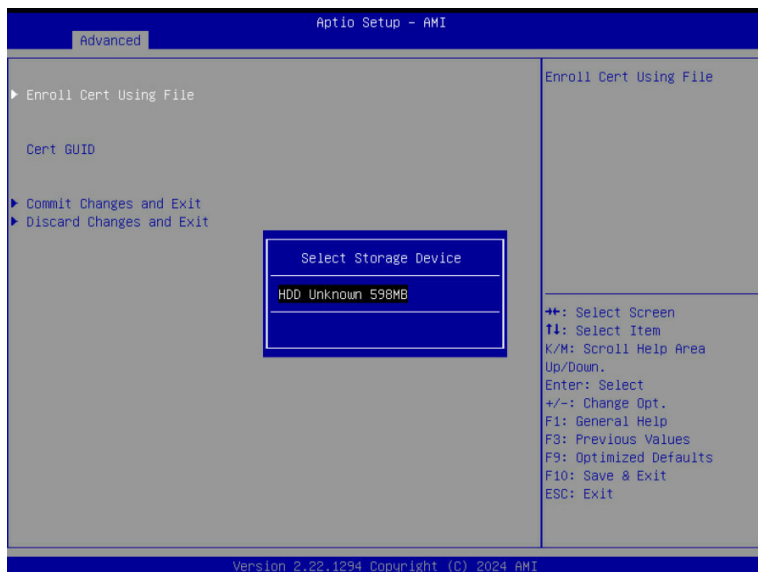
Parameter	Description
Restore on AC Power Loss ^(Note)	Defines the power state to resume to after a system shutdown that is due to an interruption in AC power. When set to Last State, the system will return to the active power state prior to shutdown. When set to Power Off, the system remains off after power shutdown. Options available: Last State, Power Off, Power On, Unspecified. The default setting depends on the BMC setting.
P2P Bridge IO Size	Specifies P2P Bridge IO aligned to the size. Options available: 0x100, 0x150, 0x1000. Default setting is 0x1000 .
SATA HDD Security Frozen	Enable/Disable this item to send freeze lock command to SATA HDD. Options available: Enabled, Disabled. Default setting is Enabled .
NVMe SSD Security Frozen	Attempt to send freeze lock command to NVMe SSDs during boot. Options available: Enabled, Disabled. Default setting is Enabled .
NVMe OPROM Select	Options available: BIOS Build-In, NVMe Device, Disabled. Default setting is BIOS Build-In .
NVMe LED Control	Enable/Disable allow user control NVMe LED. It only available the NVMe device direct connect to CPU. Options available: Disable, Enable. Default setting is Disable .

(Note) When the power policy is controlled by BMC, please wait for 15-20 seconds for BMC to save the last power state.

Parameter	Description
Chassis Opened Warning	Enable/Disable the chassis intrusion alert function. Options available: Enabled, Disabled, Clear. Default setting is Disabled .

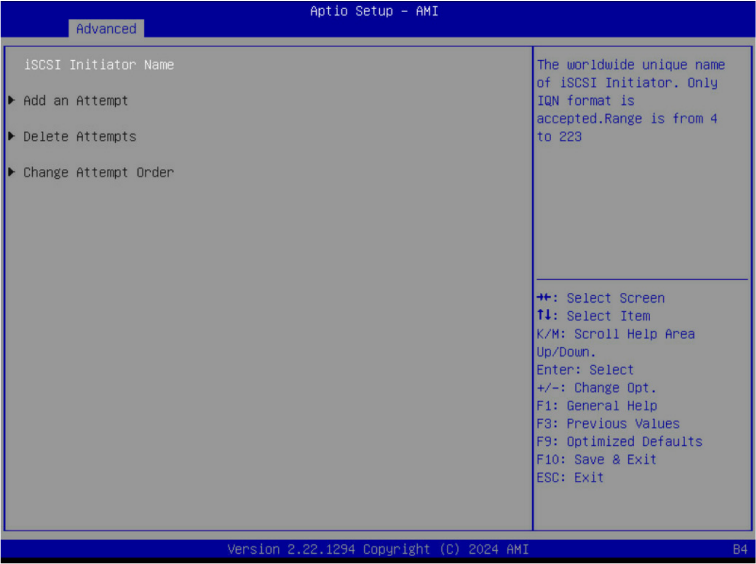
5-2-11 Tls Auth Configuration





Parameter	Description
Server CA Configuration	<p>Press [Enter] for configuration of advanced items.</p> <ul style="list-style-type: none"> ◆ Enroll Cert <ul style="list-style-type: none"> – Press [Enter] to enroll a certificate <ul style="list-style-type: none"> • Enroll Cert Using File • Cert GUID <p>Input digit character in 1111111-2222-3333-4444-1234567890ab format.</p> – Commit Changes and Exit – Discard Changes and Exit ◆ Delete Cert
Client Cert Configuration	Press [Enter] for configuration of advanced items.

5-2-12 iSCSI Configuration



Parameter	Description
Attempt Priority	<p>Press [Enter] configure advanced items.</p> <ul style="list-style-type: none">Attempt Priority<ul style="list-style-type: none">Use arrow keys to select the attempt, then press +/- keys to move the attempt up/down in the attempt order list.Commit Changes and Exit
Host iSCSI Configuration	<p>Press [Enter] to configure advanced items.</p> <ul style="list-style-type: none">iSCSI Initiator Name<ul style="list-style-type: none">Only IQN format is accepted. Range: from 4 to 223Add an AttemptDelete AttemptsChange Attempt Order

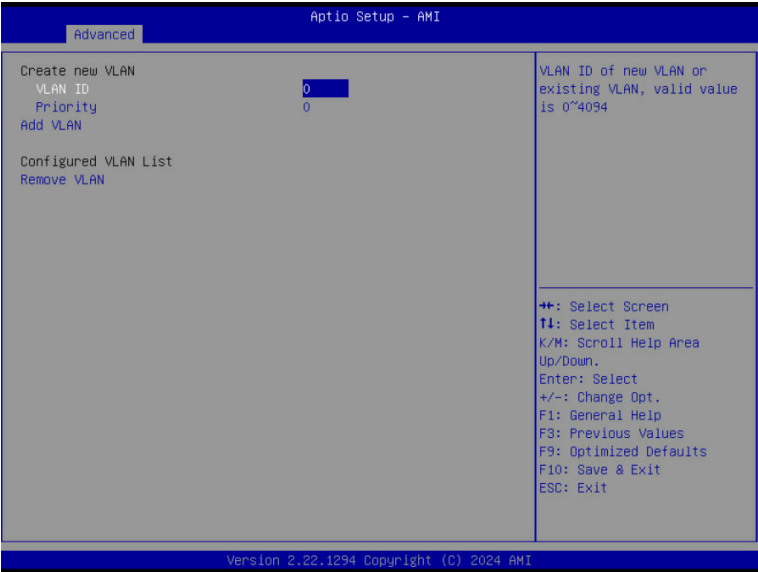
5-2-13 Intel(R) I210 Gigabit Network Connection

Aptio Setup - AMI		
Advanced		
▶ NIC Configuration		Click to configure the network device port.
Blink LEDs	0	
UEFI Driver	Intel(R) PRO/1000 8.5.21 PCI-E	
Adapter PBA	140724-006	
Device Name	Intel(R) I210 Gigabit Network Connection	
Chip Type	Intel i210	
PCI Device ID	1533	
PCI Address	5F:00:00	
Link Status	[Connected]	↔: Select Screen ↑↓: Select Item K/M: Scroll Help Area Up/Down. Enter: Select +/-: Change Opt. F1: General Help F3: Previous Values F9: Optimized Defaults F10: Save & Exit ESC: Exit
MAC Address	10:FF:E0:09:15:18	
Virtual MAC Address	00:00:00:00:00:00	
Version 2.22.1294 Copyright (C) 2024 AMI		

Aptio Setup - AMI		
Advanced		
Link Speed	[Auto Negotiated]	Specifies the port speed used for the selected boot protocol.
Wake On LAN	[Enabled]	
		↔: Select Screen ↑↓: Select Item K/M: Scroll Help Area Up/Down. Enter: Select +/-: Change Opt. F1: General Help F3: Previous Values F9: Optimized Defaults F10: Save & Exit ESC: Exit
Version 2.22.1294 Copyright (C) 2024 AMI		

Parameter	Description
NIC Configuration	<p>Press [Enter] to configure advanced items.</p> <ul style="list-style-type: none"> ◆ Link Speed <ul style="list-style-type: none"> – Default setting is Auto Negotiated. ◆ Wake On LAN <ul style="list-style-type: none"> – Enables power on of the system via LAN. Note that configuring Wake on LAN in the operating system does not change the value of this setting, but does override the behavior of Wake on LAN in OS controlled power states. – Options available: Enabled, Disabled. Default setting is Enabled. ◆ LLDP Agent <ul style="list-style-type: none"> – Enable/Disable firmware's LLDP Agent. – Options available: Enabled, Disabled. Default setting is Enabled
Blink LEDs	<p>Identifies the physical network port by blinking the associated LED. Press the numeric keys to adjust desired values (up to 15 seconds).</p>
UEFI Driver	Displays the technical specifications for the Network Interface Controller.
Adapter PBA	Displays the technical specifications for the Network Interface Controller.
Device Name	Displays the technical specifications for the Network Interface Controller.
Chip Type	Displays the technical specifications for the Network Interface Controller.
PCI Device ID	Displays the technical specifications for the Network Interface Controller.
PCI Address	Displays the technical specifications for the Network Interface Controller.
Link Status	Displays the technical specifications for the Network Interface Controller.
MAC Address	Displays the technical specifications for the Network Interface Controller.
Virtual MAC Address	Displays the technical specifications for the Network Interface Controller.

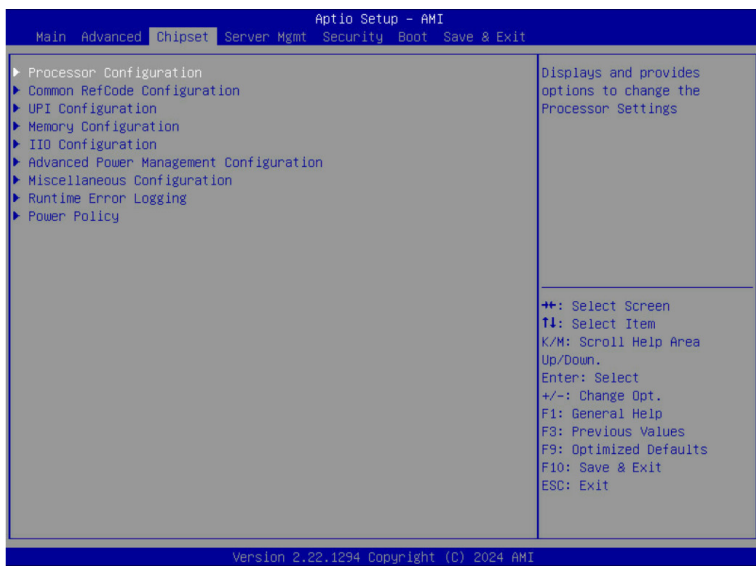
5-2-14 VLAN Configuration



Parameter	Description
Enter Configuration Menu	<p>Press [Enter] to configure advanced items.</p> <ul style="list-style-type: none">♦ Create new VLAN♦ VLAN ID<ul style="list-style-type: none">– Sets VLAN ID for a new VLAN or an existing VLAN.– Press the <+> / <-> keys to increase or decrease the desired values.– The valid range is from 0 to 4094.♦ Priority<ul style="list-style-type: none">– Sets 802.1Q Priority for a new VLAN or an existing VLAN.– Press the <+> / <-> keys to increase or decrease the desired values.– The valid range is from 0 to 7.♦ Add VLAN<ul style="list-style-type: none">– Press [Enter] to create a new VLAN or update an existing VLAN.♦ Configured VLAN List♦ Remove VLAN<ul style="list-style-type: none">– Press [Enter] to remove an existing VLAN.

5-3 Chipset Menu

Chipset Setup menu displays submenu options for configuring the function of Platform Controller Hub(PCH).
Select a submenu item, then press <Enter> to access the related submenu screen.



5-3-1 Processor Configuration

Chipset

Aptio Setup - AMI

Processor Configuration

▶ Per-Socket Configuration

Processor Socket	Socket 0	Socket 1
Processor ID	000A06F3*	000A06F3
Processor Frequency	2.400GHz	2.400GHz
Processor Max Ratio	18H	18H
Processor Min Ratio	08H	08H
Microcode Revision	030001B3	030001B3
L1 Cache RAM(Per Core)	96KB	96KB
L2 Cache RAM(Per Package)	65536KB	65536KB
L3 Cache RAM(Per Package)	98304KB	98304KB
Processor 0 Version	Intel(R) Xeon(R) 6710E	
Processor 1 Version	Intel(R) Xeon(R) 6710E	

Hardware Prefetcher

[Enable]

Adjacent Cache Prefetch

[Enable]

DCU Streamer Prefetcher

[Disable]

DCU IP Prefetcher

[Enable]

L1 Next Page Prefetcher

[Disable]

Enable Intel(R) TXT

[Disable]

VMX

[Enable]

Enable SMX

[Disable]

Change Per-Socket Settings

⚡: Select Screen

T1: Select Item

K/M: Scroll Help Area Up/Down.

Enter: Select

+/-: Change Opt.

F1: General Help

F3: Previous Values

F9: Optimized Defaults

F10: Save & Exit

ESC: Exit

Version 2.22.1294 Copyright (C) 2024 AMI

Chipset

Aptio Setup - AMI

Processor Reserved Memory [Outputs]

PRMRR Size per domain	16 MiB
PRM Size per socket	16 MiB
PRM Size per system	16 MiB

Software Guard Extension (SGX) [Outputs]

SGX activation state	Deactivated
SGX error code [HEX]	16

Software Guard Extension (SGX) [Inputs]

SGX Factory Reset	[Disabled]
SW Guard Extensions (SGX)	[Disabled]
SGX Package Info In-Band Access	[Disabled]
SGX PRMRR Size Requested	[Auto]

In Field Scan (IFS)

▶ In Field Scan (IFS)

Processor Dfx Configuration

Displays and provides option to change the Processor Dfx Settings

⚡: Select Screen

T1: Select Item

K/M: Scroll Help Area Up/Down.

Enter: Select

+/-: Change Opt.

F1: General Help

F3: Previous Values

F9: Optimized Defaults

F10: Save & Exit

ESC: Exit

Version 2.22.1294 Copyright (C) 2024 AMI

Parameter	Description
Processor Configuration	
Pre-Socket Configuration	<p>Press [Enter] to configure advanced items.</p> <ul style="list-style-type: none"> • CPU Socket 0 Configuration <ul style="list-style-type: none"> – Core Disable Bitmap(Hex) <ul style="list-style-type: none"> • Number of Cores to enable. 0 means all cores. FFFFFFFF means to disable all cores. The maximum value depends on the number of CPUs available. Press the numeric keys to adjust desired values.
Processor Socket / Processor ID / Processor Die Type / Processor Frequency / Processor Max Ratio / Processor Min Ratio / Microcode Revision / L1 Cache RAM(Per Core) / L2 Cache RAM(Per Core) / L3 Cache RAM(Per Package) / Processor # Version	Displays the technical specifications for the installed processor(s).
Enable LP [Global]	<p>Enables Logical processor (Software Method to Enable/Disable Logical Processor threads).</p> <p>Options available: ALL LPs, Single LP. Default setting is ALL LPs.</p>
Hardware Prefetcher	<p>Select whether to enable the speculative prefetch unit of the processor.</p> <p>Options available: Enable, Disable. Default setting is Enable.</p>
L2 RF0 Prefetch Disable	Options available: Enable, Disable. Default setting is Disable .
Adjacent Cache Prefetch	<p>When enabled, cache lines are fetched in pairs. When disabled, only the required cache line is fetched.</p> <p>Options available: Enable, Disable. Default setting is Enable.</p>
DCU Streamer Prefetcher	<p>Enable/Disable DCU streamer prefetcher.</p> <p>Options available: Enable, Disable. Default setting is Enable.</p>
DCU IP Prefetcher	<p>Enable/Disable DCU IP Prefetcher.</p> <p>Options available: Enable, Disable. Default setting is Enable.</p>
Extended APIC	<p>Enable/Disable extended APIC support. Note: The VT-d will be enabled automatically when x2APIC is enabled.</p> <p>Options available: Enable, Disable. Default setting is Enable.</p>
Enable Intel(R) TXT	<p>Enable/Disable the Intel Trusted Execution Technology support function.</p> <p>Options available: Enable, Disable. Default setting is Disable.</p>
VMX	<p>Enable/Disable the Vanderpool Technology. This will take effect after rebooting the system.</p> <p>Options available: Enable, Disable. Default setting is Enable.</p>
Enable SMX	<p>Enable/Disable the Safer Mode Extensions (SMX) support function.</p> <p>Options available: Enable, Disable. Default setting is Disable.</p>
AES-NI	<p>Enable/Disable the AES-NI support.</p> <p>Options available: Enable, Disable. Default setting is Enable.</p>
Debug Consent	Options available: Enable, Disable. Default setting is Disable .

Parameter	Description
Memory Encryption (TME) ^(Note)	Enable/Disable memory encryption (TME). Options available: Enabled, Disabled. Default setting is Disabled .
Total Memory Encryption Multi-Tenant (TME-MT)	Options available: Enabled, Disabled. Default setting is Disabled .
Processor CFR Configuration	<p>Press [Enter] to configure advanced items.</p> <ul style="list-style-type: none"> ◆ Provision S3M CFR <ul style="list-style-type: none"> – Options available: Disable, Enable. Default setting is Enable. ◆ Manual Commit S3M FW CFR <ul style="list-style-type: none"> – Options available: Disable, Enable, Auto. Default setting is Auto. ◆ Provision PUcode CFR <ul style="list-style-type: none"> – Options available: Disable, Enable. Default setting is Enable. ◆ Manual Commit PUcode CFR <ul style="list-style-type: none"> – Options available: Enable, Disable, Auto. Default setting is Auto. ◆ Socket0 CFR Revision Info <ul style="list-style-type: none"> – Displays CFR Revision information of the socket.

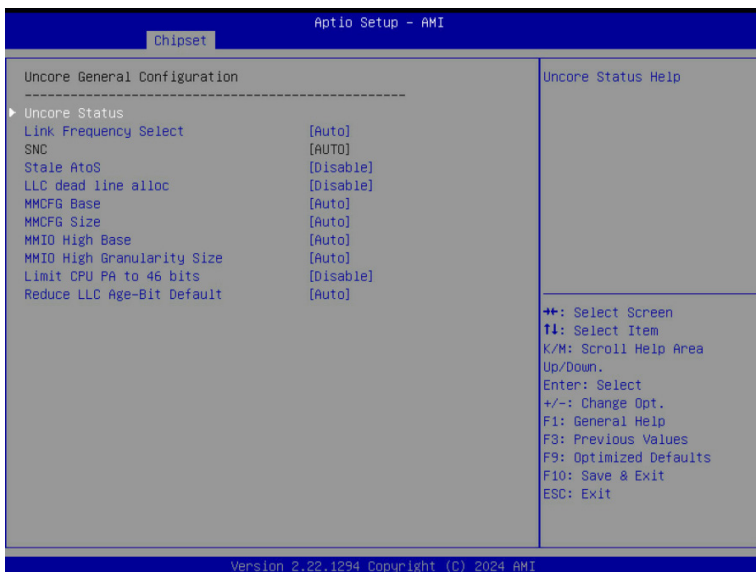
(Note) Advanced items prompt when this item is defined.

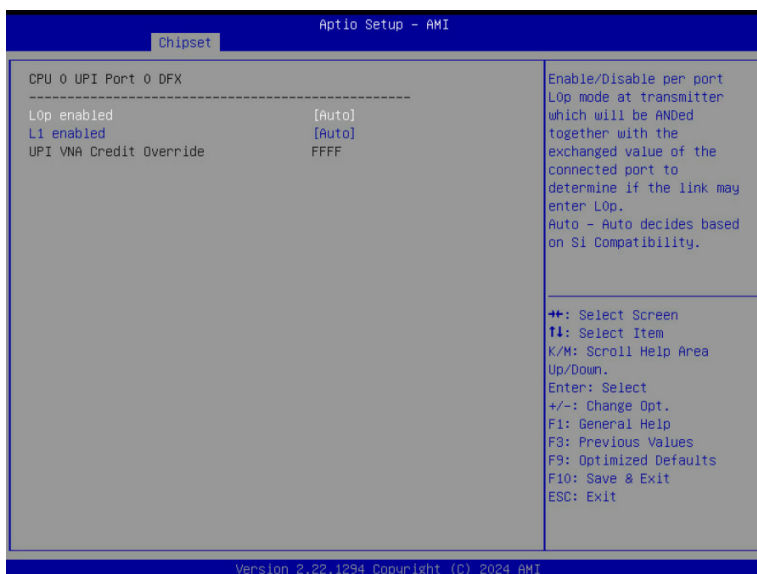
5-3-2 Common RefCode Configuration



Parameter	Description
Common RefCode Configuration	
Numa	Default setting is Enable .
Virtual Numa	Divide physical NUMA nodes into evenly sized virtual NUMA nodes in ACPI table. This may improve Windows performance on CPUs with more than 64 logical processors. Options available: Enable, Disable. Default setting is Disable .

5-3-3 UPI Configuration

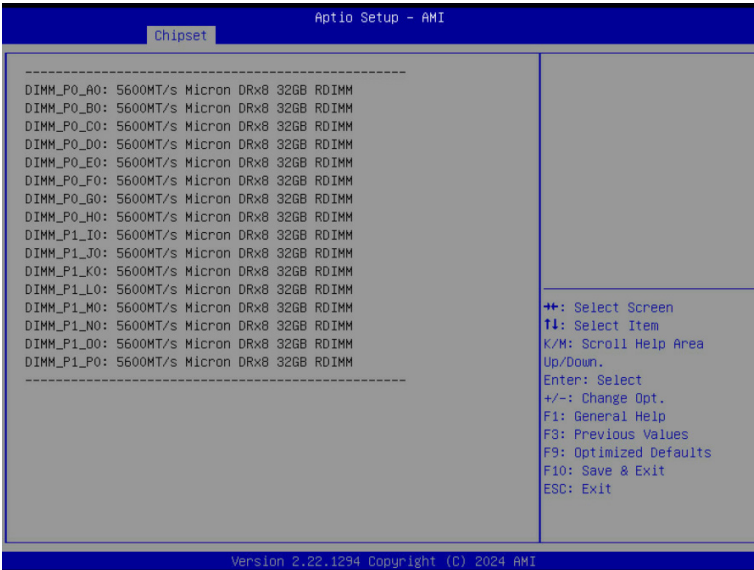
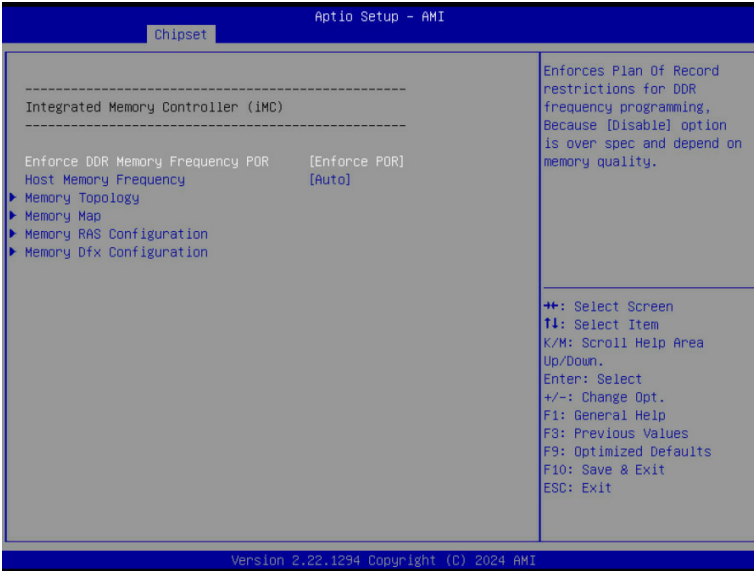


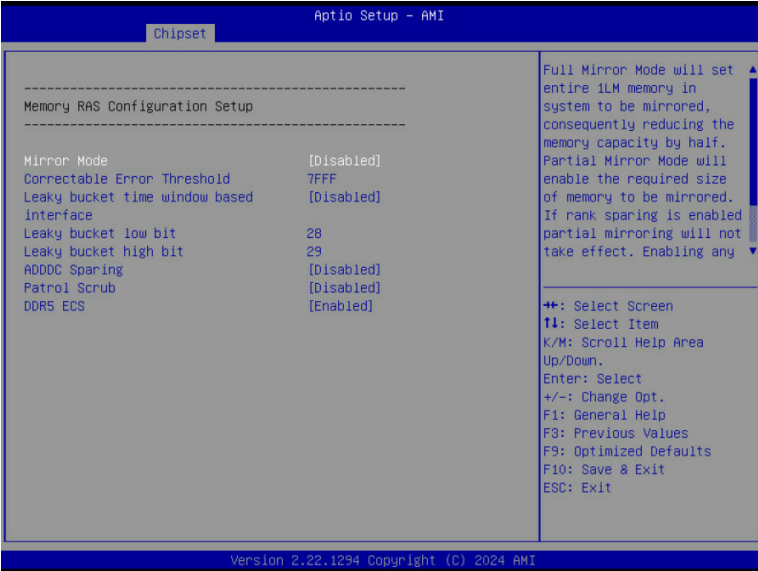
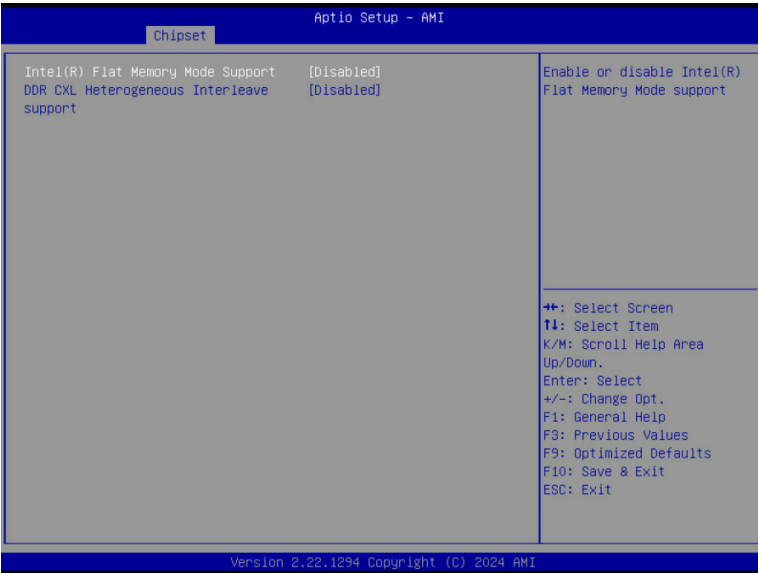


Parameter	Description
UPI General Configuration	Press [Enter] to configure advanced items. <ul style="list-style-type: none"> UPI Status <ul style="list-style-type: none"> Press [Enter] to view the Uncore status. Link Frequency Select <ul style="list-style-type: none"> Selects the UPI link frequency. Options available: 12.8GT/s, 14.4GT/s, 16.0GT/s, Auto, Use Per Link Setting. Default setting is Auto. SNC <ul style="list-style-type: none"> Enable/Disable Sub NUMA Cluster function. Options available: Auto, Disable, Enable SNC2 (2-clusters), Enable SNC4 (4-clusters). Default setting is Auto. Stale AtoS <ul style="list-style-type: none"> Enable/Disable Stale A to S directory optimization. Options available: Disable, Enable, Auto. Default setting is Auto. LLC dead line alloc <ul style="list-style-type: none"> Enable/Disable fill dead lines in LLC. Options available: Disable, Enable, Auto. Default setting is Enable. MMCFG Size <ul style="list-style-type: none"> Options available: 64M, 128M, 256M, 512M, 1G, 2G, Auto. Default setting is 512M. MMIO High Base <ul style="list-style-type: none"> Options available: 56T, 40T, 32T, 24T, 16T, 4T, 2T, 1T, 512G, 3584T. Default setting is 4T.

Parameter	Description
UPI General Configuration (continued)	<ul style="list-style-type: none"> ◆ MMIO High Granularity Size <ul style="list-style-type: none"> – Selects the allocation size used to assign mmioh resources. – Options available: 1G, 4G, 16G, 64G, 256G, 1024G. Default setting is 64G. ◆ Limit CPU PA to 46 bits <ul style="list-style-type: none"> – Options available: Disable, Enable. Default setting is Disable.

5-3-4 Memory Configuration





Aptio Setup - AMI		
Chipset		
Memory Dfx Configuration		Valid values: 0-7
DCA RX DFE Gain Coefficient	[Disabled]	
DCS RX DFE Gain Coefficient	[Disabled]	
CXL Persistent Mem Partition Size	0	
DFE READ Loopcount	64	
Custom Refresh Enable	[Disable]	
2DPC 5600 DIMM Frequency Limit	[Auto]	
Reduce IMAX by serializing MRC training	[Auto]	
Enable/Disable RxRetraining for all CPUs	[Auto]	
Enable/Disable TxRetraining for all CPUs	[Auto]	
Turnaround Calculation Debug Feature	[Disable]	
DB Rx DQ CTLE Enable	[Disable]	
DB Rx DQ CTLE Setting	0	
DCX Duty Cycle PIE Offset Range	5	
Dummy Read Enable(1)/Disable (0)	[Enable]	
MCR PDA Enum Errata	[Enable]	
Enable(1)/Disable (0)		
MCR PDA Enum Errata TX DQS PIE OFFSET	70	
Pmic Secure Mode	[Auto]	
		▲ Select Screen F1: Select Item K/M: Scroll Help Area Up/Down. Enter: Select +/-: Change Opt. F1: General Help F3: Previous Values F9: Optimized Defaults F10: Save & Exit ESC: Exit
Version 2.22.1294 Copyright (C) 2024 AMI		

Aptio Setup - AMI		
Chipset		
DCS DFE Tap 4 Coefficient	0	
RX DFE Gain Control	[Disable]	
Back to Back RMT Loop Count	1	
RMT Lane Print Per Pseudo Channel	[Disable]	
Db Mdg Vref overrides	[Disable]	
DFX Override To Skip DutyCycle At End Of WriteDqDqsPreDfe2DCentering	[Disable]	
DFX Override To Skip DutyCycle At End Of	[Enable]	
WriteDqDqsPostDfe2DCentering		
Mem IO Health CMD check	[Enable]	
TA optimal value skip programming	[Disable]	
Dfx HR CRC feature Control	[Disable]	
Directory Mode Override	[Disable]	
Pctle Enable	[Disable]	
Pctle Res1 Ctl1	[3]	
Pctle Cap1 Ctl1	[125]	
Cgcs R100	[5]	
Enable Rx Jitter Cancellation Training for RDIMM	[Auto]	
Enable Dram Duty Cycle Adjust	[Auto]	
Per-bit Training for RDIMM		
Sense Amp DQ Delay Optimization pull-back constant table option	[Option 1]	
Turnaround Checkpoint	[Auto]	
		▲ Enable Turnaround Checkpoint ▲ Select Screen F1: Select Item K/M: Scroll Help Area Up/Down. Enter: Select +/-: Change Opt. F1: General Help F3: Previous Values F9: Optimized Defaults F10: Save & Exit ESC: Exit
Version 2.22.1294 Copyright (C) 2024 AMI		

Parameter	Description
Integrated Memory Controller (iMC)	
Enforce DDR Memory Frequency POR	When set to Enable, the system enforces Plan Of Record restrictions for DDR frequency programming. Options available: POR, Disable. Default setting is POR .
Memory Frequency	Configures the maximum memory frequency. If Enforce POR is disabled, user will be able to run at higher frequencies than the memory support (limited by processor support). Default setting is Auto .
Enable ADR	Enables the detecting and enabling of ADR (Asynchronous DRAM Refresh) function. Options available: Enable, Disable. Default setting is Enable .
Legacy ADR Mode	Enable/Disable the Legacy ADR Mode. Options available: Enable, Disable, Auto. Default setting is Auto .
Minimum System Memory Size	Configures the minimum memory size. Options available: 2GB, 4GB, 6GB, 8GB. Default setting is 2GB .
ADR Data Save Mode	Specifies the Data Save Mode for ADR. Batterybacked or Type 01 NVDIMM. Options available: Disable, Batterybacked DIMMs, NVDIMMs, Copy to Flash. Default setting is NVDIMMs .
Assert ADR on Reset	Enable/Disable Assert ADR on Reset. Options available: Enabled, Disabled. Default setting is Disabled .

Parameter	Description
Assert ADR on S5	Enable/Disable Assert ADR on S5. Options available: Enabled, Disabled. Default setting is Disabled .
Get Memory Timing	Auto is the detected SPD value and use it, otherwise use BIOS Build-in. Options available: Auto, BIOS Build-in. Default setting is BIOS Build-in .
Memory Topology	Press [Enter] to view memory topology with DIMM population information.
Memory Map ^(Note1)	Press [Enter] to configure advanced items. <ul style="list-style-type: none"> ♦ Volatile Memory Mode <ul style="list-style-type: none"> – Selects 1LM or 2LM mode for volatile memory. – Options available: 1LM, 2LM. Default setting is 2LM.
Memory RAS Configuration	Press [Enter] to configure advanced items. <ul style="list-style-type: none"> ♦ Mirror Mode^(Note2) <ul style="list-style-type: none"> – Mirror Mode will set entire 1LM memory in system to be mirrored, consequently reducing the memory capacity by half. Enables the Mirror Mode will disable the XPT Prefetch. – Options available: Disabled, Full Mirror Mode, Partial Mirror Mode. Default setting is Disabled. ♦ Partial Mirror 1 Size (GB) <ul style="list-style-type: none"> – Selects multiplier of 1GB for the size of the SAD to be created. ♦ Correctable Error Threshold <ul style="list-style-type: none"> – Correctable Error Threshold (0x01-0x7fff) used for sparing, and leaky bucket. – Press the <+> / <-> keys to increase or decrease the desired values. ♦ Trigger SW Error Threshold^(Note2) <ul style="list-style-type: none"> – Enable/Disable Sparing trigger SW Error Match Threshold. – Options available: Disabled, Enabled. Default setting is Disabled. ♦ SW Per Bank Threshold <ul style="list-style-type: none"> – SW Per Bank Threshold (1-0x7FFF) used for DDR bank level error. – Press the <+> / <-> keys to increase or decrease the desired values. ♦ SW Correctable Error Time Window <ul style="list-style-type: none"> – SW Correctable Error time window based interface in hour (0-24). – Press the <+> / <-> keys to increase or decrease the desired values. ♦ Leaky bucket time window based interface^(Note2) <ul style="list-style-type: none"> – Enable/Disable leaky bucket time window based interface. – Options available: Disabled, Enabled. Default setting is Disabled.

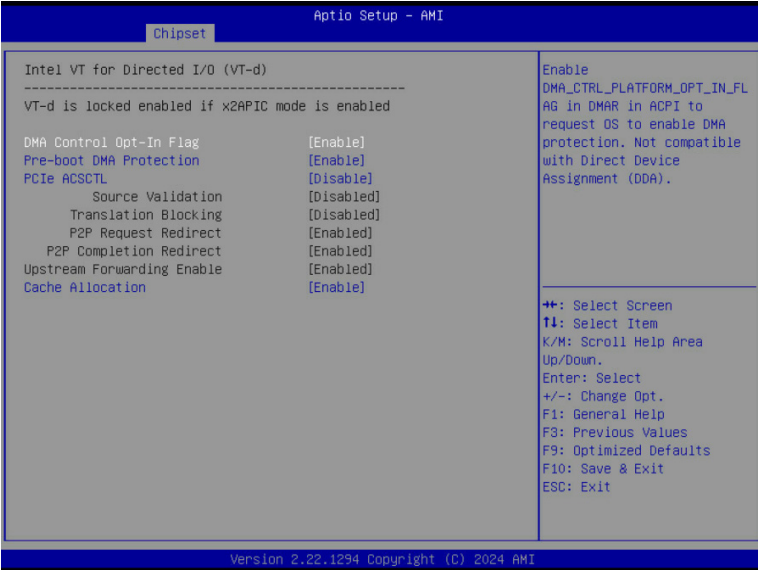
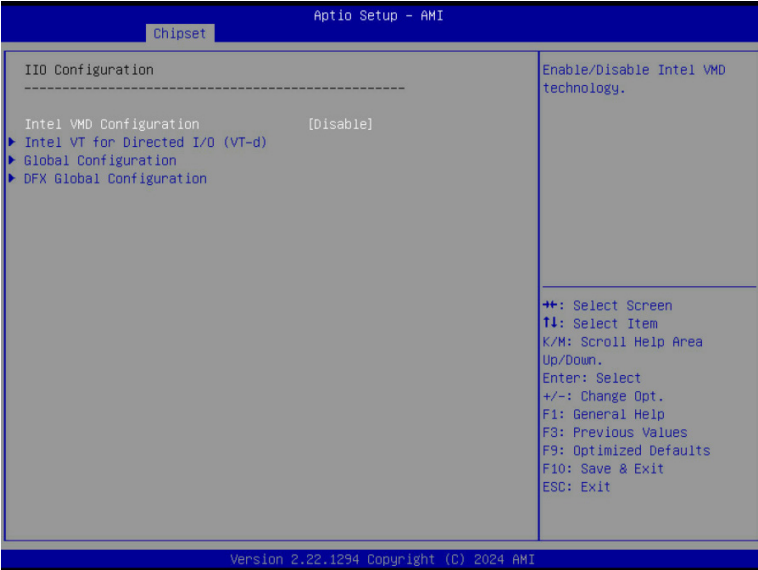
(Note1) Advanced items prompt when HBM CPU is installed.

(Note2) Advanced items prompt when this item is defined.

Parameter	Description
Memory RAS Configuration (continued)	<ul style="list-style-type: none"> ♦ Leaky bucket time window based interface Hour <ul style="list-style-type: none"> – Leaky bucket time window based interface hour used for DDR (0-24). – Press the <+> / <-> keys to increase or decrease the desired values. ♦ Leaky bucket time window based interface Minute <ul style="list-style-type: none"> – Leaky bucket time window based interface minute used for DDR (0-60). – Press the <+> / <-> keys to increase or decrease the desired values. ♦ Leaky bucket low bit <ul style="list-style-type: none"> – Configures leaky bucket low bit (0x1 - 0x29). – Press the <+> / <-> keys to increase or decrease the desired values. ♦ Leaky bucket high bit <ul style="list-style-type: none"> – Configures leaky bucket high bit (0x1 - 0x29). – Press the <+> / <-> keys to increase or decrease the desired values. ♦ ADDDC Sparing^(Note) <ul style="list-style-type: none"> – Enable/Disable ADDDC Sparing. – Options available: Disabled, Enabled. Default setting is Disabled. ♦ Enable ADDDC Error Injection <ul style="list-style-type: none"> – Options available: Disabled, Enabled. Default setting is Enabled. ♦ Patrol Scrub <ul style="list-style-type: none"> – Options available: Disabled, Enable at End of POST. Default setting is Enable at End of POST. ♦ Patrol Scrub Interval <ul style="list-style-type: none"> – Selects the number of hours (1-24) required to complete full scrub. A value of zero means auto. ♦ DDR5 ECS <ul style="list-style-type: none"> – Options available: Disabled, Enabled, Enable ECS with Result Collection. Default setting is Enabled.

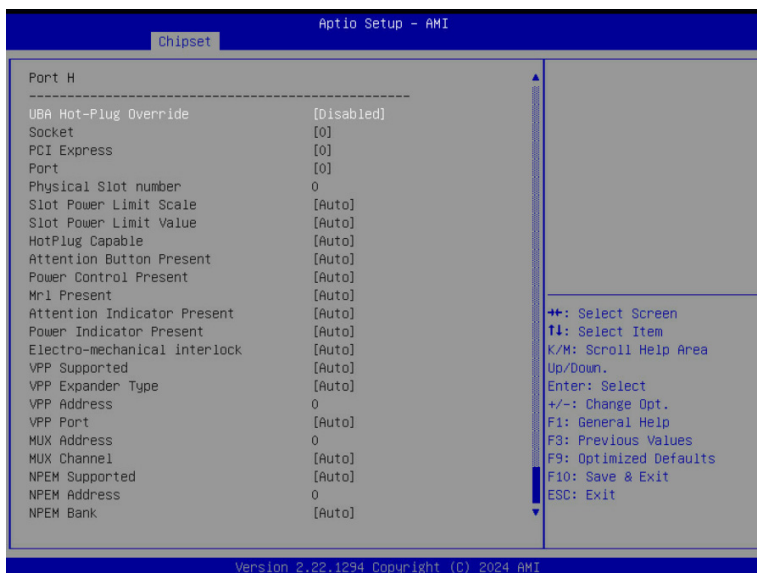
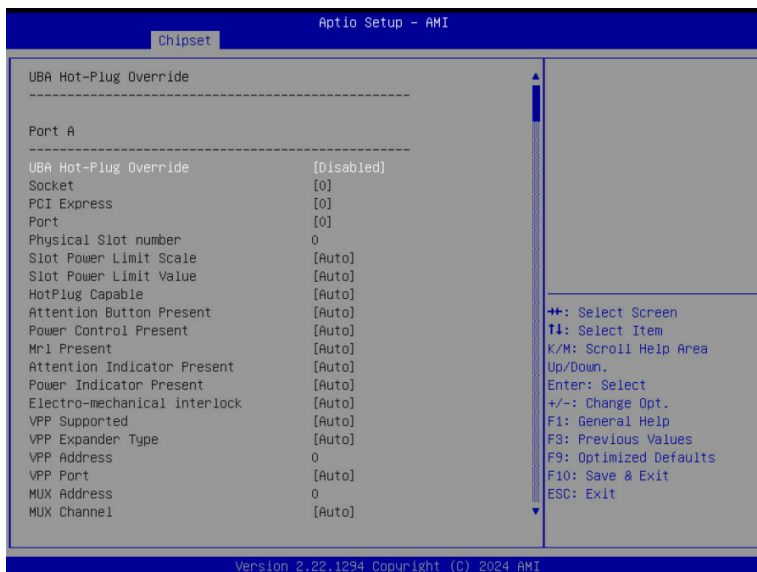
(Note) Advanced items prompt when this item is defined.

5-3-5 IIO Configuration



Aptio Setup - AMI	
Chipset	
Global Configuration	
Max Read Request Size	[4096B]
Relaxed Ordering	[Enable]
This option can set Max Read Request Size in PCI hierarchy. 'Default' keeps hardware default.	
++: Select Screen ↑↓: Select Item K/M: Scroll Help Area Up/Down. Enter: Select +/-: Change Opt. F1: General Help F3: Previous Values F9: Optimized Defaults F10: Save & Exit ESC: Exit	
Version 2.22.1294 Copyright (C) 2024 AMI	

Aptio Setup - AMI	
Chipset	
DFX Global Configuration	
EV DFX Features	[Disable]
Disable BIOS Done	[Disabled]
Link Re-Train	[Disable]
Skip IIO RC flow	[Disable]
Skip PCIe Link Training	[No]
Skip Port Personality Lock	[Disable]
CXL Header Bypass	[Enable]
CXL 1.1 as 2.0 mode	[Disable]
MSE/BME En	[Disable]
LVF Test Card Workarounds	[Disable]
CXL IDE	[Disable]
Trace Hub Allocating Flow	[Enable]
Compliance Mode	[Disable]
Equalization Bypass	[Disable]
► UBA Hot-Plug Override	
Expose IIO DFX devices and other CPU devices like PMON	
++: Select Screen ↑↓: Select Item K/M: Scroll Help Area Up/Down. Enter: Select +/-: Change Opt. F1: General Help F3: Previous Values F9: Optimized Defaults F10: Save & Exit ESC: Exit	
Version 2.22.1294 Copyright (C) 2024 AMI	



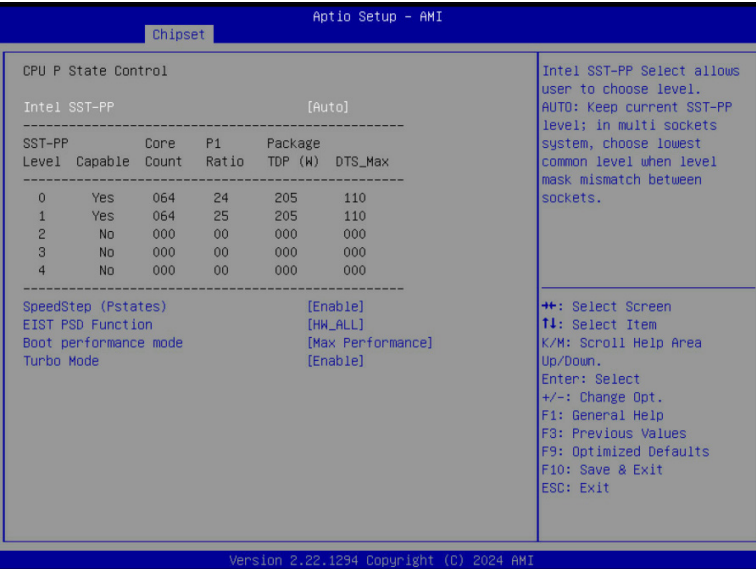
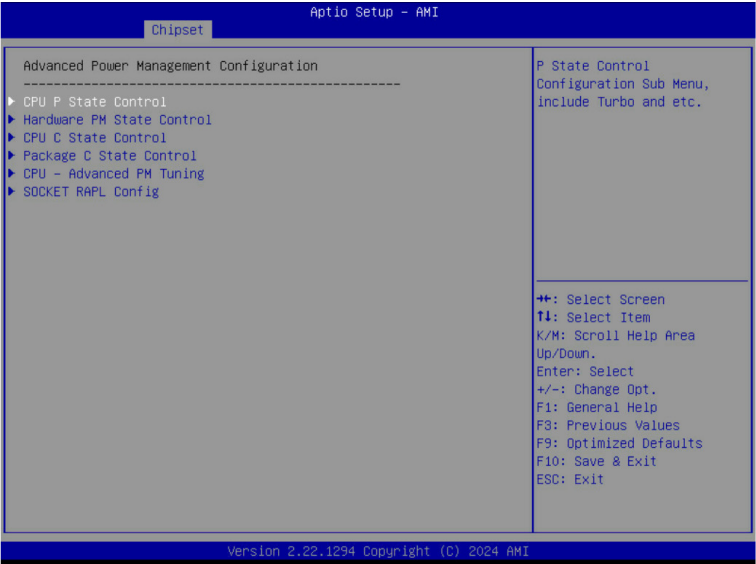
Parameter	Description
I/O Configuration	
Intel® VT for Directed I/O (VT-d)	<p>Press [Enter] to configure advanced items.</p> <ul style="list-style-type: none"> ◆ Intel® VT for Directed I/O <ul style="list-style-type: none"> – Enable/Disable the Intel VT for Directed I/O (VT-d) support function by reporting the I/O device assignment to VMM through DMAR ACPI Tables. – Options available: Enable, Disable. Default setting is Enable. ◆ Cache Allocation <ul style="list-style-type: none"> – Options available: Enable, Disable. Default setting is Enable. ◆ DMA Control Opt-In Flag <ul style="list-style-type: none"> – Enable/Disable DMA_CTRL_PLATFORM_OPT_IN_FLAG in DMAR table in ACPI. Not compatible with Direct Device Assignment (DDA). – Options available: Enable, Disable. Default setting is Disable. ◆ Interrupt Remapping <ul style="list-style-type: none"> – Enable/Disable the interrupt remapping support function. – Options available: Auto, Enable, Disable. Default setting is Auto ◆ x2APIC Opt Out <ul style="list-style-type: none"> – Options available: Enable, Disable. Default setting is Disable. ◆ Pre-boot DMA Protection <ul style="list-style-type: none"> – Options available: Enable, Disable. Default setting is Disable.

Parameter	Description
Intel® VT for Directed I/O (VT-d) (continued)	<ul style="list-style-type: none"> ◆ PCIe ACSCTL <ul style="list-style-type: none"> – Enable/Disable overwrite of PCI Access Control Services Control register in PCI root ports. – Options available: Disable, Enable. Default setting is Disable. ◆ Source Validation^(Note) <ul style="list-style-type: none"> – Options available: Disabled, Enabled. Default setting is Disabled. ◆ Translation Blocking^(Note) <ul style="list-style-type: none"> – Options available: Disabled, Enabled. Default setting is Disabled. ◆ P2P Request Redirect^(Note) <ul style="list-style-type: none"> – Options available: Disabled, Enabled. Default setting is Enabled. ◆ P2P Completion Redirect^(Note) <ul style="list-style-type: none"> – Options available: Disabled, Enabled. Default setting is Enabled. ◆ Upstream Forwarding Enable^(Note) <ul style="list-style-type: none"> – Options available: Disabled, Enabled. Default setting is Enabled.
Intel® VMD technology	<p>Press [Enter] to configure advanced items.</p> <ul style="list-style-type: none"> ◆ Intel® VMD Configuration <ul style="list-style-type: none"> – Enable/Disable Intel® VMD technology. – Options available: Enable, Disable. Default setting is Disable. ◆ Intel® VMD for Non-Hotplug NVMe^(Note1) <ul style="list-style-type: none"> – Enable/Disable Intel® VMD for Non-Hotplug NVMe. – Options available: Enable, Disable. Default setting is Disable.
I/O-PCIe Express Global Options	
PCIe Max Read Request Size	Options available: Auto, 128B, 256B, 512B, 1024B, 2048B, 4096B. Default setting is 4096B .
Pcie Relaxed Ordering	Options available: No, Yes. Default setting is Yes .

(Note) This item is available when **PCIe ACSCTL** is set to **Enable**.

(Note1) This item appears when **Intel® VMD Configuration** is set to **Enable**.

5-3-6 Advanced Power Management Configuration



Aptio Setup - AMI		
Chipset		
Hardware PM State Control		Disable: Hardware chooses a P-state based on OS Request (Legacy P-States) Native Mode:Hardware chooses a P-state based on OS guidance Out of Band Mode:Hardware autonomously chooses a P-state (no OS guidance) NOTE: When HWP is 'Disable' or 'OOB',
Hardware P-States	[Native Mode]	
HardwarePM Interrupt	[Disable]	
		++: Select Screen T4: Select Item K/M: Scroll Help Area Up/Down. Enter: Select +/-: Change Opt. F1: General Help F3: Previous Values F9: Optimized Defaults F10: Save & Exit ESC: Exit
Version 2.22.1294 Copyright (C) 2024 AMI		

Aptio Setup - AMI		
Chipset		
CPU C State Control		Allows Monitor and MWAIT instructions.
Monitor: MWAIT	[Enable]	
ACPI C1 Enumeration	[C1e]	
ACPI C6x Enumeration	[C6S as ACPI C3]	
		++: Select Screen T4: Select Item K/M: Scroll Help Area Up/Down. Enter: Select +/-: Change Opt. F1: General Help F3: Previous Values F9: Optimized Defaults F10: Save & Exit ESC: Exit
Version 2.22.1294 Copyright (C) 2024 AMI		

Aptio Setup - AMI	
Chipset	
Package C State Control	Package C State limit, the state Auto maps is program specific.
Package C State	[C0/C1 state]
++: Select Screen Tl: Select Item K/M: Scroll Help Area Up/Down. Enter: Select +/-: Change Opt. F1: General Help F3: Previous Values F9: Optimized Defaults F10: Save & Exit ESC: Exit	
Version 2.22.1294 Copyright (C) 2024 AMI	

Aptio Setup - AMI	
Chipset	
CPU - Advanced PM Tuning	0: Set dynamic Uncore frequency range from max and min fused values. Otherwise Uncore will run at a constant frequency ratio, the UFS algorithm will be disabled, but physical limits may still reduce frequency.
----- Current Uncore Ratio Range: 08 - 22 -----	
Uncore Freq Ratio: 0	
► Energy Perf BIAS	
++: Select Screen Tl: Select Item K/M: Scroll Help Area Up/Down. Enter: Select +/-: Change Opt. F1: General Help F3: Previous Values F9: Optimized Defaults F10: Save & Exit ESC: Exit	
Version 2.22.1294 Copyright (C) 2024 AMI	

Chipset

Aptio Setup - AMI

Energy Perf BIAS		Options decide who Controls EPB. In OS mode: IA32_ENERGY_PERF_BIAS is used In BIOS mode: ENERGY_PERF_BIAS_CONFIG is used In PECI mode: PCS53 is used
Power Performance Tuning ENERGY_PERF_BIAS_CFG mode	[BIOS Controls EPB] [Performance]	

++: Select Screen
↑↓: Select Item
K/M: Scroll Help Area Up/Down.
Enter: Select
+/-: Change Opt.
F1: General Help
F3: Previous Values
F9: Optimized Defaults
F10: Save & Exit
ESC: Exit

Version 2.22.1294 Copyright (C) 2024 AMI

Chipset

Aptio Setup - AMI

SOCKET RAPL Config		PL1 Power Limit in Watts. The value may vary from 0 to Fused TDP Value. If the value is 0, the Fused TDP value will be programmed. A value greater than Fused TDP value will not be programmed.
PL1 Power Limit	0	
PL1 Time Window	[1]	
PL2 Power Limit	0	
PL2 Time Window	[0.012]	

++: Select Screen
↑↓: Select Item
K/M: Scroll Help Area Up/Down.
Enter: Select
+/-: Change Opt.
F1: General Help
F3: Previous Values
F9: Optimized Defaults
F10: Save & Exit
ESC: Exit

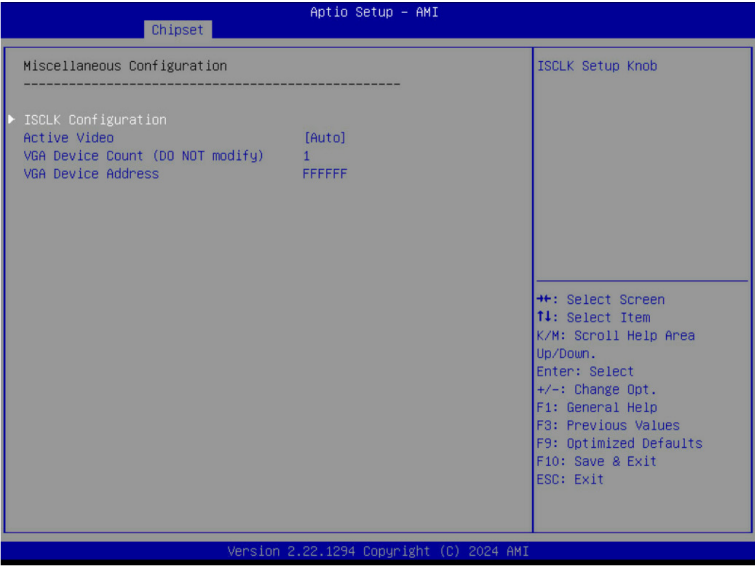
Version 2.22.1294 Copyright (C) 2024 AMI

Parameter	Description
CPU P State Control	<p>Press [Enter] to configure advanced items.</p> <ul style="list-style-type: none"> ◆ SpeedStep (Pstates) <ul style="list-style-type: none"> – Conventional Intel SpeedStep Technology switches both voltage and frequency in tandem between high and low levels in response to processor load. – Options available: Enable, Disable. Default setting is Enable. ◆ Turbo Mode <ul style="list-style-type: none"> – When this item is enabled, the processor will automatically ramp up the clock speed of 1-2 of its processing cores to improve its performance. When this item is disabled, the processor will not overclock any of its core. – Options available: Enable, Disable. Default setting is Enable.
Hardware PM State Control	<p>Press [Enter] to configure advanced items.</p> <ul style="list-style-type: none"> ◆ Hardware P-States <ul style="list-style-type: none"> – When this item is disabled, the processor hardware chooses a P-state based on OS Request (Legacy P-States). – In Native mode, the processor hardware chooses a P-state based on OS guidance. – In Out of Band mode, the processor hardware autonomously chooses a P-state (with no OS guidance). – Options available: Disable, Native Mode, Out of Band Mode, Native Mode with No Legacy Support. Default setting is Native Mode.

Parameter	Description
CPU C State Control	<p>Press [Enter] to configure advanced items.</p> <ul style="list-style-type: none"> ◆ Enable Monitor MWAIT <ul style="list-style-type: none"> – Allows Monitor and MWAIT instructions. – Options available: Disable, Enable, Auto. Default setting is Auto. ◆ CPU C6 Report <ul style="list-style-type: none"> – Enable/Disable CPU C6(ACPI C3) report to OS. – Options available: Disable, Enable, Auto. Default setting is Auto. ◆ Enhanced Halt State (C1E) <ul style="list-style-type: none"> – Core C1E auto promotion control. Takes effect after reboot. – Options available: Enable, Disable. Default setting is Enable.
Package C State Control	<p>Press [Enter] to configure advanced items.</p> <ul style="list-style-type: none"> ◆ Package C State <ul style="list-style-type: none"> – Configures the state for the C-State package limit. – Options available: C0/C1 state, C2 state, C6(non Retention) state, C6(Retention) state, No Limit, Auto. Default setting is Auto.
CPU - Advanced PM Tuning	<p>Press [Enter] to configure advanced items.</p> <ul style="list-style-type: none"> ◆ Energy Perf BIAS <ul style="list-style-type: none"> – Press [Enter] to configure advanced items. <ul style="list-style-type: none"> » Power Performance Tuning <ul style="list-style-type: none"> • Options available: OS Controls EPB, BIOS Controls EPB, PECI Controls EPB. Default setting is OS Controls EPB. » Energy_PERF_BIAS_CFG mode^(Note) <ul style="list-style-type: none"> • Options available: Performance, Balanced Performance, Balanced Power, Power. Default setting is Balanced Performance.
SOCKET RAPL Config	<p>Press [Enter] to configure advanced items.</p> <ul style="list-style-type: none"> ◆ PL1 Power Limit <ul style="list-style-type: none"> – PL1 Power Limit in Watts. The value may vary from 0 to Fused Value. If the value is 0, the fused value will be programmed. – Default setting is 0. ◆ PL1 Time Window <ul style="list-style-type: none"> – PL1 value in seconds. The value may vary from 0 to 448. – Default setting is 1. ◆ PL2 Power Limit <ul style="list-style-type: none"> – PL2 Power Limit in Watts. The value may vary from 0 to Fused Value. If the value is 0, BIOS programs 120% * TDP. – Default setting is 0. ◆ PL2 Time Window <ul style="list-style-type: none"> – PL1 value in seconds. The value may vary from 0 to 0.438. – Default setting is 0.012.

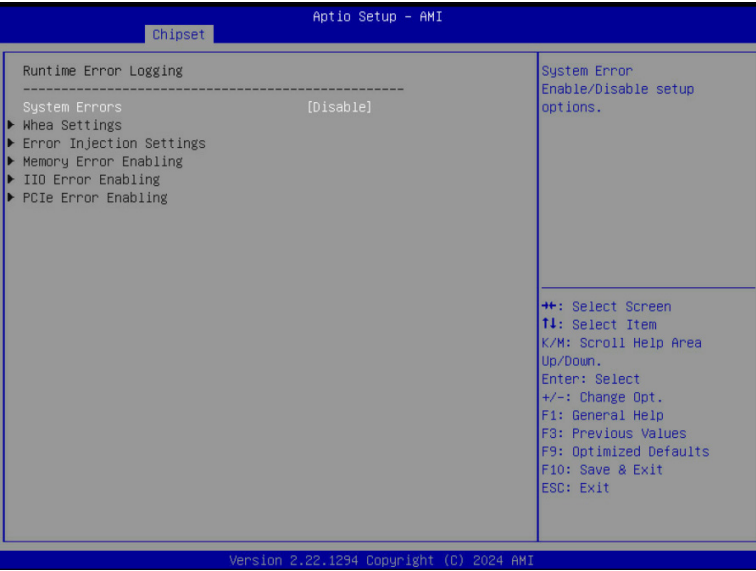
(Note) This item is configurable when **Power Performance Tuning** is set to **BIOS Controls EPB**.

5-3-7 Miscellaneous Configuration



Parameter	Description
Miscellaneous Configuration	
Active Video	Selects the active video type. Options available: Auto, Onboard Device, PCIE Device, Specific PCIE Device. Default setting is Auto .
External SSC - CK440	Enables Spread spectrum - only affects external clock generator. Options available: SSC Off, SSC = -0.3%, SSC = -0.5%, Hardware. Default setting is SSC Off .

5-3-8 Runtime Error Logging Settings

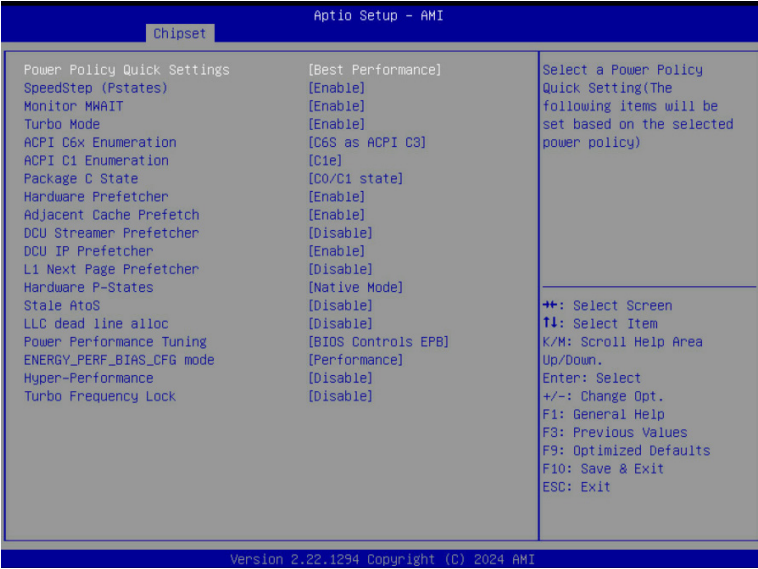


Parameter	Description
Runtime Error Logging	
System Errors	Enable/Disable system error logging function. Options available: Enable, Disable. Default setting is Enable .
S/W Error Injection Support	Enable/Disable software injection error logging function. Options available: Enable, Disable. Default setting is Disable .
Whea Settings	Press [Enter] to configure advanced items. <ul style="list-style-type: none">◆ WHEA (Windows Hardware Error Architecture) Support<ul style="list-style-type: none">– Enable/Disable WHEA Support.– Options available: Enable, Disable. Default setting is Enable.
Memory Error Enabling	Press [Enter] to configure advanced items. <ul style="list-style-type: none">◆ Memory Corrected Error<ul style="list-style-type: none">– Enable/Disable Memory Corrected Error.– Options available: Enable, Disable. Default setting is Enable.◆ Uncorrected Error disable Memory<ul style="list-style-type: none">– Enable/Disable the Memory that triggers Uncorrected Error.– Options available: Enable, Disable. Default setting is Disable.

Parameter	Description
PCIe Error Enabling	<p>Press [Enter] to configure advanced items.</p> <ul style="list-style-type: none"> ◆ PCIe Error <ul style="list-style-type: none"> – Enable/Disable PCIe error. – Options available: Enable, Disable. Default setting is Disable. ◆ Uncorrected Error^(Note) <ul style="list-style-type: none"> – Enables and escalates Uncorrectable/Recoverable Errors to error pins. – Options available: Enable, Disable. Default setting is Enable. ◆ Fatal Error Enable^(Note) <ul style="list-style-type: none"> – Enables and escalates Fatal Errors to error pins. – Options available: Enable, Disable. Default setting is Enable. ◆ Assert NMI on SERR^(Note) <ul style="list-style-type: none"> – Enable/Disable BIOS generates a non-maskable interrupt (NMI) and logs an error when a system error (SERR) occurs. – Options available: Enabled, Disabled. Default setting is Enabled. ◆ Assert NMI on PERR^(Note) <ul style="list-style-type: none"> – Enable/Disable BIOS generates a non-maskable interrupt (NMI) and logs an error when a processor bus parity error (PERR) occurs. – Options available: Enabled, Disabled. Default setting is Enabled.

(Note) This item appears when **PCIe Error** is set to **Enable**.

5-3-9 Power Policy



Parameter	Description
Power Policy Quick Settings	Selects a Power Policy Quick Setting. Options available: Standard, Best Performance, Energy Efficient. Default setting is Standard .
SpeedStep (Pstates)	Conventional Intel SpeedStep Technology switches both voltage and frequency in tandem between high and low levels in response to processor load. Options available: Enable, Disable. Default setting is Enable .
Turbo Mode	When this item is enabled, the processor will automatically ramp up the clock speed of 1-2 of its processing cores to improve its performance. When this item is disabled, the processor will not overclock any of its core. Options available: Enable, Disable. Default setting is Enable .
CPU C6 report	Enable/Disable the BIOS to enable the report from the CPU C6 state (ACPI C3) to the OS. Options available: Disable, Enable, Auto. Default setting is Auto .
Enhanced Halt State (C1E)	Enable/Disable the C1E support for lower power consumption. Takes effect after reboot. Options available: Enable, Disable. Default setting is Enable .
Package C State	Configures the C-State package limit. Options available: C0/C1 state, C2 state, C6(non Retention) state, C6(Retention) state, No Limit, Auto. Default setting is Auto .

Parameter	Description
Enable LP [Global]	Enables Logical processor (Software Method to Enable/Disable Logical Processor threads). Options available: ALL LPs, Single LP. Default setting is ALL LPs .
Hardware Prefetcher	Options available: Enable, Disable. Default setting is Enable .
Adjacent Cache Prefetch	Options available: Enable, Disable. Default setting is Enable .
DCU Streamer Prefetcher	Options available: Enable, Disable. Default setting is Enable .
Intel® VT for Directed I/O	Enable/Disable the Intel VT for Directed I/O (VT-d) support function by reporting the I/O device assignment to VMM through DMAR ACPI Tables. Options available: Enable, Disable. Default setting is Enable .

5-4 Server Management Menu



Parameter	Description
FRB-2 Timer	Enable/Disable FRB-2 timer (POST timer). Options available: Enabled, Disabled. Default setting is Enabled .
FRB-2 Timer ^(Note1) timeout	Configures the FRB2 Timer timeout. The value is between 1 to 30 minutes. Default setting is 6 minutes .
FRB-2 Timer Policy ^(Note1)	Configures the FRB2 Timer policy. Options available: Do Nothing, Reset, Power Down, Power Cycle. Default setting is Do Nothing .
OS Watchdog Timer	Enable/Disable OS Watchdog Timer function. Options available: Enabled, Disabled. Default setting is Disabled .
OS Wtd Timer Timeout ^(Note2)	Configures OS Watchdog Timer. The value is between 1 to 30 minutes. Default setting is 10 minutes .
OS Wtd Timer Policy ^(Note2)	Configure OS Watchdog Timer Policy. Options available: Reset, Do Nothing, Power Down, Power Cycle. Default setting is Reset .
Wait BMC Ready	POST wait BMC ready and reboot system. Options available: Disabled, 2 minutes, 4 minutes, 6 minutes. Default setting is 2 minutes .

(Note1) This item is configurable when **FRB-2 Timer** is set to **Enabled**.

(Note2) This item is configurable when **OS Watchdog Timer** is set to **Enabled**.

Parameter	Description
System Event Log	Press [Enter] to configure advanced items.
View FRU Information	Press [Enter] to view the FRU information.
BMC VLAN Configuration	Press [Enter] to configure advanced items.
BMC network Configuration	Press [Enter] to configure advanced items.
IPv6 BMC Network Configuration	Press [Enter] to configure advanced items.

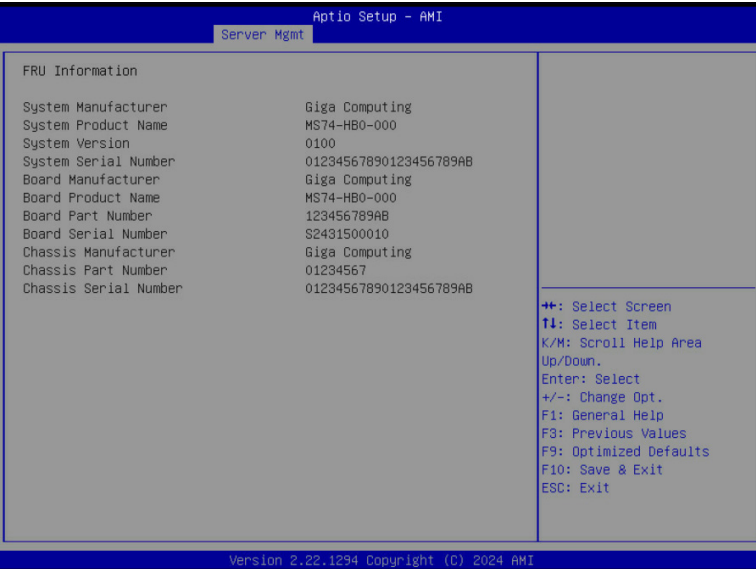
5-4-1 System Event Log



Parameter	Description
Enabling / Disabling Options	
SEL Components	Change this item to enable or disable all features of System Event Logging during boot. Options available: Enabled, Disabled. Default setting is Enabled .
Erasing Settings	
Erase SEL	Choose options for erasing SEL. Options available: No, Yes, On next reset, Yes, On every reset. Default setting is No .
When SEL is Full	Choose options for reactions to a full SEL. Options available: Do Nothing, Erase Immediately, Delete Oldest Record. Default setting is Do Nothing .
Custom EFI Logging Options	
Log EFI Status Codes	Enable/Disable the logging of EFI Status Codes (if not already converted to legacy). Options available: Disabled, Both, Error code, Progress code. Default setting is Error code .

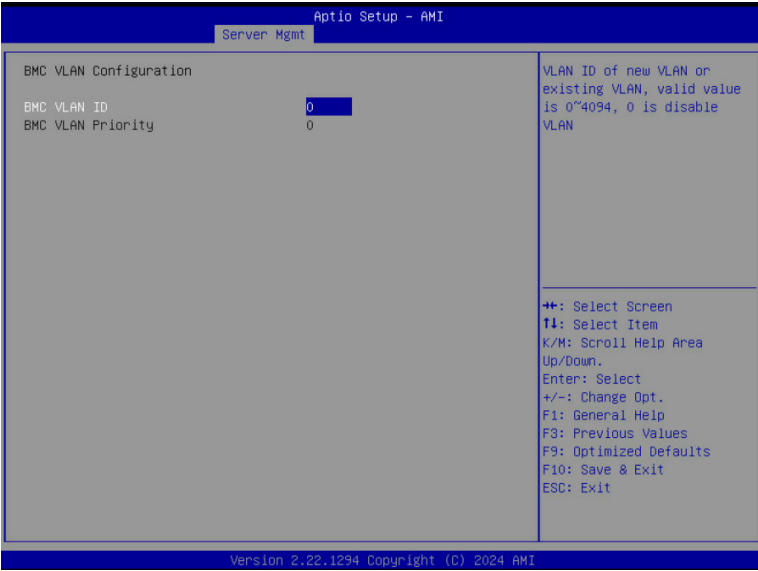
5-4-2 View FRU Information

The FRU page is a simple display page for basic system ID information, as well as System product information. Items on this window are non-configurable.



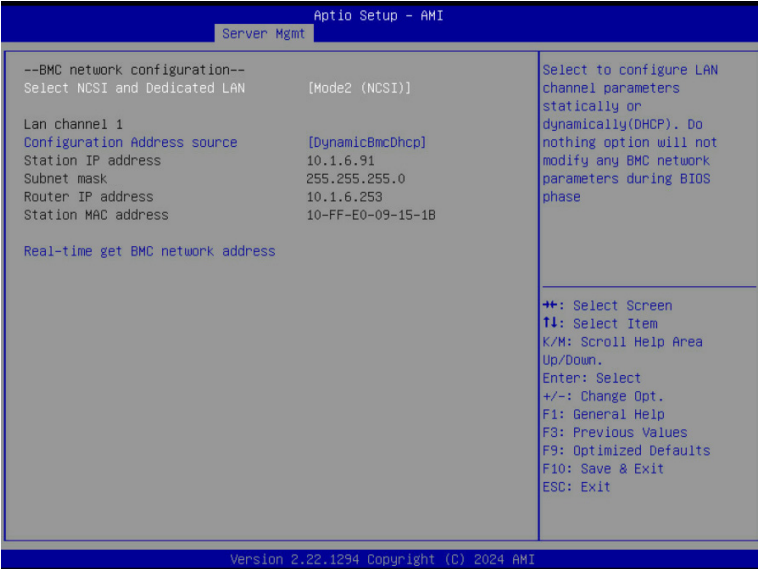
(Note) The model name will vary depends on the product you purchased

5-4-3 BMC VLAN Configuration



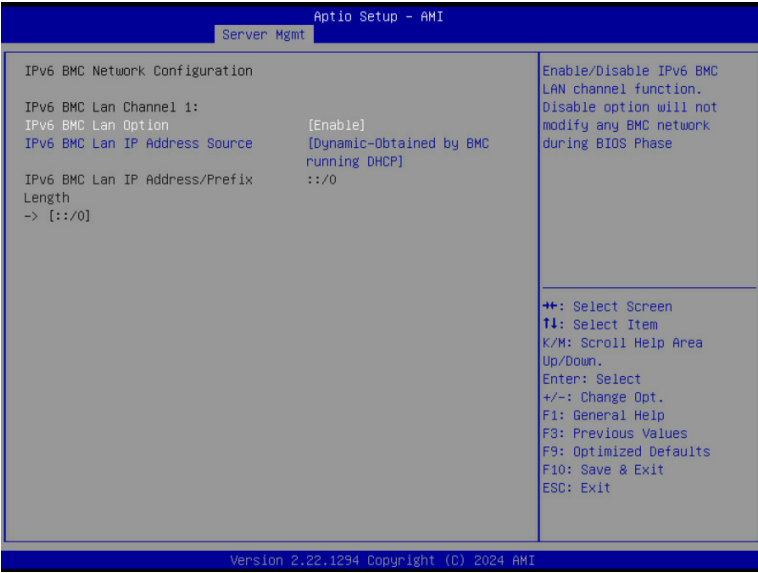
Parameter	Description
BMC VLAN Configuration	
BMC VLAN ID	Select to configure BMC VLAN ID. The valid range is from 0 to 4094. When set to 0, BMC VLAN ID will be disabled.
BMC VLAN Priority	Select to configure BMC VLAN Priority. The valid range is from 0 to 7. When BMC VLAN ID is set to 0, BMC VLAN Priority will not be selected.

5-4-4 BMC Network Configuration



Parameter	Description
BMC network configuration	
Select NCSI and Dedicated LAN	Options available: Do Nothing, Model1(Dedicated), Model2(NCSI), Mode3(Failover). Default setting is Do Nothing .
Lan Channel 1	
Configuration Address source	Selects to configure LAN channel parameters statically or dynamically (DHCP). Options available: Unspecified, Static, DynamicBmcDhcp. Default setting is DynamicBmcDhcp .
Station IP address	Displays IP Address information.
Subnet mask	Displays Subnet Mask information. Please note that the IP address must be in three digitals, for example, 192.168.000.001.
Router IP address	Displays the Router IP Address information.
Station MAC address	Displays the MAC Address information.
Real-time get BMC network address	Press [Enter] will set LAN mode and Address source and then get IP, Subnet, Gateway and MAC address.

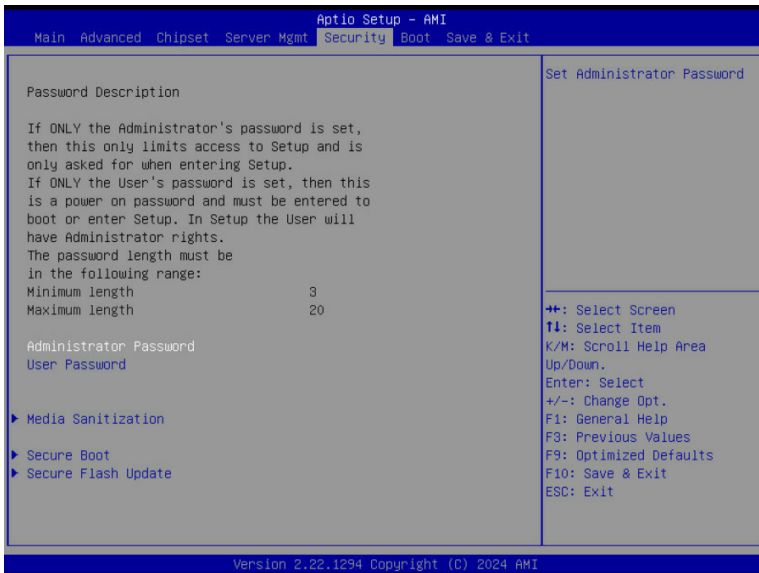
5-4-5 IPv6 BMC Network Configuration



Parameter	Description
IPv6 BMC network configuration	
IPv6 BMC Lan Channel 1	
IPv6 BMC Lan Option	Enable/Disable IPv6 BMC LAN channel function. When this item is disabled, the system will not modify any BMC network during BIOS phase. Options available: Unspecified, Disable, Enable. Default setting is Enable .
IPv6 BMC Lan IP Address Source	Selects to configure LAN channel parameters statically or dynamically (by BIOS or BMC). Options available: Unspecified, Static, Dynamic-Obtained by BMC running DHCP. Default setting is Dynamic-Obtained by BMC running DHCP .
IPv6 BMC Lan IP Address/Prefix Length	Check if the IPv6 BMC LAN IP address matches those displayed on the screen.

5-5 Security Menu

The Security menu allows you to safeguard and protect the system from unauthorized use by setting up access passwords.



There are two types of passwords that you can set:

- **Administrator Password**
Entering this password will allow the user to access and change all settings in the Setup Utility.
- **User Password**
Entering this password will restrict a user's access to the Setup menus. To enable or disable this field, a Administrator Password must first be set. A user can only access and modify the System Time, System Date, and Set User Password fields.

Parameter	Description
Administrator Password	Press [Enter] to configure the administrator password.
User Password	Press [Enter] to configure the user password.
Secure Boot	Press [Enter] to configure advanced items.

5-5-1 Secure Boot

The Secure Boot feature is applicable if supported by your Operating System. If your Operating System is not supporting Secure Boot, the system will hang when starting the Operating System.



Parameter	Description
System Mode	Displays if the system is in User mode or Setup mode.
Secure Boot	Enable/ Disable the Secure Boot function. Options available: Enabled, Disabled. Default setting is Disabled .
Secure Boot Mode ^(Note)	Secure Boot requires all the applications that are running during the booting process to be pre-signed with valid digital certificates. This way, the system knows all files being loaded before the Operating System loads to the login screen have not been tampered with. When set to Standard, it will automatically load the Secure Boot keys form the BIOS databases. When set to Custom, you can customize the Secure Boot settings and manually load its keys from the BIOS database. Options available: Standard, Custom. Default setting is Custom .
Restore Factory Keys	Forces the system to user mode and installs factory default Secure Boot key database.
Reset To Setup Mode	Reset the system to Setup Mode.

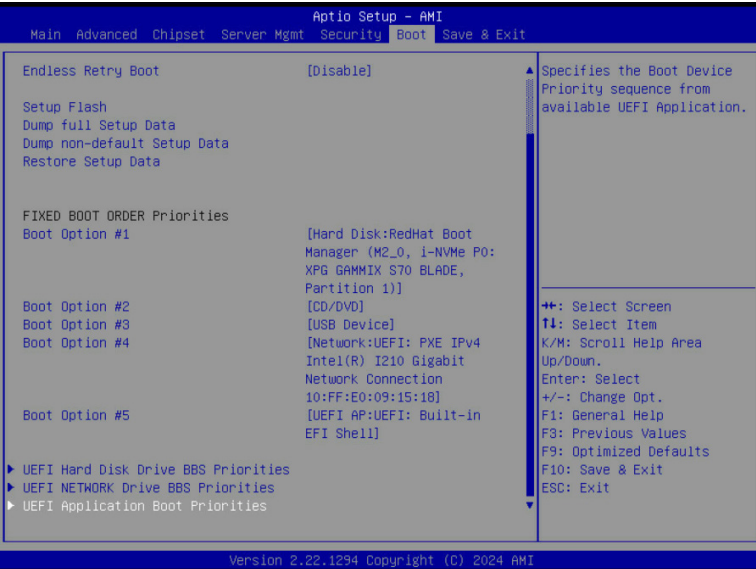
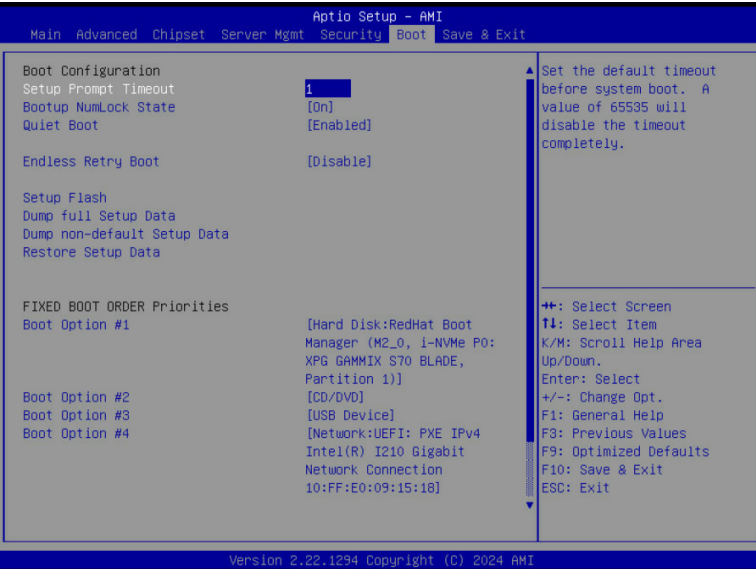
(Note) Advanced items prompt when this item is set to **Custom**.

Parameter	Description
Key Management	<p>Press [Enter] to configure advanced items.</p> <p>Please note that this item is configurable when Secure Boot Mode is set to Custom.</p> <ul style="list-style-type: none"> ◆ Factory Key Provision <ul style="list-style-type: none"> – Allows to provision factory default Secure Boot keys when system is in Setup Mode. – Options available: Enabled, Disabled. Default setting is Disabled. ◆ Restore Factory Keys <ul style="list-style-type: none"> – Installs all factory default keys. It will force the system in User Mode. – Options available: Yes, No. ◆ Reset To Setup Mode <ul style="list-style-type: none"> – Reset the system to Setup Mode. – Options available: Yes, No. ◆ Enroll Efi Image <ul style="list-style-type: none"> – Press [Enter] to enroll SHA256 hash of the binary into Authorized Signature Database (db). ◆ Export Secure Boot variables <ul style="list-style-type: none"> – Copy NVRAM content of Secure Boot variables to files in a root folder on a file system device. ◆ Secure Boot variable <ul style="list-style-type: none"> – Displays the current status of the variables used for secure boot. ◆ Platform Key (PK) <ul style="list-style-type: none"> – Displays the current status of the Platform Key (PK). – Press [Enter] to configure a new PK. – Options available: Update. ◆ Key Exchange Keys (KEK) <ul style="list-style-type: none"> – Displays the current status of the Key Exchange Key Database (KEK). – Press [Enter] to configure a new KEK or load additional KEK from storage devices. – Options available: Update, Append. ◆ Authorized Signatures (DB) <ul style="list-style-type: none"> – Displays the current status of the Authorized Signature Database. – Press [Enter] to configure a new DB or load additional DB from storage devices. – Options available: Update, Append. ◆ Forbidden Signatures (DBX) <ul style="list-style-type: none"> – Displays the current status of the Forbidden Signature Database. – Press [Enter] to configure a new dbx or load additional dbx from storage devices. – Options available: Update, Append.

Parameter	Description
Key Management (continued)	<ul style="list-style-type: none"> ♦ Authorized TimeStamps (DBT) <ul style="list-style-type: none"> – Displays the current status of the Authorized TimeStamps Database. – Press [Enter] to configure a new DBT or load additional DBT from storage devices. – Options available: Update, Append. ♦ OsRecovery Signatures <ul style="list-style-type: none"> – Displays the current status of the OsRecovery Signature Database. – Press [Enter] to configure a new OsRecovery Signature or load additional OsRecovery Signature from storage devices. – Options available: Update, Append.

5-6 Boot Menu

The Boot menu allows you to set the drive priority during system boot-up. BIOS setup will display an error message if the legacy drive(s) specified is not bootable.

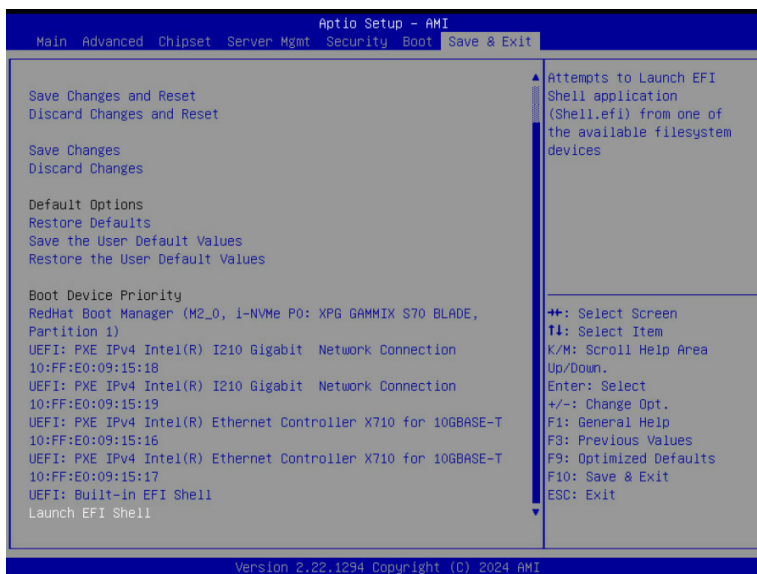
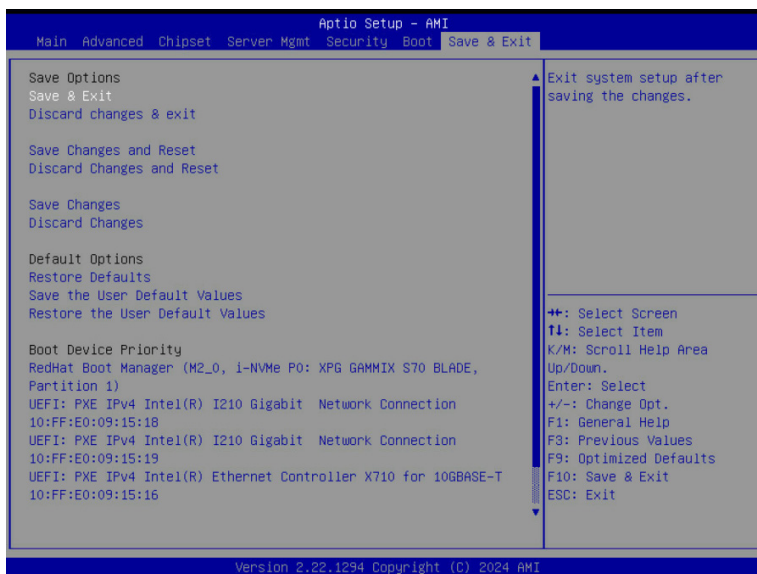


Parameter	Description
Boot Configuration	
Setup Prompt Timeout	Number of seconds to wait for setup activation key. 65535 (0xFFFF) means indefinite waiting. Press the numeric keys to input the desired values.
Bootup NumLock State	Enable/Disable the Bootup NumLock function. Options available: On, Off. Default setting is On .
Quiet Boot	Enable/Disable showing the logo during POST. Options available: Enabled, Disabled. Default setting is Enabled .
Endless Retry Boot	Options available: Disable, Enable. Default setting is Disable .
Setup Flash	Press [Enter] to run setup flash.
Dump full Setup Data	Press [Enter] to dump full setup data to file.
Dump non-default Setup Data	Press [Enter] to dump non-default setup data to file.
Restore Setup Data	Press [Enter] to restore setup data from file.

Parameter	Description
FIXED BOOT ORDER Priorities	
Boot Option #1 / #2 / #3 / #4 / #5	Press [Enter] to configure the boot order priority. By default, the server searches for boot devices in the following sequence: <ol style="list-style-type: none"> 1. Hard drive. 2. CD-COM/DVD drive. 3. USB device. 4. Network. 5. UEFI.
UEFI Application Boot Priorities	Press [Enter] to configure the boot priority.

5-7 Save & Exit Menu

The Save & Exit menu displays the various options to quit from the BIOS setup. Highlight any of the exit options then press <Enter>.



Parameter	Description
Save Options	
Save and Exit	Saves changes made and closes the BIOS setup. Options available: Yes, No.
Discard changes and exit	Discards changes made and exits the BIOS setup. Options available: Yes, No.
Save Changes and Reset	Restarts the system after saving the changes made. Options available: Yes, No.
Discard Changes and Reset	Restarts the system without saving any changes. Options available: Yes, No.
Save Changes	Saves changes done so far to any of the setup options. Options available: Yes, No.
Discard Changes	Discards changes made and closes the BIOS setup. Options available: Yes, No.
Default Options	

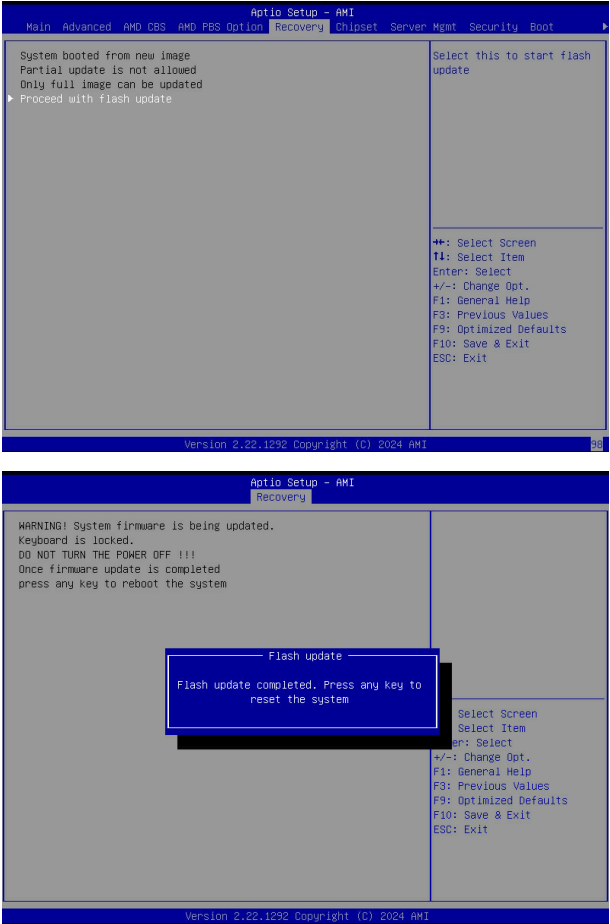
Parameter	Description
Restore Defaults	Loads the default settings for all BIOS setup parameters. Setup Defaults are quite demanding in terms of resources consumption. If you are using low-speed memory chips or other kinds of low-performance components and you choose to load these settings, the system might not function properly. Options available: Yes, No.
Save the User Default Values	Saves the changes made as the user default settings. Options available: Yes, No.
Restore the User Default Values	Loads the user default settings for all BIOS setup parameters. Options available: Yes, No.
Boot Device Priority	Press [Enter] to configure the device as the boot-up drive.
Launch EFI Shell	Attempts to Launch EFI Shell application (Shell.efi) from one of the available file system devices.

5-8 BIOS Recovery

The system has an embedded recovery technique. In the event that the BIOS becomes corrupt the boot block can be used to restore the BIOS to a working state. To restore your BIOS, please follow the instructions listed below:

Recovery Instruction:

1. Copy the XXX.rom to USB diskette.
2. Setting BIOS Recovery jump to enabled status.
3. Boot into BIOS recovery.
4. Run Proceed with flash update.
5. BIOS updated.



5-9 BIOS POST Beep code (AMI standard)

5-9-1 PEI Beep Codes

# of Beeps	Description
1	Memory not Installed.
1	Memory was installed twice (InstallPeiMemory routine in PEI Core called twice)
2	Recovery started
3	DXE IPL was not found
3	DXE Core Firmware Volume was not found
4	Recovery failed
4	S3 Resume failed
7	Reset PPI is not available

5-9-2 DXE Beep Codes

# of Beeps	Description
1	Invalid password
4	Some of the Architectural Protocols are not available
5	No Console Output Devices are found
5	No Console Input Devices are found
6	Flash update is failed
7	Reset protocol is not available
8	Platform PCI resource requirements cannot be met