# **GIGABYTE**<sup>™</sup>

# G893-SD1-AAX3 G893-SD1-AAX5

HPC/AI Server - 5th/4th Gen Intel® Xeon® Scalable 8U DP NVIDIA HGX™ H200 8-GPU (AAX3) 8U DP NVIDIA HGX™ B200 8-GPU (AAX5)

User Manual Rev. 1.0

### **Copyright**

© 2025 Giga Computing Technology CO., LTD. All rights reserved. The trademarks mentioned in this manual are legally registered to their respective owners.

### **Disclaimer**

Information in this manual is protected by copyright laws and is the property of Giga Computing. Changes to the specifications and features in this manual may be made by Giga Computing without prior notice. No part of this manual may be reproduced, copied, translated, transmitted, or published in any form or by any means without Giga Computing's prior written permission.

### **Documentation Classifications**

In order to assist in the use of this product, Giga Computing provides the following types of documentation:

- User Manual: detailed information & steps about the installation, configuration and use of this product (e.g. motherboard, server barebones), covering hardware and BIOS.
- User Guide: detailed information about the installation & use of an add-on hardware or software component (e.g. BMC firmware, rail-kit) compatible with this product.
- Quick Installation Guide: a short guide with visual diagrams that you can reference easily for installation purposes of this product (e.g. motherboard, server barebones).

Please see the support section of the online product page to check the current availability of these documents.

### For More Information

For related product specifications, the latest firmware and software, and other information please visit our website at http://www.gigabyte.com/Enterprise

For GIGABYTE distributors and resellers, additional sales & marketing materials are available from our reseller portal: http://reseller.b2b.gigabyte.com

For further technical assistance, please contact your GIGABYTE representative or visit https://esupport.gigabyte.com/ to create a new support ticket

For any general sales or marketing enquiries, you may also message GIGABYTE server directly by email: server.grp@gigabyte.com

### Conventions

The following conventions are used in this user's guide:

L.	<b>NOTE!</b> Gives bits and pieces of additional information related to the current topic.
	CAUTION! Gives precautionary measures to avoid possible hardware or software problems.
	WARNING! Alerts you to any damage that might result from doing or not doing specific actions.

### Server Warnings and Cautions

Before installing a server, be sure that you understand the following warnings and cautions.

### 

#### To reduce the risk of electric shock or damage to the equipment:

- Do not disable the power cord grounding plug. The grounding plug is an important safety feature.
- Plug the power cord into a grounded (earthed) electrical outlet that is easily accessible at all times.
- · Unplug all the power cords from the power supplies to disconnect power to the equipment.



- · Shock Hazard! Disconnect all power supply cords before servicing.
- Do not route the power cord where it can be walked on or pinched by items placed against it. Pay particular
  attention to the plug, electrical outlet, and the point where the cord extends from the server.

## 

To reduce the risk of personal injury from hot surfaces, allow the drives and the internal system components to cool before touching them.

### WARNING!

This server is equipped with high speed fans. Keep away from hazardous moving fan blades during servicing.



This equipment is intended to be used in Restrict Access Location. The access can only be gained by Skilled person. Only authorized by well trained professional person can access the restrict access location.

# 

The equipment should only be repaired, maintained or replaced by skilled personnel.

# 

- Do not operate the server for long periods with the access panel open or removed. Operating the server in this manner results in improper airflow and improper cooling that can lead to thermal damage.
- Danger of explosion if battery is incorrectly replaced.
- · Replace only with the same or equivalent type recommended by the manufacturer.
- · Dispose of used batteries according to the manufacturer's instructions.

## 

Risk of explosion if battery is replaced incorrectly or with an incorrect type. Replace the battery only with the same or equivalent type recommended by the manufacturer. Dispose of used batteries according to the manufacturer's instructions.

### Electrostatic Discharge (ESD)

### 

ESD CAN DAMAGE DRIVES, BOARDS, AND OTHER PARTS. WE RECOMMEND THAT YOU PERFORM ALL PROCEDURES AT AN ESD WORKSTATION. IF ONE IS NOT AVAILABLE, PROVIDE SOME ESD PROTECTION BY WEARING AN ANTI-STATIC WRIST STRAP AT-TACHED TO CHASSIS GROUND -- ANY UNPAINTED METAL SURFACE -- ON YOUR SERVER WHEN HANDLING PARTS.

Always handle boards carefully. They can be extremely sensitive to ESD. Hold boards only by their edges without any component and pin touching. After removing a board from its protective wrapper or from the system, place the board component side up on a grounded, static free surface. Use a conductive foam pad if available but not the board wrapper. Do not slide board over any surface.

**System power on/off:** To remove power from system, you must remove the system from rack. Make sure the system is removed from the rack before opening the chassis, adding, or removing any non hot-plug components.

**Hazardous conditions, devices and cables:** Hazardous electrical conditions may be present on power, telephone, and communication cables. Turn off the system and discon-nect the cables attached to the system before servicing it. Otherwise, personal injury or equipment damage can result.

**Electrostatic discharge (ESD) and ESD protection:** ESD can damage drives, boards, and other parts. We recommend that you perform all procedures in this chapter only at an ESD workstation. If one is not available, provide some ESD protection by wearing an antistatic wrist strap attached to chassis ground (any unpainted metal surface on the server) when handling parts.

**ESD** and handling boards: Always handle boards carefully. They can be extremely sensi-tive to electrostatic discharge (ESD). Hold boards only by their edges. After removing a board from its protective wrapper or from the system, place the board component side up on a grounded, static free surface. Use a conductive foam pad if available but not the board wrapper. Do not slide board over any surface.

**Installing or removing jumpers:** A jumper is a small plastic encased conductor that slips over two jumper pins. Some jumpers have a small tab on top that can be gripped with fin-gertips or with a pair of fine needle nosed pliers. If the jumpers do not have such a tab, take care when using needle nosed pliers to remove or install a jumper; grip the narrow sides of the jumper with the pliers, never the wide sides. Gripping the wide sides can dam-age the contacts inside the jumper, causing intermittent problems with the function con-trolled by that jumper. Take care to grip with, but not squeeze, the pliers or other tool used to remove a jumper, or the pins on the board may bend or break.

# Table of Contents

Chapter 1	Hardv	vare Installation	9
	1-1	Installation Precautions	9
	1-2	Product Specifications	10
	1-3	System Block Diagram	13
	1-4	PCle Block Diagram	14
Chapter 2	Syste	m Appearance	15
	2-1	Front View	15
	2-2	Rear View	16
	2-3	Top View	17
	2-4	Front Panel LED and Buttons	18
	2-4	-1 RoT LEDs	19
	2-5	Front Panel System LAN LEDs	21
	2-6	Power Supply Unit (PSU) LED	22
	2-7	Hard Disk Drive LEDs	23
Chapter 3	Syste	m Hardware Installation	24
	3-1	Removing and Installing the Chassis Top Cover	25
	3-2	Removing and Installing the GPU Tray	26
	3-3	Removing and Installing the Motherboard Tray	27
	3-4	Removing the Heat Sink	28
	3-5	Installing the CPU	29
	3-6	Installing the Memory	31
	3-6	-1 Eight Channel Memory Configuration	31
	3-6	-2 Installing the Memory	32
	3-6		
	3-6		
	3-7	Installing the PCI Expansion Card	
	3-8	Installing the Hard Disk Drive	37
	3-9	Replacing the System Fan Module	
	3-10	Removing and Installing the Power Supply	
	3-11	Installing the System into the Cabinet	41
	3-12	Removing the System from the Cabinet	42
	3-13	Cable Connection	44
Chapter 4	Mothe	erboard Components	49

4-1	Mo	therboard Components	. 49
4-2	Jun	nper Setting	. 51
4-3	Bad	kplane Board Storage Connector	. 52
4-:	3-1	CBPG641	
Chapter 5 BIOS	S Set	up	
5-1		Prain Menu	
5-2		vanced Menu	
	2-1	Trusted Computing	
•	2-2	Serial Port Console Redirection	
	2-3	SIO Configuration	
	2-4	PCI Subsystem Settings	
	2-5	USB Configuration	
•	2-6	Network Stack Configuration	
•	2-7	Post Report Configuration	
	2-8	KMIP Server Configuration	
	2-9	KMS Policy Configuration	
•	2-10	NVMe Configuration	
•	2-11	Chipset Configuration	
	2-12	TIs Auth Configuration	
5-3	2-13	iSCSI Configuration	
5-3	2-14	Intel(R) Ethernet Controller X710 for 10GBASE-T	
5-3	2-15	VLAN Configuration	
5-3	2-16	MAC IPv6 Network Configuration	
5-3	2-17	MAC IPv4 Network Configuration	
5-3	2-18	Driver Health	
5-3	Chi	pset Menu	
5-5	3-1	Processor Configuration	
5-3	3-2	Common RefCode Configuration	
5-3	3-3	UPI Configuration	
5-3	3-4	Memory Configuration	
5-:	3-5	IIO Configuration	
5-	3-6	Advanced Power Management Configuration	98
5-	3-7	PCH Configuration	101
5-	3-8	Miscellaneous Configuration	103
5-	3-9	Server ME Configuration	104
5-	3-10	Runtime Error Logging Settings	105
5-	3-11	Power Policy	
5-4	Ser	ver Management Menu	109
5	4-1	System Event Log	111

5-4	-2	View FRU Information	112
5-4	-3	BMC VLAN Configuration	113
		BMC Network Configuration	
		IPv6 BMC Network Configuration	
5-5	Sec	urity Menu	116
		Secure Boot	
5-6	Boo	t Menu	120
		e & Exit Menu	
5-8		S Recovery1	
00	510		

### Chapter 1 Hardware Installation

### 1-1 Installation Precautions

The motherboard/system contain numerous delicate electronic circuits and components which can become damaged as a result of electrostatic discharge (ESD). Prior to installation, carefully read the user manual and follow these procedures:

- Prior to installation, do not remove or break motherboard S/N (Serial Number) sticker or warranty sticker provided by your dealer. These stickers are required for warranty validation.
- Always remove the AC power by unplugging the power cord from the power outlet before installing or removing the motherboard or other hardware components.
- When connecting hardware components to the internal connectors on the motherboard, make sure they are connected tightly and securely.
- When handling the motherboard, avoid touching any metal leads or connectors.
- It is best to wear an electrostatic discharge (ESD) wrist strap when handling electronic components such as a motherboard, CPU or memory. If you do not have an ESD wrist strap, keep your hands dry and first touch a metal object to eliminate static electricity.
- Prior to installing the motherboard, please have it on top of an antistatic pad or within an electrostatic shielding container.
- Before unplugging the power supply cable from the motherboard, make sure the power supply has been turned off.
- Before turning on the power, make sure the power supply voltage has been set according to the local voltage standard.
- Before using the product, please verify that all cables and power connectors of your hardware components are connected.
- To prevent damage to the motherboard, do not allow screws to come in contact with the motherboard circuit or its components.
- Make sure there are no leftover screws or metal components placed on the motherboard or within the computer casing.
- Do not place the computer system on an uneven surface.
- Do not place the computer system in a high-temperature environment.
- Turning on the computer power during the installation process can lead to damage to system components as well as physical harm to the user.
- If you are uncertain about any installation steps or have a problem related to the use of the product, please consult a certified computer technician.

### 1-2 Product Specifications



### NOTE:

We reserve the right to make any changes to the product specifications and product-related information without prior notice.

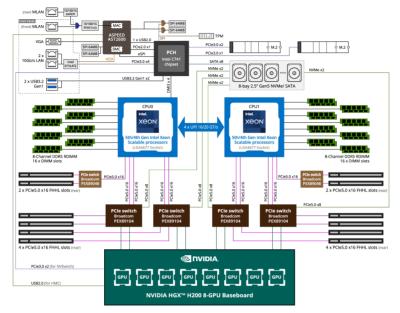
System	<ul> <li>◆ 8U</li> </ul>
Dimension	<ul> <li>447 x 351 x 923 (W x H x D, mm)</li> </ul>
CPU	<ul> <li>5th Generation Intel® Xeon® Scalable Processors</li> </ul>
	<ul> <li>4th Generation Intel® Xeon® Scalable Processors</li> </ul>
	Intel® Xeon® CPU Max Series
	<ul> <li>Dual processor, TDP up to 350W</li> </ul>
	··· F ····· · · · · · · · · · · · · · ·
	[Note] If only 1 CPU is installed, some PCIe or memory functions might be unavailable.
Socket	• 2 x LGA4677
	Socket E
Chipset	Intel® C741
Security	UEFI Secure Boot
	Silicon root of trust (Option)
	SNMP Support: V3
Memory	32 x DIMM slots
	DDR5 memory supported
	8-Channel memory per processor
	5th Gen Intel® Xeon®:
	<ul> <li>RDIMM: Up to 5600 MT/s (1DPC), 4400 MT/s (2DPC)</li> </ul>
	4th Gen Intel® Xeon®:
	<ul> <li>RDIMM: Up to 4800 MT/s (1DPC), 4400 MT/s (2DPC)</li> </ul>
	Intel® Xeon® Max Series:
	<ul> <li>RDIMM: Up to 4800 MT/s (1DPC), 4400 MT/s (2DPC)</li> </ul>
	Front (I/O board - CFPG540):
	<ul> <li>2 x 10Gb/s LAN (1 x Intel® X710-AT2)</li> </ul>
	- Support NCSI function
	oupport noon unouon
	<ul> <li>1 x 10/100/1000 Mbps Management LAN</li> </ul>
	Rear (MLAN board - CDB66):
	1 x 10/100/1000 Mbps Management LAN
	[Note] When both MLAN ports are connected with cables, the front MLAN port will be
	set as the default.

Integrated in Aspeed® AST2600 - 1 x VGA port         Image: Storage       Front hot-swap: - 8 x 2.5° Gen5 NVMe/SATA - (NVMe from PEX89104)         Internal M.2: - 1 x M.2 (2280/22110), PCIe Gen3 x2, from PCH - 1 x M.2 (2280/22110), PCIe Gen3 x1, from PCH         Image: SAS       N/A         Image: RAID       Intel® SATA RAID 0/1/10/5         Image: RAID       PCIe Bridge Board - CBBG045 x 2: - 4 x FIHL x16 (Gen5 x16), from PEX89104      <	Storage       Front <ul> <li>Interr</li> <li>1</li> <li>1</li> </ul> SAS <ul> <li>Interr</li> <li>1</li> <li>1</li> </ul> SAS <ul> <li>N</li> <li>G893             </li> <li>N</li> </ul> GB33 <ul> <li>N</li> <li>G893             </li> <li>N</li> <li>N</li> </ul> Cases                Expansion Slot <li>Front I/O</li> <li>N               <li>N  <ul> <li>N</li> </ul> <li>T</li>             Interview             Interview         </li></li>	1 x VGA port hot-swap: 3 x 2.5" Gen5 NVMe/SATA (NVMe from PEX89104) hal M.2: 1 x M.2 (2280/22110), PCIe Gen3 x2, from PCH
Internal M.2:       • 1 x M.2 (2280/22110), PCIe Gen3 x2, from PCH         • 1 x M.2 (2280/22110), PCIe Gen3 x1, from PCH         Image: SAS       • N/A         Image: SAS       • N/IA         Image: SAS       • N/IA         Image: SAS       • PCle Bridge Board - CPBG45 x 2: • 4 x FHHL x16 (Gen5 x16), from PEX89048	<ul> <li>8</li> <li>1</li> </ul>	3 x 2.5" Gen5 NVMe/SATA (NVMe from PEX89104) nal M.2:   x M.2 (2280/22110), PCIe Gen3 x2, from PCH
- (NVMe from PEX89104)         Internal M.2:         • 1 x M.2 (2280/22110), PCIe Gen3 x2, from PCH         • 1 x M.2 (2280/22110), PCIe Gen3 x1, from PCH         Image: SAS         • N/A         Image: SAS         Image: SAS         • NVIDIA HGX™ H200 with 8 x SXM GPUs         Image: SAS         Image: SAS         • NVIDIA HGX™ H200 with 8 x SXM GPUs         Image: SAS         • NVIDIA HGX™ H200 with 8 x SXM GPUs         Image: SAS         • NVIDIA HGX™ B200 with 8 x SXM GPUs         Image: SAS         • PCIe Bridge Board - CBG76:         • 8 x FHHL x16 (Gen5 x16), from PEX89048         Image: SAS SD: A A SAS         • YO board - CFPG540:         • 2 x USB 3.2 Gen1 ports (Type-A) <td>Interr Interr SAS N SAS N N RAID II Modular GPU G893 N G893 N Cases N N Cases N N Cases N N Cases N N Cases N N Cases N N Cases N N Cases N N Cases N N Cases N N Cases N N N Cases N N N N N Cases N N N N N N N N N N N N N</td> <td>(NVMe from PEX89104) nal M.2: x M.2 (2280/22110), PCIe Gen3 x2, from PCH</td>	Interr Interr SAS N SAS N N RAID II Modular GPU G893 N G893 N Cases N N Cases N N Cases N N Cases N N Cases N N Cases N N Cases N N Cases N N Cases N N Cases N N Cases N N N Cases N N N N N Cases N N N N N N N N N N N N N	(NVMe from PEX89104) nal M.2: x M.2 (2280/22110), PCIe Gen3 x2, from PCH
Internal M.2:       1 x M.2 (2280/22110), PCIe Gen3 x2, from PCH       1 x M.2 (2280/22110), PCIe Gen3 x1, from PCH         Image: SAS       N/A         Image: SAS       SAS         Image: SAS       N/A         Image: SAS       SAS         Image: SAS       NVIDIA HGX™ H200 with 8 x SXM GPUs         Image: SAS       PCIe Bridge Board - CBG76:         Image: SAS       SAS Gen1 ports (Type-A)	Interr interr SAS N SAS N N N N N N N Cases N Cases N Cases N Cases N Cases N Cases N Cases N N Cases N N Cases N N Cases N N Cases N N Cases N N Cases N N Cases N N Cases N N Cases N N Cases N N Cases N N Cases N N Cases N N Cases N N Cases N N Cases N N Cases N N Cases N N N N Cases N N N N N N N N N N N N N	nal M.2: x M.2 (2280/22110), PCIe Gen3 x2, from PCH
I x M.2 (2280/22110), PCle Gen3 x2, from PCH         I x M.2 (2280/22110), PCle Gen3 x1, from PCH         I x M.2 (2280/22110), PCle Gen3 x1, from PCH         Image: SAS         RAID         Intel® SATA RAID 0/1/10/5         Image: RAID         Modular GPU         G893-SD1-AAX3         NVIDIA HGX™ H200 with 8 x SXM GPUs         G893-SD1-AAX5         NVIDIA HGX™ B200 with 8 x SXM GPUs         PCle Bridge Board - CBG76:         - 8 x FHHL x16 (Gen5 x16), from PEX89104         PCle Bridge Board - CPBG045 x 2:         - 4 x FHHL x16 (Gen5 x16), from PEX89048         Image: Pront I/O         I/O board - CFPG540:         2 x USB 3.2 Gen1 ports (Type-A)         1 x MLAN port (default)         1 x MLAN port (default)         1 x MLAN port (default)         1 x NM button         1 x NM button         1 x Reset button         1 x Storage activity LED         1 x System status LED		x M.2 (2280/22110), PCIe Gen3 x2, from PCH
I x M.2 (2280/22110), PCle Gen3 x2, from PCH         I x M.2 (2280/22110), PCle Gen3 x1, from PCH         I x M.2 (2280/22110), PCle Gen3 x1, from PCH         Image: SAS         RAID         Intel® SATA RAID 0/1/10/5         Image: RAID         Modular GPU         G893-SD1-AAX3         NVIDIA HGX™ H200 with 8 x SXM GPUs         G893-SD1-AAX5         NVIDIA HGX™ B200 with 8 x SXM GPUs         PCle Bridge Board - CBG76:         - 8 x FHHL x16 (Gen5 x16), from PEX89104         PCle Bridge Board - CPBG045 x 2:         - 4 x FHHL x16 (Gen5 x16), from PEX89048         Image: Pront I/O         I/O board - CFPG540:         2 x USB 3.2 Gen1 ports (Type-A)         1 x MLAN port (default)         1 x MLAN port (default)         1 x MLAN port (default)         1 x NM button         1 x NM button         1 x Reset button         1 x Storage activity LED         1 x System status LED		x M.2 (2280/22110), PCIe Gen3 x2, from PCH
<ul> <li>1 x M.2 (2280/22110), PCle Gen3 x1, from PCH</li> <li>SAS</li> <li>N/A</li> <li>RAID</li> <li>Intel® SATA RAID 0/1/10/5</li> <li>Modular GPU</li> <li>G893-SD1-AAX3         <ul> <li>NVIDIA HGX<sup>™</sup> H200 with 8 x SXM GPUs</li> <li>G893-SD1-AAX5             <ul></ul></li></ul></li></ul>	<ul> <li>Image: SAS</li> <li>SAS</li> <li>RAID</li> <li>Image: RAID</li> <li>Modular GPU</li> <li>G893</li> <li>N</li> <li>G893</li> <li>G893</li> <li>G893</li> <li>N</li> <li>G893</li> <li>N</li> <li>G893</li> <li>G893</li> <li>G893</li> <li>N</li> <li>G893</li></ul>	
SAS       N/A         Image: RAID       Intel® SATA RAID 0/1/10/5         Image: RAID       Intel® SATA RAID 0/1/10/5         Image: RAID       Sessed and a constraint of the sessed and the sesessed and the sessed and the sessed and th	SAS       •       N         Image: RAID       II         Image: RAID       II         Image: RAID       II         Image: RAID       II         Image: RAID       III         Image: RAID       IIIIIIIIIIIIIIIIIIIIIIIIIIIIIIIIIIII	x M.2 (2280/22110), PCIe Gen3 x1, from PCH
Image: Ward Point of the text of the text of tex of text of text of text of text of tex of text of tex	Image: Second state sta	
Intel® SATA RAID 0/1/10/5         Image: Sata RAID 0/1/10/5         Image: Sata RAID 0/1/10/5         State Sata RAID 0/1/10/5         Image: Sata RAID 0/1/10/5         State Sata RAID 0/1/10/0         State Sata RAID 0/1/10/5         Stat	Modular GPU         G893           Modular GPU         G893           Expansion Slot         F           Front I/O         I/O bo           Front I/O         I/O bo           1         1	V/A
<ul> <li>NVIDIA HGX™ H200 with 8 x SXM GPUs</li> <li>G893-SD1-AAX5         <ul> <li>NVIDIA HGX™ B200 with 8 x SXM GPUs</li> </ul> </li> <li>PCIe Bridge Board - CBG76:             <ul> <li>8 x FHHL x16 (Gen5 x16), from PEX89104</li> <li>PCIe Bridge Board - CPBG045 x 2:                 <ul></ul></li></ul></li></ul>	Front I/O         I/O bo           • 1         • 1	ntel® SATA RAID 0/1/10/5
<ul> <li>NVIDIA HGX™ H200 with 8 x SXM GPUs</li> <li>G893-SD1-AAX5         <ul> <li>NVIDIA HGX™ B200 with 8 x SXM GPUs</li> </ul> </li> <li>PCIe Bridge Board - CBG76:             <ul> <li>8 x FHHL x16 (Gen5 x16), from PEX89104</li> <li>PCIe Bridge Board - CPBG045 x 2:                 <ul></ul></li></ul></li></ul>	<ul> <li>N</li> <li>G893</li> <li>N</li> <li>G893</li> <li>N</li> <li>Expansion Slot</li> <li>F</li> <li>F</li> <li>Front I/O</li> <li>I/O bo</li> <li>2</li> <li>1</li> <li>2</li> <li>1</li> </ul>	-SD1-AAX3
<ul> <li>NVIDIA HGX™ B200 with 8 x SXM GPUs</li> <li>PCle Bridge Board - CBG76:         <ul> <li>8 x FHHL x16 (Gen5 x16), from PEX89104</li> </ul> </li> <li>PCle Bridge Board - CPBG045 x 2:             <ul> <li>4 x FHHL x16 (Gen5 x16), from PEX89048</li> </ul> </li> <li>PCle Bridge Board - CPBG045 x 2:             <ul> <li>4 x FHHL x16 (Gen5 x16), from PEX89048</li> </ul> </li> <li>PCle Bridge Board - CPBG045 x 2:             <ul> <li>4 x FHHL x16 (Gen5 x16), from PEX89048</li> </ul> </li> <li>PCle Bridge Board - CPBG045 x 2:             <ul> <li>4 x FHHL x16 (Gen5 x16), from PEX89048</li> </ul> </li> <li>PCle Bridge Board - CPBG045 x 2:         <ul> <li>4 x FHHL x16 (Gen5 x16), from PEX89048</li> </ul> </li> <li>PCle Bridge Board - CPBG045 x 2:         <ul> <li>4 x FHHL x16 (Gen5 x16), from PEX89048</li> </ul> </li> <li>PCle Bridge Board - CPBG045 x 2:         <ul> <li>4 x FHHL x16 (Gen5 x16), from PEX89048</li> </ul> </li> <li>PCle Argue Board - CFPG540:         <ul> <li>4 x FHHL x16 (Gen5 x16), from PEX89048</li> </ul> </li> <li>PCle Argue Board - CFPG540:         <ul> <li>4 x FHHL x16 (Gen5 x16), from PEX89048</li> <li>2 x USB 3.2 Gen1 ports (Type-A)</li> <li>1 x VGA port</li> <li>2 x RJ45 ports</li> <li>1 x MLAN port (default)</li> <li>1 x Power button with LED</li> <li>1 x NMI button</li> <li>1 x Reset button</li> <li>1 x Storage activity LED</li> <li>1 x System status LED</li> </ul> </li> <li>MLAN board - CDB66:</li> </ul>	Expansion Slot     F     Front I/O     Front I/O     I/O bo     2     1     1     1     1     1     1     1     1     1	IVIDIA HGX™ H200 with 8 x SXM GPUs
<ul> <li>NVIDIA HGX™ B200 with 8 x SXM GPUs</li> <li>PCle Bridge Board - CBG76:         <ul> <li>8 x FHHL x16 (Gen5 x16), from PEX89104</li> </ul> </li> <li>PCle Bridge Board - CPBG045 x 2:             <ul> <li>4 x FHHL x16 (Gen5 x16), from PEX89048</li> </ul> </li> <li>PCle Bridge Board - CPBG045 x 2:             <ul> <li>4 x FHHL x16 (Gen5 x16), from PEX89048</li> </ul> </li> <li>PCle Bridge Board - CPBG045 x 2:             <ul> <li>4 x FHHL x16 (Gen5 x16), from PEX89048</li> </ul> </li> <li>PCle Bridge Board - CPBG045 x 2:             <ul> <li>4 x FHHL x16 (Gen5 x16), from PEX89048</li> </ul> </li> <li>PCle Bridge Board - CPBG045 x 2:         <ul> <li>4 x FHHL x16 (Gen5 x16), from PEX89048</li> </ul> </li> <li>PCle Bridge Board - CPBG045 x 2:         <ul> <li>4 x FHHL x16 (Gen5 x16), from PEX89048</li> </ul> </li> <li>PCle Bridge Board - CPBG045 x 2:         <ul> <li>4 x FHHL x16 (Gen5 x16), from PEX89048</li> </ul> </li> <li>PCle Argue Board - CFPG540:         <ul> <li>4 x FHHL x16 (Gen5 x16), from PEX89048</li> </ul> </li> <li>PCle Argue Board - CFPG540:         <ul> <li>4 x FHHL x16 (Gen5 x16), from PEX89048</li> <li>2 x USB 3.2 Gen1 ports (Type-A)</li> <li>1 x VGA port</li> <li>2 x RJ45 ports</li> <li>1 x MLAN port (default)</li> <li>1 x Power button with LED</li> <li>1 x NMI button</li> <li>1 x Reset button</li> <li>1 x Storage activity LED</li> <li>1 x System status LED</li> </ul> </li> <li>MLAN board - CDB66:</li> </ul>	Expansion Slot     F	
Expansion Slot       • PCle Bridge Board - CBG76: <ul> <li>- 8 x FHHL x16 (Gen5 x16), from PEX89104</li> <li>• PCle Bridge Board - CPBG045 x 2:                 <ul></ul></li></ul>	Expansion Slot • F - • • • • • • • • • • • • • • • • • • •	-SD1-AAX5
<ul> <li>8 x FHHL x16 (Gen5 x16), from PEX89104</li> <li>PCle Bridge Board - CPBG045 x 2:         <ul> <li>4 x FHHL x16 (Gen5 x16), from PEX89048</li> </ul> </li> <li>Front I/O</li> <li>1/0 board - CFPG540:         <ul> <li>2 x USB 3.2 Gen1 ports (Type-A)</li> <li>1 x VGA port</li> <li>2 x RJ45 ports</li> <li>1 x MLAN port (default)</li> <li>1 x Power button with LED</li> <li>1 x ID button with LED</li> <li>1 x NMI button</li> <li>1 x Reset button</li> <li>1 x Storage activity LED</li> <li>1 x System status LED</li> </ul> </li> </ul>	Front I/O I/O bo	IVIDIA HGX™ B200 with 8 x SXM GPUs
<ul> <li>PCle Bridge Board - CPBG045 x 2:         <ul> <li>4 x FHHL x16 (Gen5 x16), from PEX89048</li> </ul> </li> <li>I/O board - CFPG540:         <ul> <li>2 x USB 3.2 Gen1 ports (Type-A)</li> <li>1 x VGA port</li> <li>2 x RJ45 ports</li> <li>1 x MLAN port (default)</li> <li>1 x Power button with LED</li> <li>1 x ID button with LED</li> <li>1 x NMI button</li> <li>1 x Reset button</li> <li>1 x Storage activity LED</li> <li>1 x System status LED</li> </ul> </li> </ul>	<ul> <li>Front I/O</li> <li>I/O bo</li> <li>2</li> <li>1</li> <li>2</li> <li>1</li> </ul>	PCle Bridge Board - CBG76:
- 4 x FHHL x16 (Gen5 x16), from PEX89048         Image: Front I/O       I/O board - CFPG540:         • 2 x USB 3.2 Gen1 ports (Type-A)         • 1 x VGA port         • 2 x RJ45 ports         • 1 x MLAN port (default)         • 1 x Power button with LED         • 1 x NMI button         • 1 x Reset button         • 1 x Storage activity LED         • 1 x System status LED	- Front I/O I/O bo • 2 • 1 • 2 • 1 • 1 • 1 • 1 • 1 • 1 • 1 • 1	8 x FHHL x16 (Gen5 x16), from PEX89104
- 4 x FHHL x16 (Gen5 x16), from PEX89048         Image: Front I/O       I/O board - CFPG540:         • 2 x USB 3.2 Gen1 ports (Type-A)         • 1 x VGA port         • 2 x RJ45 ports         • 1 x MLAN port (default)         • 1 x Power button with LED         • 1 x NMI button         • 1 x Storage activity LED         • 1 x System status LED	- Front I/O I/O bo • 2 • 1 • 2 • 1 • 1 • 1 • 1 • 1 • 1 • 1 • 1	
I/O       I/O board - CFPG540:         •       2 x USB 3.2 Gen1 ports (Type-A)         •       1 x VGA port         •       2 x RJ45 ports         •       1 x MLAN port (default)         •       1 x MLAN port (default)         •       1 x NULAN port (default)         •       1 x NMI button         •       1 x Reset button         •       1 x Storage activity LED         •       1 x System status LED	Front I/O I/O bo • 2 • 1 • 2 • 1 • 1 • 1 • 1 • 1 • 1 • 1 • 1	-
<ul> <li>2 x USB 3.2 Gen1 ports (Type-A)</li> <li>1 x VGA port</li> <li>2 x RJ45 ports</li> <li>1 x MLAN port (default)</li> <li>1 x Power button with LED</li> <li>1 x ID button with LED</li> <li>1 x NMI button</li> <li>1 x Reset button</li> <li>1 x Storage activity LED</li> <li>1 x System status LED</li> </ul>	<ul> <li>2</li> <li>1</li> <li>2</li> <li>1</li> </ul>	
<ul> <li>1 x VGA port</li> <li>2 x RJ45 ports</li> <li>1 x MLAN port (default)</li> <li>1 x Power button with LED</li> <li>1 x ID button with LED</li> <li>1 x NMI button</li> <li>1 x Reset button</li> <li>1 x Storage activity LED</li> <li>1 x System status LED</li> </ul> Rear I/O MLAN board - CDB66:	<ul> <li>1</li> <li>2</li> <li>1</li> </ul>	
<ul> <li>2 x RJ45 ports</li> <li>1 x MLAN port (default)</li> <li>1 x Power button with LED</li> <li>1 x ID button with LED</li> <li>1 x NMI button</li> <li>1 x Reset button</li> <li>1 x Storage activity LED</li> <li>1 x System status LED</li> </ul>	<ul> <li>2</li> <li>1</li> </ul>	
<ul> <li>1 x MLAN port (default)</li> <li>1 x Power button with LED</li> <li>1 x ID button with LED</li> <li>1 x NMI button</li> <li>1 x Reset button</li> <li>1 x Storage activity LED</li> <li>1 x System status LED</li> </ul> Rear I/O MLAN board - CDB66:	<ul> <li>1</li> </ul>	
1 x Power button with LED     1 x ID button with LED     1 x ID button with LED     1 x NMI button     1 x Reset button     1 x Reset button     1 x Storage activity LED     1 x System status LED     Rear I/O     MLAN board - CDB66:	<ul> <li>1</li> <li>1</li> <li>1</li> <li>1</li> <li>1</li> <li>1</li> <li>1</li> <li>1</li> <li>1</li> </ul>	
1 x ID button with LED     1 x NMI button     1 x Reset button     1 x Reset button     1 x Storage activity LED     1 x System status LED     Rear I/O     MLAN board - CDB66:	<ul> <li>1</li> <li>1</li> <li>1</li> <li>1</li> <li>1</li> <li>1</li> <li>1</li> </ul>	
1 x NMI button     1 x Reset button     1 x Reset button     1 x Storage activity LED     1 x System status LED     MLAN board - CDB66:	• 1 • 1 • 1 • 1	
1 x Reset button     1 x Storage activity LED     1 x System status LED     MLAN board - CDB66:	• 1 • 1 • 1	
1 x Storage activity LED     1 x System status LED     MLAN board - CDB66:	• 1 • 1	
1 x System status LED     MLAN board - CDB66:	• 1	
Rear I/O MLAN board - CDB66:		• •
		hoard - CDB66
Backplane     Speed and bandwidth: PCIe Gen5 x4 or SATA 6Gb/s	· · · · · · · · · · · · · · · · · · ·	
Board	Board	x MLAN port
	HO B	x MLAN port Speed and bandwidth: PCIe Gen5 x4 or SATA 6Gb/s
Modules - Optional TPM2.0 kit: CTM010	Modules -	x MLAN port Speed and bandwidth: PCIe Gen5 x4 or SATA 6Gb/s x TPM header with SPI interface

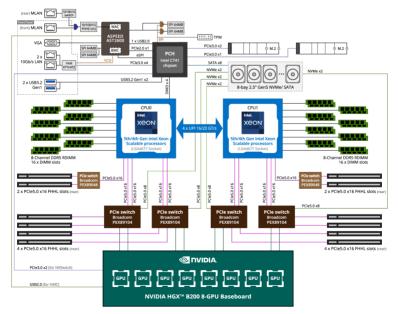
Power Supply	G893-SD1-AAX3
	<ul> <li>8 x 3000W 80 PLUS Titanium redundant power supplies <sup>[1]</sup></li> </ul>
	G893-SD1-AAX5
	<ul> <li>12 x 3000W 80 PLUS Titanium redundant power supplies <sup>[1]</sup></li> </ul>
	<sup>[1]</sup> The system power supply requires C19 power cord.
	<sup>[Note]</sup> GIGABYTE offers PSUs with various efficiency ratings and power outputs. Full redundancy may depend on your server configuration, and alternative PSU options may be needed. Please contact our sales representatives for the best power solution.
	[Note] Please refer to GIGABYTE Website for detail power supply specification.
System	Aspeed® AST2600 Baseboard Management Controller
Management	GIGABYTE Management Console web interface
	Dashboard
	HTML5 KVM
	Sensor Monitor (Voltage, RPM, Temperature, CPU Statusetc.)
	Sensor Reading History Data
	FRU Information
	SEL Log in Linear Storage / Circular Storage Policy
	Hardware Inventory     Fan Profile
	System Firewall     Power Consumption
	Power Control
	Advanced power capping
	<ul> <li>LDAP / AD / RADIUS Support</li> </ul>
	Backup & Restore Configuration
	Remote BIOS/BMC/CPLD Update
	Event Log Filter
	User Management
	Media Redirection Settings
	PAM Order Settings
	SSL Settings
	SMTP Settings
Operating	G893-SD1-AAX3
Properties	<ul> <li>Operating temperature: 10°C to 30°C</li> </ul>
	Operating humidity: 8% to 80% (non-condensing)
	Non-operating temperature: -40°C to 60°C
	<ul> <li>Non-operating humidity: 20% to 95% (non-condensing)</li> </ul>
	G893-SD1-AAX5
	Operating temperature: 10°C to 30°C
	<ul> <li>Operating humidity: 8% to 80% (non-condensing)</li> </ul>
	<ul> <li>Non-operating temperature: -40°C to 60°C</li> </ul>
	Non-operating humidity: 20% to 95% (non-condensing)

### 1-3 System Block Diagram

#### G893-SD1-AAX3

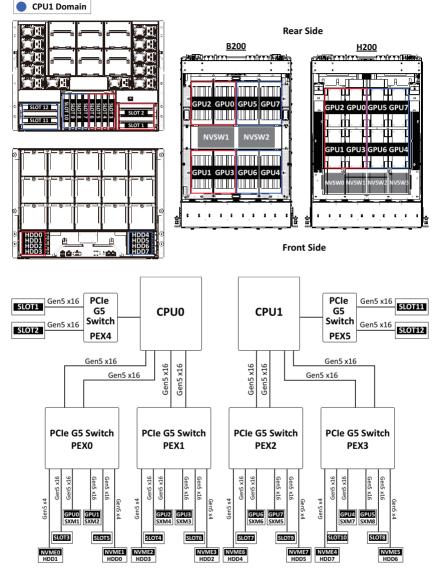


#### G893-SD1-AAX5



### 1-4 PCle Block Diagram

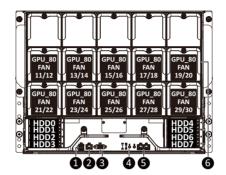
CPU0 Domain



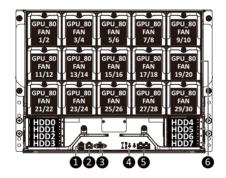
### Chapter 2 System Appearance

### 2-1 Front View

### G893-SD1-AAX3



### G893-SD1-AAX5



#### No. Description

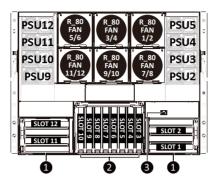
- 1. USB 3.2 Gen1 Port x 2
- 2. Management LAN Port
- 3. VGA Port
- 4. Front Panel LEDs and Buttons
- 5. Data LAN Port x 2
- 6. GPU Tray



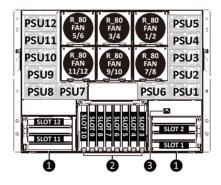
Go to the section 2-4 Front Panel Buttons and LEDs for detail description of function LEDs.

### 2-2 Rear View

### G893-SD1-AAX3



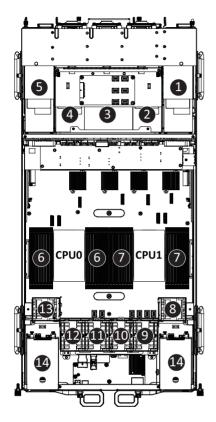
### G893-SD1-AAX5



No.	Description
1.	PCIe Card Cage x 2
2.	PCIe Slot x 8
3.	Management LAN Port

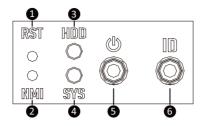
#### NOTE!

• Only one Management LAN (Front/Rear side) can be used at a time.



No.	Description
	Power Supply Unit x 5 (Top)
1.	PCIe Slot x 2 (Bottom)
	Rear_BP_80_FAN_7/8 (Bottom)
2.	Power Supply Unit x 1
	PCIe Slot x 8
3.	Rear_BP_80_FAN_5/6
	Rear_BP_80_FAN_3/4
4.	Power Supply Unit x 1
	Power Supply Unit x 5 (Top)
5.	PCIe Slot x 2 (Bottom)
	Rear_BP_80_FAN_1/2 (Bottom)
6.	CPU0 DDR5 Memory
7.	CPU1 DDR5 Memory
8.	SYS_60_FAN_11/12
9.	SYS_60_FAN_9/10
10.	SYS_60_FAN_7/8
11.	SYS_60_FAN_5/6
12.	SYS_60_FAN_3/4
13.	SYS_60_FAN_1/2
14.	2.5" Storage Bays

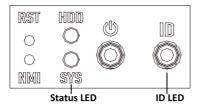
### 2-4 Front Panel LED and Buttons



No.	Name	Color	Status	Description
1.	Reset Button			Press the button to reset the system.
2.	NMI button			Press the button server generates a NMI to the processor if the multiple-bit ECC errors occur, which effectively halt the server.
		Green	On	HDD locate
		Green	Blink	HDD access
3.	HDD Status	Amber	On	HDD fault
	LED	Green/ Amber	Blink	HDD rebuilding
		N/A	Off	No HDD access or no HDD fault.
		Green	On	System is operating normally.
			On	Critical condition, may indicate: System fan failure System temperature
4.	System Status LED <sup>(Note)</sup>	Amber	Blink	Non-critical condition, may indicate: Redundant power module failure Temperature and voltage issue Chassis intrusion
		N/A	Off	System is not ready, may indicate: POST error NMI error Processor or terminator missing
-	Power button	Green	On	System is powered on
5.	with LED	N/A	Off	System is not powered on or in ACPI S5 state (power off)
6.	ID Button <sup>(Note)</sup>			Press the button to activate system identification

(Note) If your server features RoT function, please see the following section for detail LED behavior.

### 2-4-1 RoT LEDs



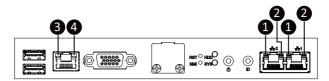
	LED on Front panel(Note5)	
	ID LED	Status LED
EC Firmware (FW) Authentication fail or not exit		
EC FW is broken or not exit (Note1)	OFF	OFF
Authenticating/Recovering BMC/BIOS Images		
Authenticating Images	OFF	OFF
Recovering BMC Active Flash	Blinks Blue 4 times per second	Blinks Green 4 times per second
Recovering BIOS Active Flash	Blinks Blue 4 times per second	Blinks Green 4 times per second
Authentication (AUTH) Pass		
Recovering BIOS Active Flash	OFF	OFF
BMC : AUTH pass after doing recovery BIOS : AUTH pass after doing recovery	OFF	OFF
BMC : AUTH pass after doing recovery BIOS : AUTH pass	OFF	OFF
BMC : AUTH pass BIOS : AUTH pass after doing recovery	OFF	OFF
Active Flash Authentication (AUTH) Fail		
BMC : AUTH Fail <sup>(Note2)</sup>	Blinks Blue 1 time per second	Blinks Green 1 time per second
BIOS : AUTH fail <sup>(Note2)</sup>	Blinks Blue 1 time per second	Blinks Amber 1 time per second

	Blinks Blue	Blinks Green
	2000 2000	2
BMC : AUTH fail after doing recovery <sup>(Note3)</sup>	2 times per	2 times per
<b>3</b> • • • • • • • • • • • • • • • • • • •	second	second
	[ON OFF OFF]	[ON OFF OFF]
	Blinks Blue	Blinks Amber
BIOS : AUTH fail after doing recovery <sup>(Note3)</sup>	2 times per	2 times per
BIOS . AOTH fail after doing recovery	second	second
	[ON OFF OFF]	[ON OFF OFF]
Backup Flash Authentication Fail <sup>(Note4)</sup>		
	Blinks Blue	Blinks Green
	2 times per	2 times per
BMC : AUTH fail	second	second
	[ON OFF	[ON OFF
	ON OFF]	ON OFF]
	Blinks Blue	Blinks Amber
	2 times per	2 times per
BIOS : AUTH fail	second	second
	[ON OFF	[ON OFF
	ON OFF]	ON OFF]

#### NOTE!

- 1. EC FW is broken or not exited result in Microchip CEC1702 cannot load EC FW for authentication.
- 2 (1) Authentication fail include below scenarios Configuration table is missing or modified Public key is missing or modified Protected area or signature is modified Flash empty
- If active flash is still authentication failed after recovery sequence, Microchip CEC1702 stop the process and showing LED behavior.
- If backup flash authentication is failed cause by configuration table, public key or protected area is broken. Microchip CEC1702 stop the process and showing LED behavior.
- Front panel LED is controlled by BMC or Microchip CEC1702. Once Microchip CEC1702 is working(Auth or recovery), the front panel LED is controlled by Microchip CEC1702 and vice versa.

### 2-5 Front Panel System LAN LEDs



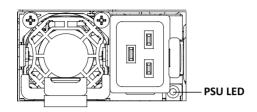
No.	Name	Color	Status	Description					
		Green	On	10 Gbps data rate					
1.	10GbE Speed LED	Yellow	On	5Gbps, 2.5Gbps, 1Gbps data rate					
	00000	N/A	Off	100 Mbps data rate					
		Green	On	Link between system and network or no access					
2.	10GbE Link / Activity LED	Green	Blink	Data transmission or reception is occurring.					
		N/A	N/A Off No data transmission or reception is occ						
		Yellow	On	1 Gbps data rate					
3.	1GbE Speed	Green	On	100 Mbps data rate					
		N/A	Off	10 Mbps data rate					
		Green	On	Link between system and network or no access					
4.	1GbE Link / Activity LED	Green	Blink	Data transmission or reception is occurring.					
	,	N/A	Off	No data transmission or reception is occurring.					

### 2-6 Power Supply Unit (PSU) LED



### NOTE!

The power supply may be vary based on the system configuration.



State	Description
OFF	No AC power to all power supplies
1Hz Green Blinking	AC present / only standby on / Cold redundant mode
2Hz Green Blinking	Power supply firmware updating mode
Amber	AC cord unplugged or AC power lost; with a second power supply in parallel still with AC input power
Amber	Power supply critical event causing shut down: failure, OCP, OVP, fan failure and UVP
1Hz Amber Blinking	Power supply warning events where the power supply continues to operate: high temp, high power, high current and slow fan

### 2-7 Hard Disk Drive LEDs



RAID S	SKU	LED1	Locate	HDD Fault	Rebuilding	HDD Access	HDD Present (No Access)
	Disk LED (LED on	Green	ON(*1)	OFF		BLINK (*2)	OFF
No RAID configuration (via PCH, HBA)	Back Panel)	Amber	OFF	OFF		OFF	OFF
	Removed HDD Slot	Green	ON(*1)	OFF			
	(LED on Back Panel)	Amber	OFF	OFF			
		Green	ON	OFF		BLINK (*2)	OFF
RAID configuration (via HW RAID Card or	Disk LED	Amber	OFF	ON	(Low Speed: 2 Hz)	OFF	OFF
SW RAID Card of		Green	ON(*1)	OFF	(*3)		
	Removed HDD Slot	Amber	OFF	ON	(*3)		

LED 2	HDD Present	No HDD
Green	ON	OFF

NOTE:

\*1: Depends on HBA/Utility Spec.

\*2: Blink cycle depends on HDD's activity signal.

\*3: If HDD is pulled out during rebuilding, the disk status of this HDD is regarded as faulty.

### Chapter 3 System Hardware Installation



#### Pre-installation Instructions

Computer components and electronic circuit boards can be damaged by electrostatic discharge. Working on computers that are still connected to a power supply can be extremely dangerous. Follow the simple guidelines below to avoid damage to your computer or injury to yourself.

- Always disconnect the computer from the power outlet whenever you are working inside the computer case.
- If possible, wear a grounded wrist strap when you are working inside the computer case. Alternatively, discharge any static electricity by touching the bare metal system of the computer case, or the bare metal body of any other grounded appliance.
- Hold electronic circuit boards by the edges only. Do not touch the components on the board unless it is necessary to do so. Do not flex or stress the circuit board.
- Leave all components inside the static-proof packaging until you are ready to use the component
  for the installation.

### 3-1 Removing and Installing the Chassis Top Cover

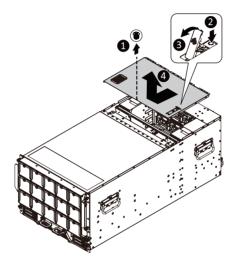


Before you remove or install the chassis top cover

• Make sure the system is not turned on or connected to AC power.

#### Follow these instructions to remove/install the chassis top cover:

- 1. Remove the screw securing the chassis cover.
- 2. Push button to unlock the handle.
- 3. Pull the grip handle to open the panel cover.
- Slide the cover towards the front of the system and then remove the cover in the direction indicated by the arrow.
- 5. Follow steps 1-4 in reverse order to re-install the chassis cover



### 3-2 Removing and Installing the GPU Tray

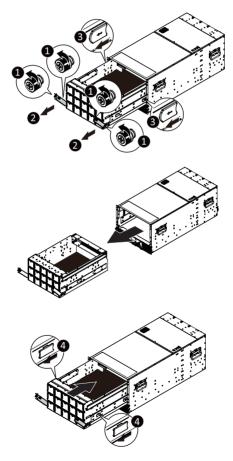


Before you remove or install the GPU tray:

Make sure the system is not turned on or connected to AC power.

#### Follow these instructions to remove/install the GPU tray:

- 1. Loosen the thumbnail screw securing the handles on both sides of the system.
- Pull the grip handles on both sides of the system slide the tray to the front of the system at the same time to pull out the tray.
- 3. Slide the white latch on both sides of the tray rail and carefully remove the GPU tray.
- 4. To reinstall the GPU tray, align it with the rails on both sides and push the blue latches on each side of the tray rail backward to slide it into the system. Then, reverse steps 1-2 to secure the GPU tray in position.



### 3-3 Removing and Installing the Motherboard Tray

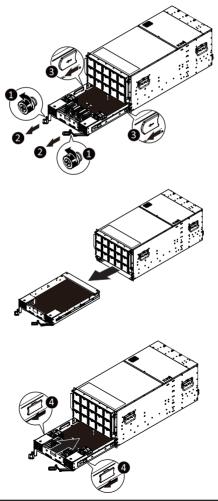


Before you remove or install the Motherboard tray:

Make sure the system is not turned on or connected to AC power.

#### Follow these instructions to remove/install the Motherboard tray:

- 1. Loosen the thumbnail screw securing the handles on both sides of the system.
- 2. Pull the grip handles on both sides of the system slide the tray to the front of the system at the same time to pull out the tray.
- 3. Slide the white latch on both sides of the tray rail and carefully remove the Motherboard tray.
- 4. To reinstall the Motherboard tray, align it with the rails on both sides and push the blue latches on each side of the tray rail backward to slide it into the system. Then, reverse steps 1-2 to secure the Motherboard tray in position.



### 3-4 Removing the Heat Sink



Read the following guidelines before you begin to remove/install the heat sink:

- Always turn off the computer and unplug the power cord from the power outlet before installing the heat sink to prevent hardware damage.
- Unplug all cables from the power outlets.
- · Disconnect all telecommunication cables from their ports.
- Place the system unit on a flat and stable surface.
- · Open the system according to the instructions.

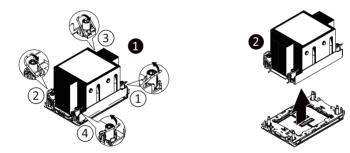


#### WARNING!

Failure to turn off the server before you start installing components may cause serious damage. Do not attempt the procedures described in the following sections unless you are a qualified service technician.

#### Follow these instructions to remove/install the heat sink:

- Loosen the captive screws securing the heat sink in place in reverse order (4→3→2→1). Move the rotating wires into the unlatch position.
- 2. Lift and remove the heat sink from the system.
- 3. To reinstall the heat sink reverse steps 1-2 while ensuring that you tighten the captive screws in sequential order  $(1\rightarrow 2\rightarrow 3\rightarrow 4)$ .



When installing the heat sink to CPU, use T30-Lobe driver to tighten 4 captive nuts in sequence as 1-4. Please refer to the Heat Sink Label for the screw tightening torque value.

• To ensure the system operates properly, make sure the heat sink is seated on the processor firmly.

### 3-5 Installing the CPU



Read the following guidelines before you begin to install the CPU:

- •Make sure that the motherboard supports the CPU.
- Always turn off the computer and unplug the power cord from the power outlet before installing the CPU to prevent hardware damage.
- •Unplug all cables from the power outlets.
- •Disconnect all telecommunication cables from their ports.
- •Place the system unit on a flat and stable surface.
- •Open the system according to the instructions.



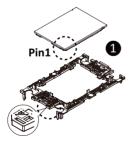
#### WARNING!

Failure to properly turn off the server before you start installing components may cause serious damage. Do not attempt the procedures described in the following sections unless you are a qualified service technician.

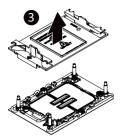
#### Follow these instructions to Install the CPU:

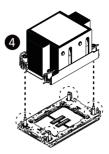
- Align and install the processor on the carrier. NOTE: Apply thermal compound evenly on the top of the CPU. Remove the protective cover from the underside of the heat sink.
- 2. Carefully flip the heat sink cover. Then install the carrier assembly on the bottom of the heat sink and make sure the gold arrow is located in the correct direction.
- Remove the CPU cover.
   NOTE: Save the CPU cover in the event that you need to remove the CPU from the socket.
- 4. Align the heat sink with the CPU socket by the guide pins and make sure the gold arrow is located in the correct direction. Then place the heat sink onto the top of the CPU socket.
- Position the rotating wires into the latch position. Tighten the screws in sequential order (1→2→3→4).

**NOTE:** When dissembling the heat sink, loosen the screws in reverse order  $(4 \rightarrow 3 \rightarrow 2 \rightarrow 1)$  and then move the rotating wires into the unlatch position.











**Carrier Types used for Package Types** 

Package Type	Xeon <sup>®</sup> SP XCC	Xeon <sup>®</sup> SP MCC	Xeon <sup>®</sup> SP+HBM
Carrier Code	E1A	E1B	E1C

NOTE!

- The carrier code is marked on each carrier and matches a code laser marked on to the IHS(Integrated Heat Spreader) to ensure the right parts are used together
- When installing the Heat Sink to CPU, use T30-Lobe driver to tighten 4 captive nuts in sequence as 1-4.
- Please refer to the Heat Sink Label for the screw tightening torque value.

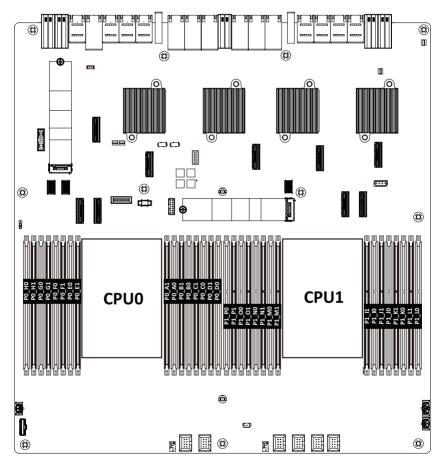
### 3-6 Installing the Memory

Read the following guidelines before you begin to install the memory:

- Make sure that the motherboard supports the memory. It is recommended that memory of the same capacity, brand, speed, and chips be used.
- Always turn off the computer and unplug the power cord from the power outlet before installing the memory to prevent hardware damage.
- Memory modules have a foolproof design. A memory module can be installed in only one direction. If you are unable to insert the memory, switch the direction.

### 3-6-1 Eight Channel Memory Configuration

This motherboard provides 32 DDR5 memory slots and supports 8-Channel Technology. After the memory is installed, the BIOS will automatically detect the specifications and capacity of the memory.



### 3-6-2 Installing the Memory



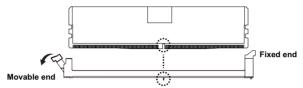
Before installing a memory module, make sure to turn off the computer and unplug the power cord from the power outlet to prevent damage to the memory module.

Be sure to install DDR5 DIMMs on this motherboard.

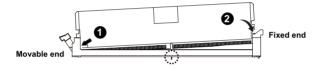
Make sure your DIMM slots have a single latch or a double latch.

#### Follow these instructions to install a DIMM module with Single Latch :

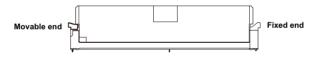
1. Open the plastic latch of the memory slot, then place the memory module as pre-inserted vertically position.



 Hold it with both hands, insert the memory module into the movable end first, and then insert the memory module into the fixed end.



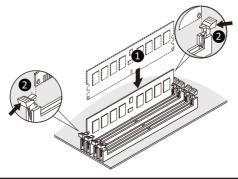
 Then use both hands to insert the memory module vertically into the DIMM slot and push it down. Close the plastic latch at the edge of the DIMM slots to lock the memory module.



4. Reverse the installation steps when you want to remove the memory module.

#### Follow these instructions to install a DIMM module with Double Latch:

- 1. Insert the DIMM memory module vertically into the DIMM slot and push it down.
- 2. Close the plastic clip at both edges of the DIMM slots to lock the DIMM module.
- 3. Reverse the installation steps when you want to remove the DIMM module.



### 3-6-3 DIMM Population Table

Туре	Ranks Per DIMM and	DIM	M Capacity	(GB)	Speed (MT/s); Voltage (V); DIMM per Channel (DPC)					
	Data Width				1DPC <sup>1</sup>	2DPC				
		16Gb	24Gb <sup>2</sup>	36Gb	1.1	V				
	SRx8 (RC D)	16GB	24GB	NA						
	SRx4 (RC C)	32GB	48GB	NA	1					
RDIMM	SRx4 (RC F) 9x4	32GB	NA	NA	1					
RDIMM	DRx8 (RC E)	32GB	48GB	NA	4000	4400				
	DRx4 (RC A)	64GB	96GB	128GB	4800	4400				
	DRx4 (RC B) 9x4	64GB	NA	NA	1					
	(4R/8R)x4	2H-128GB		NIA						
RDIMM 3DS	(RC A)	4H-256GB	NA	NA						

#### 4th Gen Intel Xeon Scalable Processors Memory Support

NOTE

1. IDPC applies to 1SPC or 2SPC implementations (SPC - Sockets Per Channel)
 2. 24Gb XCC only w/ limited configs: 1DPC all DIMM types, 2DPC 96GB only, Only 8 and 16 DIMM configs, no fallbacks.

#### 5th Gen Intel Xeon Scalable Processors Memory Support

Туре	Ranks Per DIMM and Data Width	DIM	M Capacity	(GB)	Speed (MT/s); Voltage (V); DIMM per Channel (DPC)						
		16Gb	24Gb <sup>2</sup>	36Gb	1.1						
	SRx8 (RC D)	16GB	24GB	NA							
	SRx4 (RC C)	32GB	48GB	NA	1						
RDIMM	SRx4 (RC F) 9x4	NA	NA	NA	5600 <sup>3</sup>						
RDIIVIIVI	DRx8 (RC E)	32GB	48GB	NA	0000	4400 <sup>3</sup>					
	DRx4 (RC A)	64GB	96GB	128GB		4400*					
	DRx4 (RC B) 9x4	NA	NA	NA	1						
RDIMM 3DS	(4R/8R)x4	2H-128GB	NA	NA	5600 <sup>4</sup>						
	(RC A)	4H-256GB		INA	0000						

NOTE:

1. 1DPC applies to 1SPC or 2SPC implementations (SPC - Sockets Per Channel)

2. 24Gb 2DPC not POR w/ 24GB and 48GB DIMMs.

3. DDR5-5600 RDIMMs will be limited to 5600 MT/s 1DPC and 4400 MT/s 2DPC. DDR5-4800 DIMMs will be limited to 4800 MT/s 1DPC and 4400 MT/s 2DPC

4. DDR5-5600 DIMMS are required for 5600 and 5200 1DPC speeds.

### 3-6-4 Processor and Memory Module Matrix Table

Memory Q'ty	CPU0													CPU1																		
for each CPU	HO	H1	G0	G1	F0	F1	E0	E1	A1	A0	B1	B0	<b>C1</b>	C0	D1	D0	P0	P1	00	01	NO	N1	MO	M1	11	10	J1	JO	К1	к0	L1	L0
1 DIMM										v																v						
2 DIMM			v							v									v							v						
4 DIMM			v				v			v				v					v				v			v				v		
6 DIMM			v		v		v			v				v		v			v		v		v			v				v		v
8 DIMM	v		v		v		v			v		v		v		v	v		v		v		v			v		v		v		v
12 DIMM	v		v	v	v		v	v	v	v		v	v	v		v	v		v	v	v		v	v	v	v		v	v	v		v
16 DIMM	v	v	v	v	v	v	v	v	v	v	v	v	v	v	v	v	v	v	v	v	v	v	v	v	v	v	v	v	v	v	v	v

### 3-7 Installing the PCI Expansion Card



Voltages can be present within the server whenever an AC power source is connected. This voltage is present even when the main power switch is in the off position. Ensure that the system is powered-down and all power sources have been disconnected from the server prior to installing a PCIe card.

· Failure to observe these warnings could result in personal injury or damage to equipment.

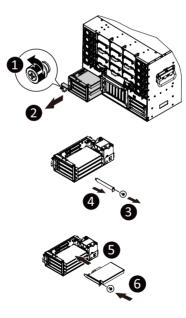
#### Follow these instructions for a PCI Expansion card:

#### Left PCIe Card Cage

- 1. Loosen the thumbnail screw securing the handle of the PCIe card cage.
- 2. Pull the cage out of the system.
- 3. Remove the screw securing the slot cover to the riser bracket.
- 4. Remove the slot cover from the riser bracket.
- Orient the PCIe card with the riser guide slot and push it towards the arrow until it is securely seated in the PCIe card connector.
   NOTE: Some riser brackets allow for single or multiple PCIe cards.

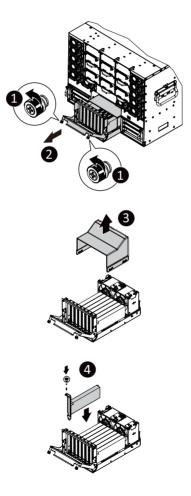
Repeat steps 3-5 as necessary.

- 6. Secure the PCIe card with the screw.
- 7. Reverse steps 1-2 to reinstall the PCIe card cage in position.



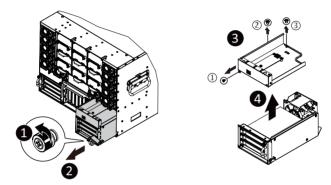
#### Middle PCIe Card Cage

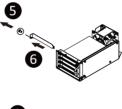
- 1. Loosen the thumbnail screws securing the handle of the PCIe card cage.
- 2. Pull the cage out of the system.
- 3. Lift the fan duct and remove it.
- Align the PCIe card with the slot and push it towards the arrow until it is securely seated in the PCIe card connector. Then, secure the PCIe card with the screw.
   NOTE: Some riser brackets allow for single or multiple PCIe cards.
   Repeat step 4 as necessary.
- 5. To install the PCIe card cage, push the cage back into the system. Reverse the previous steps to remove the PCIe card.

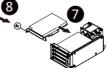


#### Right PCIe Card Cage

- 1. Loosen the thumbnail screw securing the handle of the PCIe card cage.
- 2. Pull the cage out of the system.
- 3. Remove the screws securing the MLAN tray, in the specified sequence.
- 4. Lift the MLAN tray and remove it.
- 5. Remove the screw securing the slot cover to the riser bracket.
- 6. Remove the slot cover from the riser bracket.
- Orient the PCIe card with the riser guide slot and push it towards the arrow until it is securely seated in the PCIe card connector.
   NOTE: Some riser brackets allow for single or multiple PCIe cards.
- Repeat steps 5-7 as necessary.
- 8. Secure the PCIe card with the screw.
- 9. Reverse steps 1-4 to reinstall the PCIe card cage in position.







# 3-8 Installing the Hard Disk Drive

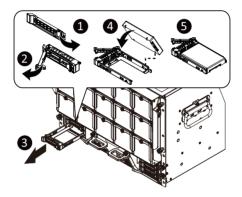


Read the following guidelines before you begin to install the hard disk drive:

- Take note of the drive tray orientation before sliding it out.
- The tray will not fit back into the bay if inserted incorrectly.
- Make sure that the hard disk drive is connected to the hard disk drive connector on the backplane.

### Follow these instructions to install a 2.5" hard disk drive:

- 1. Press the release button.
- 2. Extend the locking lever.
- 3. Pull the locking lever in the direction indicated to remove the HDD tray.
- 4. Align the hard disk drive with the positioning stud on the HDD tray.
- 5. Slide the hard disk drive into the HDD tray.
- 6. Reinsert the HDD tray into the slot and close the locking lever.



# 3-9 Replacing the System Fan Module



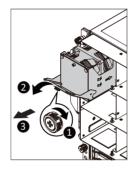
#### CAUTION!

Before you remove or install the system fans follow these steps:

 Disconnect all necessary cable connections. Failure to observe these warnings could result in personal injury or damage to the equipment

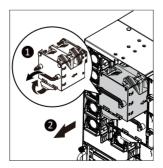
#### Follow these instructions to replace the GPU fan assembly:

- 1. Loosen the thumbnail screw securing the handle of the fan module.
- 2. Flip the handle and then grasp it firmly.
- 3. Pull out the fan module from the system.
- 4. Reverse the previous steps to install the replacement fan module.



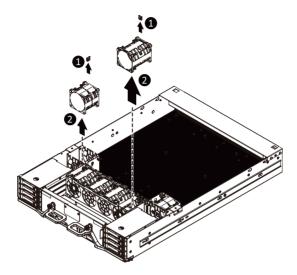
#### Follow these instructions to replace the fan assembly:

- 1. Flip and grasp the handle and simultaneously press the retaining clip on the bottom side of the fan module in the direction indicated.
- 2. Pull out the fan module from the system.
- 3. Reverse the previous steps to install the replacement fan module.



#### Internal System Fan

- 1. Remove the edge saddle by pulling it away from the fan assembly.
- 2. Lift the fan assembly from the chassis.
- 3. Reverse the previous steps to install the replacement fan assembly.



# 3-10 Removing and Installing the Power Supply

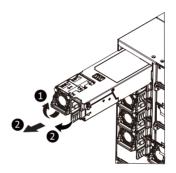


#### CAUTION!

Please see Section 2-2 "Rear View" for installation sequence.

#### Follow these instructions to replace the power supply:

- 1. Flip and then grasp the power supply handle.
- 2. Press the retaining clip on the right side of the power supply in the direction indicated.
- 3. Pull out the power supply using the handle.
- Insert the replacement power supply firmly into the chassis. Connect the AC power cord to the replacement power supply.



# 3-11 Installing the System into the Cabinet

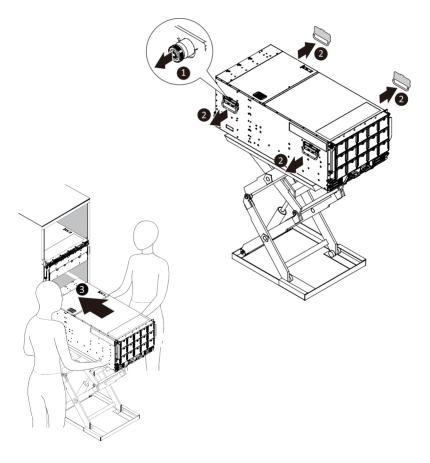


Read the following guidelines before you begin to install the system into the cabinet:

- Make sure the system is not turned on or connected to AC power.
- A Lift Table is required. Place the system unit on Lift Table. Recommended load capacity for the lift table: 200 kilograms.
- Four Person lift required. Firmly hold the bottom of the system when required to lift and carry the system.
- · Failure to observe these warnings could result in personal injury or damage to the equipment.

#### Follow these instructions to install the system into the cabinet:

- 1. Pull out and release the thumbnail screw securing the chassis handle in place.
- 2. Remove the four handles on each side of the system.
- 3. Carefully slide the system into the cabinet.



# 3-12 Removing the System from the Cabinet

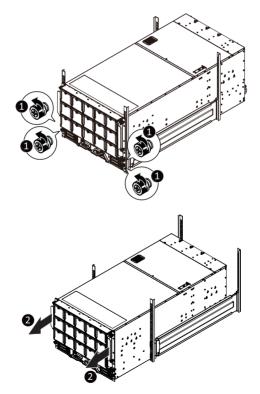


Read the following guidelines before you begin to remove the system from the cabinet:

- Always turn off the computer and unplug the power cord from the power outlet before removing the system from the cabinet.
- Disconnect all necessary cable connections.
- A Lift Table is required. Place the system unit on Lift Table. Recommended load capacity for the lift table: 200 kilograms.
- Four Person lift required. Firmly hold the bottom of the system when required to lift and carry the system.
- Failure to observe these warnings could result in personal injury or damage to the equipment.

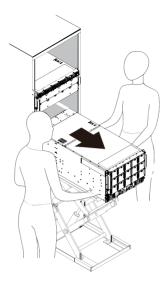
#### Follow these instructions to remove the system from the cabinet:

- 1. Loosen the thumbnail screws on each side that secure the system.
- 2. Gently pull out the system from the cabinet and place it on Lift table.



### 

- The illustrations are for reference only.
  - The actual slide rail may vary depending on your purchase.

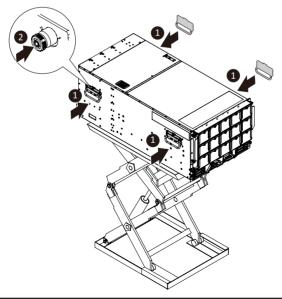




Before lifting the system, installing the four chassis handles on the system is required.

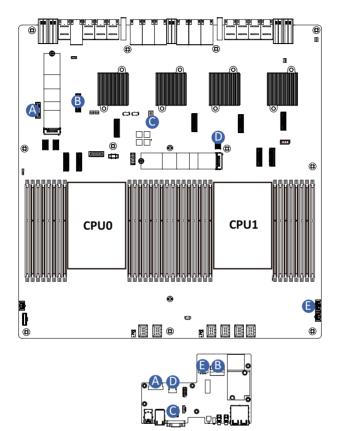
### Follow these instructions to install the chassis handles on the system:

- 1. Attach the four chassis handles to the system.
- 2. Push and lock the thumbnail screw to secure the chassis handle in place.

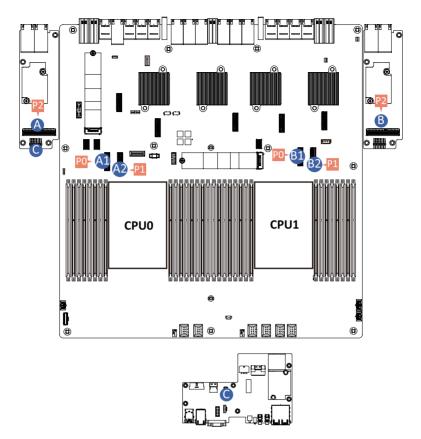


System Hardware Installation - 43 -

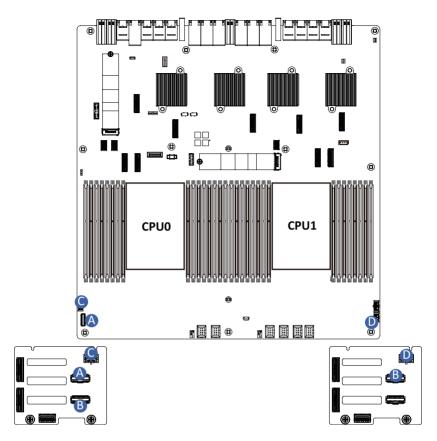
# 3-13 Cable Connection



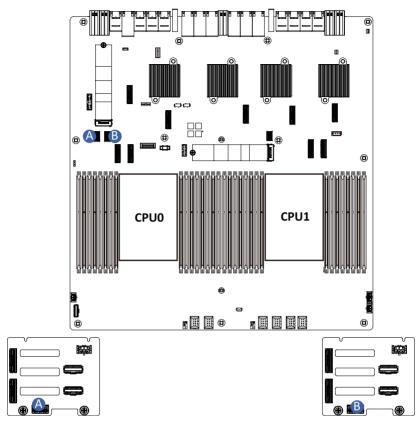
A	Front USB 3.0 Cable	Motherboard: F_USB3	
		Front IO Board: F_USB3_CON	
в	Front IO Decord Circul Cable	Motherboard: FP_IO	
В	Front IO Board Signal Cable	Front IO Board: FP_IO	
с	Front IO VGA Cable	Motherboard: VGA_CON	
		Front IO Board: F_VGA1_CON	
	D Front IO Board LAN Cable	Motherboard: FP_LAN	
		Front IO Board: FP_MLAN	
E	Front IO Board Power Cable	Motherboard: FP_PWR	
	Toncio Board Towel Cable	Front IO Board: P12V_2	



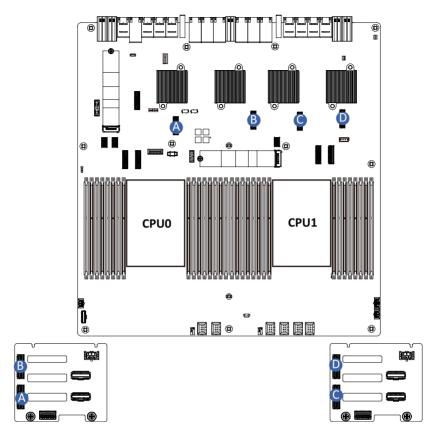
	Rear LAN to Motherboard Signal Cable	Rear LAN Board: U2_PE1	
A		Motherboard: A1: U2_P0_PE1A A2: U2_P0_PE1B	
		Rear LAN Board: U2_PE1	
В	Rear LAN to Motherboard Signal Cable	Motherboard: B1: U2_P1_PE1A B2: U2_P1_PE1B	
с	Rear LAN to Front IO LAN Signal Cable	Rear LAN Board: REAR_MLAN	
		Front IO Board: CN_LAN_F	



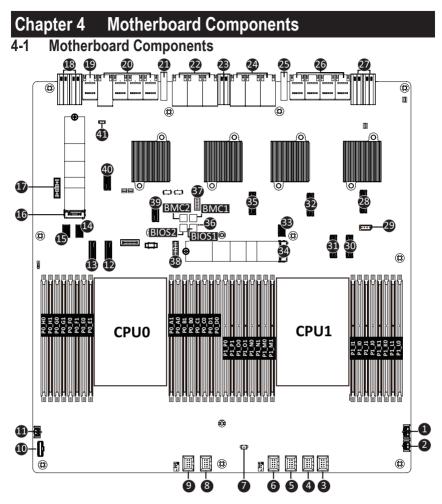
Α	HDD Backplane Board Signal Cable	Motherboard: BP_1
A .	HDD Backplane Board Signal Cable	Front HDD Board: BP_1
в	HDD Backplane Board Signal Cable	Left Front HDD Board: BP_SERIES
D	HDD Backplane Board Signal Cable	Right Front HDD Board: BP_1
с	HDD Backplane Board Power Cable	Motherboard: BPB_PWR1
		Left Front HDD Board: BP_PWR
D	HDD Backplane Board Power Cable	Motherboard: BPB_PWR2
		Right Front HDD Board: BP_PWR



ſ	Α	SATA Cable	Motherboard: SL_CN1
	~	SAIA Cable	Left Front HDD Board: SL_CN1
ſ	в	SATA Cable	Motherboard: SL_CN2
	Б	SATA Cable	Right Front HDD Board: SL_CN1



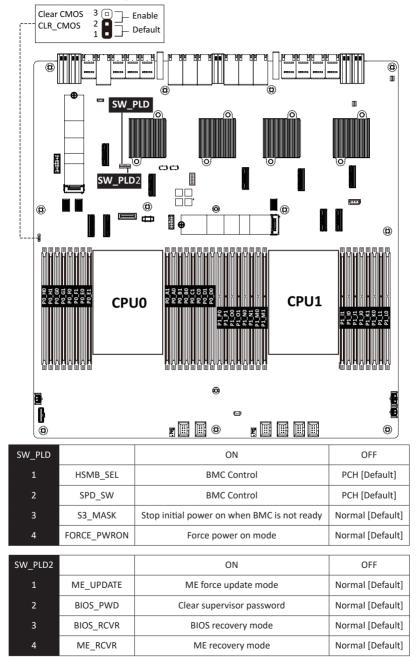
Α	NVMe 0-1	Motherboard: U2_PEX0	с	NVMe 4-5 Cable	Motherboard: U2_PEX2
	Cable	Front HDD Board: U2_0	0		Front HDD Board: U2_0
в	NVMe 2-3	Motherboard: U2_PEX1	р	D NVMe 6-7 Cable	Motherboard: U2_PEX3
	Cable	Front HDD Board: U2_1			Front HDD Board: U2_1



Item	Description
1	2 x 4 Pin Front Panel Power Connector (FP_PWR)
2	2 x 3 Backplane Power Connector (BPB_PWR2)
3	FAN_11/FAN_12 Connector
4	FAN_9/FAN_10 Connector
5	FAN_7/FAN_8 Connector
6	FAN_5/FAN_6 Connector
7	Battery Cable Connector
8	FAN_3/FAN_4 Connector
9	FAN_1/FAN_2 Connector
10	HDD Backplane Board Connector
11	2 x 3 Backplane Power Connector (BPB_PWR1)
12	MCIO Connector (U2_P0_PE1B/PCIe Gen5)

Item	Description	
13	MCIO Connector (U2_P0_PE1A/PCIe Gen5)	
14	SlimLine Connector (HDD Backplane Board SATA Signal/SL_CN2)	
15	SlimLine Connector (HDD Backplane Board SATA Signal/SL_CN1)	
16	M.2 Slot (PCIe Gen3 x2, Support NGFF-22110)	
17	Front USB 3.2 Gen1 Connector	
18	Motherboard Power Connector (MB_PWR1/MB_PWR2)	
19	Power Distribution Board Connector (PDB_IO)	
20	PCIe Signal Connector (EX_SXMJ3-6)	
21	Guide Pin Connector (GP1)	
22	PCIe Signal Connector (EX_SLT1_3/EX_SLT2_3/EX_SLT4)	
23	PCIe Bridge Board Power Connector (PCIE_PWR1)	
24	PCIe Signal Connector (EX_SLT5_6/EX_SLT6_7/EX_SLT8)	
25	Guide Pin Connector (GP2)	
26	PCIe Signal Connector (EX_SXMJ7-10)	
27	Motherboard Power Connector (MB_PWR3/MB_PWR4)	
28	MCIO Connector (U2_PEX3/PCIe Gen5)	
29	IPMB Connector	
30	MCIO Connector (U2_P1_PE1B/PCIe Gen5)	
31	MCIO Connector (U2_P1_PE1A/PCIe Gen5)	
32	MCIO Connector (U2_PEX2/PCIe Gen5)	
33	SlimLine Connector (for MLAN/FP_LAN)	
34	M.2 Slot (PCIe Gen3 x1, Support NGFF-22110)	
35	MCIO Connector (U2_PEX1/PCIe Gen5)	
36	BMC Firmware Readiness LED	
37	VGA/Serial Port Header	
38	TPM Module Connector	
39	MCIO Connector (U2_PEX0/PCIe Gen5)	
40	MCIO Connector (for System I/O/FP_IO)	
41	VROC Module Connector	

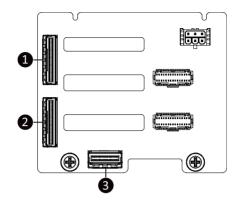
# 4-2 Jumper Setting



Motherboard Components

# 4-3 Backplane Board Storage Connector

# 4-3-1 CBPG641



Item	Description
1.	MCIO 8i (SFF-TA-1016 / U2_1
2.	MCIO 8i (SFF-TA-1016 / U2_0)
3.	MCIO 4i (SFF-TA-1016 / SL_CN1)

# Chapter 5 BIOS Setup

BIOS (Basic Input and Output System) records hardware parameters of the system in the EFI on the motherboard. Its major functions include conducting the Power-On Self-Test (POST) during system startup, saving system parameters, loading the operating system etc. The BIOS includes a BIOS Setup program that allows the user to modify basic system configuration settings or to activate certain system features. When the power is turned off, the battery on the motherboard supplies the necessary power to the CMOS to keep the configuration values in the CMOS.

To access the BIOS Setup program, press the <DEL> key during the POST when the power is turned on.



BIOS flashing is potentially risky, if you do not encounter any problems when using the current BIOS version, it is recommended that you don't flash the BIOS. To flash the BIOS, do it with caution. Inadequate BIOS flashing may result in system malfunction.

 It is recommended that you not alter the default settings (unless you need to) to prevent system instability or other unexpected results. Inadequately altering the settings may result in system's failure to boot. If this occurs, try to clear the CMOS values and reset the board to default values. (Refer to the Exit section in this chapter or introductions of the battery/clearing CMOS jumper in Chapter 4 for how to clear the CMOS values.)

#### **BIOS Setup Program Function Keys**

	•
<←><→>	Move the selection bar to select the screen
<↑><↓>	Move the selection bar to select an item
<+>	Increase the numeric value or make changes
<->	Decrease the numeric value or make changes
<enter></enter>	Execute command or enter the submenu
<esc></esc>	Main Menu: Exit the BIOS Setup program
	Submenus: Exit current submenu
<f1></f1>	Show descriptions of general help
<f3></f3>	Restore the previous BIOS settings for the current submenus
<f9></f9>	Load the Optimized BIOS default settings for the current submenus
<f10></f10>	Save all the changes and exit the BIOS Setup program

### Main

This setup page includes all the items of the standard compatible BIOS.

### Advanced

This setup page includes all the items of AMI BIOS special enhanced features.

(ex: Auto detect fan and temperature status, automatically configure hard disk parameters.)

### Chipset

This setup page includes all the submenu options for configuring the functions of the Platform Controller Hub.

### Server Management

Server additional features enabled/disabled setup menus.

### Security

Change, set, or disable supervisor and user password. Configuration supervisor password allows you to restrict access to the system and BIOS Setup.

A supervisor password allows you to make changes in BIOS Setup.

A user password only allows you to view the BIOS settings but not to make changes.

### Boot

This setup page provides items for configuration of the boot sequence.

### Save & Exit

Save all the changes made in the BIOS Setup program to the CMOS and exit BIOS Setup. (Pressing <F10> can also carry out this task.)

Abandon all changes and the previous settings remain in effect. Pressing <Y> to the confirmation message will exit BIOS Setup. (Pressing <Esc> can also carry out this task.)

# 5-1 The Main Menu

Once you enter the BIOS Setup program, the Main Menu (as shown below) appears on the screen. Use arrow keys to move among the items and press <Enter> to accept or enter other sub-menu.

#### Main Menu Help

The on-screen description of a highlighted setup option is displayed on the bottom line of the Main Menu.

#### Submenu Help

While in a submenu, press <F1> to display a help screen (General Help) of function keys available for the menu. Press <Esc> to exit the help screen. Help for each item is in the Item Help block on the right side of the submenu.



When the system is not stable as usual, select the **Restore Defaults** item to set your system to its defaults.

The BIOS Setup menus described in this chapter are for reference only and may differ by BIOS version.

Main Advanced Chipset Serve	Aptio Setup – AMI r Mgmt Security Boot Save & Ex	it		
BIOS Information Project Name Project Version Build Date and Time	MSB3-PE0-000 D04 10/28/2024 01:49:08	1		
BMC Information BMC Firmware Version	13.06.11			
Processor Information CPU 0 Brand String	INTEL(R) XEON(R) PLATINUM 8592+			
CPU 1 Brand String	INTEL(R) XEON(R) PLATINUM 8592+			
Max CPU Speed	1900 MHz	→+: Select Screen		
CPU Signature	C06F2	↑↓: Select Item		
Processor Core	128	K/M: Scroll Help Area		
Microcode Patch	21000283	Up/Down.		
		Enter: Select		
Platform Information		+/-: Change Opt.		
Processor	EMR-SP Ax	F1: General Help		
PCH	EBG - B1	F3: Previous Values		
RC Revision	111.D23	F9: Optimized Defaults		
		F10: Save & Exit		
Memory Information		ESC: Exit		
Total Memory	2097152 MB			
Version 2.22.1290 Copyright (C) 2024 AMI				

Main Advanced Chipset Serv	Aptio Setup – AMI er Mgmt Security Boot Save	& Exit
Processor Core Microcode Patch	128 21000283	<ul> <li>Set the Time. Use Tab to switch between Time elements.</li> </ul>
Platform Information		
Processor	EMR-SP AX	
PCH	EBG - B1	
RC Revision	111.D23	
Memory Information		
Total Memory	2097152 MB	
Usable Memory	2097152 MB	
Memory Frequency	4400 MHz	
Onboard LAN Information		++: Select Screen
LAN1 MAC Address	10-FF-E0-30-99-A6	↑↓: Select Item
LAN2 MAC Address	10-FF-E0-30-99-A7	K/M: Scroll Help Area
		Up/Down.
CPLD Firmware version Unknown	NA	Enter: Select
		+/-: Change Opt.
Unknown Unknown	NA NA	F1: General Help F3: Previous Values
Unknown	NH	F9: Optimized Defaults
UNKNOWN	NH	F10: Save & Exit
System Date	[Mon 12/30/2024]	ESC: Exit
System Time	[14:59:56]	LOG. EXIC
	[14.35.36]	

Parameter	Description
BIOS Information	
Project Name	Displays the project name information.
Project Version	Displays version number of the BIOS setup utility.
Build Date and Time	Displays the date and time when the BIOS setup utility was created.
BMC Information <sup>(Note1)</sup>	
BMC Firmware Version <sup>(Note1)</sup>	Displays BMC firmware version information.
Processor Information	
CPU Brand String/ Max CPU Speed / CPU Signature / Processor Core / Microcode Patch	Displays the technical information for the installed processor(s).
Platform Information	
Processor/ PCH/ RC Revision	Displays the information of the installed processor(s) and PCH.
Memory Information(Note2)	
Total Memory	Displays the total memory size of the installed memory.
Usable Memory	Displays the usable memory size of the installed memory.

(Note1) Functions available on selected models.

(Note2) This section will display capacity and frequency information of the memory that the customer has installed.

Parameter	Description	
Memory Frequency	Displays the frequency information of the installed memory.	
Onboard LAN Information <sup>(Note3)</sup>		
LAN# MAC Address	Displays LAN MAC address information.	
System Date	Sets the date following the weekday-month-day-year format.	
System Time	Sets the system time following the hour-minute-second format.	

(Note3) The number of LAN ports listed will depend on the motherboard / system model.

# 5-2 Advanced Menu

The Advanced Menu displays submenu options for configuring the function of various hardware components. Select a submenu item, then press <Enter> to access the related submenu screen.



Version 2.22.1290 Copyright (C) 2024 AMI

### 5-2-1 Trusted Computing

Advanced	Aptio Setup – AMI	
Configuration TPH v1.2 Support NO Security Device Found	(Enable)	Enables or Disables BIOS support for security device. 0.S. will not sho Security Device. TCG EFI protocol and INTIA interface will not be available.
		++: Select Screen 1: Select Item K/M: Scroll Help Area Up/Down. Enter: Select +/-: Change Opt. F1: General Help F3: Previous Values F9: Optimized Defaults F10: Save & Exit ESC: Exit
Versi	on 2.22.1290 Copyright (C) 20	)24 AMI

Parameter	Description
Configuration	
TPM v1.2 Support	Enable/Disable BIOS support for security device. OS will not show security device. TCG EFI protocol and INT1A interface will not be available. Options available: Disable, Enable. Default setting is <b>Enable</b> .

### 5-2-2 Serial Port Console Redirection

Advanced	Aptio Setup – AMI	
COM1 Console Redirection Serial Port for Out-of-Band Manage Windows Emergency Management Serv Console Redirection EMS Console Redirection Settings		Console Redirection Enable or Disable.
		++: Select Screen 11: Select Item K/M: Scroll Help Area Up/Down. Enter: Select +/-: Change Opt. F1: General Help F3: Previous Values F9: Optimized Defaults F10: Save & Exit ESC: Exit
Versio	n 2.22.1290 Copyright (C) 20	D24 AMI

Parameter	Description	
COM1 Console Redirection <sup>(Note)</sup>	Console redirection enables the users to manage the system from a remote location. Options available: Enabled, Disabled. Default setting is <b>Disabled</b> .	
COM1 Console Redirection Settings	<ul> <li>Press [Enter] to configure advanced items.</li> <li>Please note that this item is configurable when COM1 Console</li> <li>Redirection is set to Enabled.</li> <li>Terminal Type <ul> <li>Selects a terminal type to be used for console redirection.</li> <li>Options available: VT100, VT100PLUS, VT-UTF8, ANSI. Default setting is VT100PLUS.</li> </ul> </li> <li>Bits per second <ul> <li>Selects the transfer rate for console redirection.</li> <li>Options available: 9600, 19200, 38400, 57600, 115200. Default setting is 115200.</li> </ul> </li> <li>Data Bits <ul> <li>Selects the number of data bits used for console redirection.</li> <li>Options available: 7, 8. Default setting is 8.</li> </ul> </li> </ul>	

(Note) Advanced items prompt when this item is defined.

Parameter	Description		
COM1 Console Redirection Settings (continued)	<ul> <li>Parity         <ul> <li>A parity bit can be sent with the data bits to detect some transmission errors.</li> <li>Even: parity bit is 0 if hum of 1's in the data bits is even.</li> <li>Odd: parity bit is 0 if num of 1's in the data bits is odd.</li> <li>Mark: parity bit is always 1. Space: Parity bit is always 0.</li> <li>Mark and Space Parity do not allow for error detection.</li> <li>Options available: None, Even, Odd, Mark, Space. Default setting is None.</li> </ul> </li> <li>Stop Bits         <ul> <li>Stop bits indicate the end of a serial data packet. (A start bit indicates the beginning). The standard setting is 1 stop bit. Communication with slow devices may require more than 1 stop bit.</li> <li>Options available: 1, 2. Default setting is 1.</li> </ul> </li> <li>Flow Control         <ul> <li>Flow Control can prevent data loss from buffer overflow. When sending data, if the receiving buffers are full, a 'stop' signal can be sent to re-start the flow. Hardware flow control uses two wires to send start/stop signals.</li> <li>Options available: None, Hardware RTS/CTS. Default setting is None.</li> </ul> </li> <li>VT-UTF8 Combo Key Support         <ul> <li>Enable/Disable the VT-UTF8 Combo Key Support.</li> <li>Options available: Enabled, Disabled. Default setting is <b>Disabled</b>.</li> </ul> </li> <li>Recorder Mode         <ul> <li>When this mode enabled, only texts will be send. This is to capture Terminal data.</li> <li>Options available: Enabled, Disabled. Default setting is <b>Disabled</b>.</li> </ul> </li> <li>Resolution 100x31         <ul> <li>Enable/Disable extended terminal resolution.</li> <li>Options available: Enabled, Disabled. Default setting is <b>Enabled</b>.</li> </ul> </li> <li>Putty KeyPad         <ul></ul></li></ul>		

Parameter	Description
Serial Port for Out-of-Band Management / Windows Emergency Management Services (EMS) Console Redirection <sup>(Note)</sup>	EMS console redirection allows the user to configure Console Redirection Settings to support Out-of-Band Serial Port management. Options available: Enabled, Disabled. Default setting is <b>Disabled</b> .
Serial Port for Out-of-Band EMS Console Redirection Settings	<ul> <li>Press [Enter] to configure advanced items.</li> <li>Please note that this item is configurable when Serial Port for Out-of-Band Management EMS Console Redirection is set to Enabled.</li> <li>Out-of-Band Mgmt Port <ul> <li>Microsoft Windows Emergency Management Service (EMS) allows for remote management of a Windows Server OS through a serial port.</li> <li>Default setting is COM1.</li> </ul> </li> <li>Terminal Type EMS <ul> <li>Selects a terminal type to be used for console redirection.</li> <li>Options available: VT100, VT100PLUS, VT-UTF8, ANSI. Default setting is VT10PLUS.</li> </ul> </li> <li>Bits per second EMS <ul> <li>Selects the transfer rate for console redirection.</li> <li>Options available: 9600, 19200, 57600, 115200. Default setting is 115200.</li> </ul> </li> <li>Flow Control EMS <ul> <li>Flow control EMS</li> <li>Flow control can prevent data loss from buffer overflow. When sending data, if the receiving buffers are full, a 'stop' signal can be sent to stop the data flow. Once the buffers are empty, a 'start' signal can be sent to re-start the flow. Hardware flow control uses two wires to send start/stop signals.</li> <li>Options available: None, Hardware RTS/CTS, Software Xon/Xoff. Default setting is None.</li> </ul> </li> </ul>

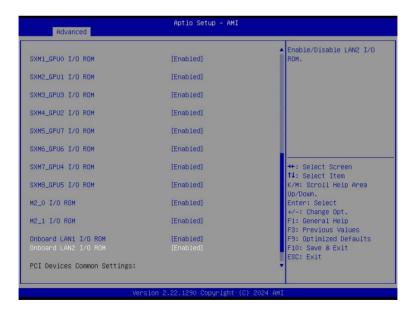
### 5-2-3 SIO Configuration

Aptio Setup – AMI Advanced	
AMI SID Driver Version : A5.19.00 Super IO Chip Logical Device(s) Configuration [*Active*] Serial Port WARNING: Logical Devices state on the left side of the control, reflects the current Logical Device state. Changes made during Setup Session will be shown after you restart the system.	View and Set Basic properties of the SIO Logical device. Like IO Base, IRQ Range, DMA Channel and Device Mode.
	++: Select Screen 11: Select Item K/M: Scroll Help Area Uµ/Down. Enter: Select +/-: Change Opt. F1: General Help F3: Previous Values F9: Optimized Defaults F10: Save & Exit ESC: Exit
Version 2.22.1290 Copyright (C) 2024 AM	II

Parameter	Description	
AMI SIO Driver Version	Displays the AMI SIO driver version information.	
Super IO Chip Logical Device(s) Configuration	Press [Enter] to configure advanced items.	
[*Active*] Serial Port	<ul> <li>When set to Enabled allows you to configure the serial port settings. When set to Disabled, displays no configuration for the serial port.</li> <li>Options available: Enabled, Disabled. Default setting is Enabled.</li> <li>Logical Device Settings/Current: <ul> <li>Displays the serial port base I/O address and IRQ.</li> </ul> </li> <li>Possible: <ul> <li>Configures the serial port base I/O address and IRQ.</li> <li>Use Automatic Settings</li> <li>IO=3F8h; IRQ=4; DMA;</li> <li>IO=3F8h; IRQ=4; DMA;</li> <li>IO=3F8h; IRQ=4; DMA;</li> <li>IO=2E8h; IRQ=4; DMA;</li> <li>IO=2E8h; IRQ=4; DMA;</li> <li>IO=2E8h; IRQ=4; DMA;</li> <li>Default setting is Use Automatic Settings.</li> </ul> </li> </ul>	

### 5-2-4 PCI Subsystem Settings

Advanced	Aptio Setup – AMI	
PCI Bus Driver Version SLOT1 I/O ROM	A5.01.30 [Enabled]	▲ Enable/Disable SLOT1 I/O ROM
SLOT2 I/O ROM	[Enabled]	
SLOT3 I/O ROM	[Enabled]	
SLOT4 I/O ROM	[Enabled]	
SLOT5 I/O ROM	[Enabled]	
SLOT6 I/O ROM	[Enabled]	100
SLOT7 I/O ROM	[Enabled]	++: Select Screen
SLOT8 I/O ROM	[Enabled]	t↓: Select Item K/M: Scroll Help Area
SLOT9 I/O ROM	[Enabled]	Up/Down. Enter: Select
SLOT10 I/O ROM	[Enabled]	+/-: Change Opt. F1: General Help
SLOT11 I/O ROM	[Enabled]	F3: Previous Values F9: Optimized Defaults
SOLT12 I/O ROM	[Enabled]	F10: Save & Exit ESC: Exit



Parameter	Description	
PCI Bus Driver Version	Displays the PCI Bus Driver version information.	
SLOT_# I/O ROM <sup>(Note1)</sup>	When enabled, this setting will initialize the device expansion ROM for the related PCIe slot. Options available: Enabled, Disabled. Default setting is <b>Enabled</b> .	
SMX_# I/O ROM <sup>(Note2)</sup>	When enabled, this setting will initialize the device expansion ROM for the related GPU slot. Options available: Enabled, Disabled. Default setting is <b>Enabled</b> .	
M2_# I/O ROM <sup>(Note3)</sup>		
Onboard LAN1/ LAN2 Controller <sup>(Note4)</sup>	Enable/Disable the onboard LAN controller. Options available: Enabled, Disabled. Default setting is <b>Enabled</b> .	
Onboard LAN1/ LAN2 I/O ROM(Note4)	Enable/Disable the onboard LAN devices, and initializes device expansion ROM. Options available: Enabled, Disabled. Default setting is <b>Enabled</b> .	
PCI Devices Common Settings		
Above 4G Decoding	Enable/Disable memory mapped I/O to 4GB or greater address space (Above 4G Decoding). Options available: Enabled, Disabled. Default setting is <b>Enabled</b> .	
Re-Size BAR Support	If system has Resizable BAR capable PCIe Devices, this option Enables or Disables Resizable BAR Support. Options available: Enabled, Disabled. Default setting is <b>Disabled</b> .	
SR-IOV Support	If the system has SR-IOV capable PCIe devices, this item Enable/Disable Single Root IO Virtualization Support. Options available: Enabled, Disabled. Default setting is <b>Enabled</b> .	

(Note1) This section is dependent on the available PCIe Slot.

(Note2) This section is dependent on the available GPU Slot.

(Note3) This section is dependent on the available M2 Slot.

(Note4) This section is dependent on the available LAN controller.

### 5-2-5 USB Configuration

Advanced	Aptio Setup — AMI	
USB Configuration		This is a workaround for OSes without XHCI hand-off
USB Devices:		support. The XHCI
9 Drives, 1 Keyboard, 1 Mouse,	1 Hub	ownership change should be claimed by XHCI driver.
XHCI Hand-off		
USB Mass Storage Driver Support	[Enabled]	
Port 60/64 Emulation	[Enabled]	
		++: Select Screen 1: Select Item K/M: Scroll Help Area Up/Down. Enter: Select +/-: Change Opt. F1: General Help F3: Previous Values F3: Optimized Defaults F10: Save & Exit ESC: Exit
Version 2	.22.1290 Copyright (C) 2024 AMI	

Parameter	Description
USB Configuration	
USB Devices:	Displays the USB devices connected to the system.
XHCI Hand-off	Enable/Disable the XHCI (USB 3.0) Hand-off support. Options available: Enabled, Disabled. Default setting is <b>Enabled</b> .
USB Mass Storage Driver Support <sup>(Note)</sup>	Enable/Disable the USB Mass Storage Driver Support. Options available: Enabled, Disabled. Default setting is <b>Enabled</b> .
Port 60/64 Emulation	Enables the I/O port 60h/64h emulation support. This should be enabled for the complete USB Keyboard Legacy support for non- USB aware OSes. Options available: Enabled, Disabled. Default setting is <b>Enabled</b> .

### 5-2-6 Network Stack Configuration

Advanced	Aptio Setup – AMI	
Network Stack IPv4 PXE Support IPv4 HTTP Support IPv6 PXE Support IPv6 FTTP Support PXE boot wait time Media detect count	[Enabled] [Enabled] [Disabled] [Disabled] [Disabled] 0 1	Enable/Disable UEFI Network Stack
		★: Select Screen 11: Select Item K/M: Scroll Help Area Up/Down. Enter: Select +/-: Change Opt. F1: General Help F3: Previous Values F9: Optimized Defaults F10: Save & Exit ESC: Exit

Version 2.22.1290 Copyright (C) 2024 AMI

Parameter	Description
Network Stack	Enable/Disable the UEFI network stack. Options available: Enabled, Disabled. Default setting is <b>Enabled</b> .
Ipv4 PXE Support	Enable/Disable the Ipv4 PXE feature. Options available: Enabled, Disabled. Default setting is <b>Enabled</b> .
Ipv4 HTTP Support	Enable/Disable the Ipv4 HTTP feature. Options available: Enabled, Disabled. Default setting is <b>Disabled</b> .
Ipv6 PXE Support	Enable/Disable the Ipv6 PXE feature. Options available: Enabled, Disabled. Default setting is <b>Disabled</b> .
Ipv6 HTTP Support	Enable/Disable the Ipv6 HTTP feature. Options available: Enabled, Disabled. Default setting is <b>Disabled</b> .
PXE boot wait time	Wait time in seconds to press ESC key to abort the PXE boot. Press the <+> / <-> keys to increase or decrease the desired values.
Media detect count	Number of times the presence of media will be checked. Press the <+> / <-> keys to increase or decrease the desired values.

### 5-2-7 Post Report Configuration

Post Report Configuration		Post Error Message Suppor
Error Message Report		Enabled/Disabled
Post Error Message		
Halt On	[No Error]	
		**: Select Screen
		14: Select Item
		K/M: Scroll Help Area
		Up/Down.
		Enter: Select
		+/−: Change Opt.
		F1: General Help
		F3: Previous Values F9: Optimized Defaults
		F10: Save & Exit
		ESC: Exit

Parameter	Description
Post Report Configuration	
Error Message Report	
Post Error Message	Enable/Disable the POST Error Message support. Options available: Enabled, Disabled. Default setting is <b>Enabled</b> .
Halt On	Options available: No Error, All Error. Default setting is No Error.

### 5-2-8 KMIP Server Configuration

Enter IP4 address in dotted-decimal notation Example: 192.168.10.12
+: Select Screen 14: Select Item K/M: Scroll Help Area Up/Down. Enter: Select +/-: Change Opt.
F1: General Help F3: Previous Values F9: Optimized Defaults F10: Save & Exit ESC: Exit

Parameter	Description
KMIP Server Configuration	
KMIP Server IP address	
KMIP TCP Port Number	
Time Zone	Enter the correct time zone for this server. Default setting is <b>GMT+8</b> .
Client Credentials	Use User and password credentials to authenticate the Client. Options available: Enabled, Disabled, Clear. Default setting is <b>Enabled</b> .
Client UserName	Enter Client identity: UserName. Name Length: 0-63 characters.
Client Password	Enter Client identity: Password. Password Length: 0-31 characters.
KMS TLS Certificate / Size	
CA Certificate	Enroll factory defaults or load the KMS TLS certificates from the file.
Client Certificate	Enroll factory defaults or load the KMS TLS certificates from the file.
Client Private Key	Enroll factory defaults or load the KMS TLS certificates from the file.
	BIOS Setup

### 5-2-9 KMS Policy Configuration

Advanced	Aptio Setup – AMI	
KMS Option KMS KMIP Server Retry Count	[KMS with KMIP] 5	KMS Option +: Select Screen 14: Select Item K/M: Scroll Help Area Up/Doun. Enter: Select +/-: Change Opt. Fi General Help F3: Previous Values F9: Optimized Defaults F10: Save & Exit ESC: Exit
	on 2.22.1290 Copyright (C) 2	2004 .007

Parameter	Description
KMS Option	Options available: KMS with KMIP, Disabled. Default setting is <b>KMS with KMIP</b> .
KMS KMIP Server Retry Count	Define KMS KMIP Server Retry Count.

### 5-2-10 NVMe Configuration

Advanced	Aptio Setup	- AMI
NVMe Configuratio ▶ NVME1: INTEL SSDF		
NVME0: INTEL SSDF		
NVME2: INTEL SSDF		
NVME3: INTEL SSDF		
► NVME7: INTEL SSDF		
▶ NVME6: INTEL SSDF	E2KX010T8	
▶ NVME4: INTEL SSDF	E2KX010T8	
▶ NVME5: INTEL SSDF	E2KX010TB	
		<pre>++: Select Screen 11: Select Item K/M: Scroll Help Area Up/Down. Enter: Select +/-: Change Opt. F1: General Help F5: Previous Values F9: Optimized Defaults F10: Save &amp; Exit ESC: Exit</pre>
	Version 2.22.1290 Copy	night (U) 2024 AMI
ameter	Description	

NVMe Configuration

Displays the NVMe devices connected to the system.

### 5-2-11 Chipset Configuration

Aptio Setup – AMI	
[Power Off] [0x1000]	Specify what state when power is re-applied after a power failure (G3 state)
[Enabled] [BIOS Build-In] [Disable]	
(Disabled) [Enabled]	
	→+: Select Screen 14: Select Item
	K∕H: Scroll Help Area Up/Down. Enter: Select
	+/-: Change Opt. F1: General Help F3: Previous Values F9: Optimized Defaults F10: Save & Exit
	ESC: Exit
	[Power Off] [Ox1000] [Enabled] [Enabled] [B105 Build-In] [Disable] [Disabled]

Parameter	Description
Restore on AC Power Loss <sup>(Note1)</sup>	Defines the power state to resume to after a system shutdown that is due to an interruption in AC power. When set to Last State, the system will return to the active power state prior to shutdown. When set to Power Off, the system remains off after power shutdown. Options available: Last State, Power Off, Power On, Unspecified. The default setting depends on the BMC setting.
P2P Bridge IO Size	Specifies P2P Bridge IO aligned to the size. Options available: 0x100, 0x150, 0x1000. Default setting is <b>0x1000</b> .
SATA HDD Security Frozen	Enable/Disable this item to send freeze lock command to SATA HDD. Options available: Enabled, Disabled. Default setting is <b>Enabled</b> .
NVMe SSD Security Frozen	Attempt to send freeze lock command to NVMe SSDs during boot. Options available: Enabled, Disabled. Default setting is <b>Enabled</b> .
NVMe OPROM Select	Options available: BIOS Build-In, NVMe Device, Disabled. Default setting is <b>BIOS Build-In.</b>
NVMe LED Control	Enable/Disable allow user control NVMe LED. It only available the NVMe device direct connect to CPU. Options available: Disable, Enable. Default setting is <b>Disable</b> .

(Note1) When the power policy is controlled by BMC, please wait for 15-20 seconds for BMC to save the last power state.

Parameter	Description
Chassis Opened Warning <sup>(Note2)</sup>	Enable/Disable the chassis intrusion alert function. Options available: Enabled, Disabled, Clear. Default setting is <b>Disabled</b> .
Power Button 1s Shutdown	Enable/Disable the chassis intrusion alert function. Options available: Enabled, Disabled, Clear. Default setting is <b>Disabled</b> .

## 5-2-12 TIs Auth Configuration

Advanced	Aptio Setup – AMI	
<ul> <li>Server CA Configuration</li> <li>Client Cert Configuratic</li> </ul>	n	Press <enter≻ configure<br="" to="">Server CA.</enter≻>
	Version 2.22.1290 Copyright (C) 2024 (	++: Select Screen 11: Select Item K/M: Scroll Help Area Up/Doun. Enter: Select +/-: Change Opt. F1: General Help F3: Previous Values F3: Optimized Defaults F10: Save & Exit ESC: Exit
arameter	Description	
erver CA Configuration	Press [Enter] for configuration of adva • Enroll Cert - Press [Enter] to enroll a certifica • Enroll Cert Using File • Cert GUID	
lient Cert Configuration	Press [Enter] for configuration of adva	nced items.

### 5-2-13 iSCSI Configuration

	The worldwide unique name
Add an Attempt	of iSCSI Initiator. Only IQN format is accepted.Range is from 4
Delete Attempts	to 223
Change Attempt Order	
	++: Select Screen
	↑↓: Select Item
	K/M: Scroll Help Area
	Up/Down. Enter: Select
	+/-: Change Opt.
	F1: General Help
	F3: Previous Values
	F9: Optimized Defaults F10: Save & Exit
	ESC: Exit

Parameter	Description	
Attempt Priority	<ul> <li>Press [Enter] configure advanced items.</li> <li>Attempt Priority <ul> <li>Use arrow keys to select the attempt, then press +/- keys to move the attempt up/down in the attempt order list.</li> </ul> </li> <li>Commit Changes and Exit</li> </ul>	
Host iSCSI Configuration	<ul> <li>Press [Enter] to configure advanced items.</li> <li>iSCSI Initiator Name <ul> <li>Only IQN format is accepted. Range: from 4 to 223</li> </ul> </li> <li>Add an Attempt</li> <li>Delete Attempts</li> <li>Change Attempt Order</li> </ul>	

### 5-2-14 Intel(R) Ethernet Controller X710 for 10GBASE-T

	Aptio Setup – AMI	
Advanced Firmware Image Properties NIC Configuration		View device firmware version information.
Blink LEDS UEFI Driver Adapter PBA Device Name Chip Type PCI Device ID PCI Address Link Status MAC Address Virtual MAC Address	0 Intel(R) 40GbE 4.9.49 H64862-000 Intel(R) Ethernet Controller X710 for 10EBASE-T Intel X710 15FF 04:00:00 [Disconnected] 10:FF:E0:30:99:A6 00:00:00:00:00:00	<pre>++: Select Screen 14: Select Item K/M: Scroll Help Area Up/Doun. Enter: Select +/-: Change Dpt. F1: General Help F3: Previous Values F9: Optimized Defaults F10: Save &amp; Exit ESC: Exit</pre>
Version 2	.22.1290 Copyright (C) 2024 AMI Aptio Setup – AMI	
Option ROM version Unique NVM/EEPROM ID NVM Version Version 2	1.3353.0 0x8000DB77 9.20 .22,1290 Copyright (C) 2024 AM	+: Select Screen 14: Select Item K/M: Scroll Help Area Up/Down. Enter: Select +/-: Change Opt. F1: General Help F3: Previous Values F9: Optimized Defaults F10: Save & Exit ESC: Exit

Advanced	Aptio Setup - AMI	
Link Speed Hake On LAN LLDP Agent	[Auto Negotiated] [Enabled] [Enabled]	Enables power on of the system via LAN. Note that configuring Wake on LAN in the operating system does not change the value of this setting, but does override the behavior of Wake on LAN in OS controlled power states.
		<ul> <li>Select Screen</li> <li>Select Item</li> <li>K/M: Scroll Help Area</li> <li>Up/Doun.</li> <li>Enter: Select</li> <li>+/-1 Change Opt.</li> <li>F1: General Help</li> <li>F3: Previous Values</li> <li>F9: Optimized Defaults</li> <li>F10: Save &amp; Exit</li> <li>ESC: Exit</li> </ul>
	Version 2.22.1290 Copyright (C) 20	24 AMI

Parameter	Description	
Firmware Image Properties	Press [Enter] to view device firmware version information	
NIC Configuration	<ul> <li>Press [Enter] to configure advanced items.</li> <li>Link Speed <ul> <li>Allows for automatic link speed adjustment.</li> <li>Options available: Auto Negotiated, 100 Mbps Half, 100 Mbps Full, 1000 Mbps Half, 1000 Mbps Full. Default setting is Auto Negotiated.</li> </ul> </li> <li>Wake On LAN <ul> <li>Enables power on of the system via LAN. Note that configuring Wake on LAN in the operating system does not change the value of this setting, but does override the behavior of Wake on LAN in OS controlled power states.</li> <li>Options available: Enabled, Disabled. Default setting is Enabled.</li> </ul> </li> <li>LLDP Agent <ul> <li>Options available: Enabled, Disabled. Default setting is Enabled.</li> </ul> </li> </ul>	
Blink LEDs	Identifies the physical network port by blinking the associated LED. Press the numeric keys to adjust desired values (up to 15 seconds).	
UEFI Driver	Displays the technical specifications for the Network Interface Controller.	
Adapter PBA	Displays the technical specifications for the Network Interface Controller.	

Parameter	Description
Device Name	Displays the technical specifications for the Network Interface Controller.
Chip Type	Displays the technical specifications for the Network Interface Controller.
PCI Device ID	Displays the technical specifications for the Network Interface Controller.
PCI Address	Displays the technical specifications for the Network Interface Controller.
Link Status	Displays the technical specifications for the Network Interface Controller.
MAC Address	Displays the technical specifications for the Network Interface Controller.
Virtual MAC Address	Displays the technical specifications for the Network Interface Controller.

## 5-2-15 VLAN Configuration

Advanced	Aptio Setup – AMI	
Create new VLAN VLAN ID Priority Add VLAN Configured VLAN List Remove VLAN	0	VLAN ID of new VLAN or existing VLAN, valid value is 0~4094
		++: Select Screen 14: Select Item K/M: Scroll Help Area Up/Down. Enter: Select +/-: Change Opt. F1: General Help F3: Previous Values F9: Optimized Defaults F10: Save & Exit ESC: Exit
v	ersion 2.22.1290 Copyright ((	C) 2024 AMI

Parameter	Description	
Enter Configuration Menu	<ul> <li>Press [Enter] to configure advanced items.</li> <li>Create new VLAN</li> <li>VLAN ID <ul> <li>Sets VLAN ID for a new VLAN or an existing VLAN.</li> <li>Press the &lt;+&gt; / &lt;-&gt; keys to increase or decrease the desired values.</li> <li>The valid range is from 0 to 4094.</li> </ul> </li> <li>Priority <ul> <li>Sets 802.1Q Priority for a new VLAN or an existing VLAN.</li> <li>Press the &lt;+&gt; / &lt;-&gt; keys to increase or decrease the desired values.</li> <li>The valid range is from 0 to 7.</li> </ul> </li> <li>Add VLAN <ul> <li>Press [Enter] to create a new VLAN or update an existing VLAN.</li> </ul> </li> <li>Configured VLAN List</li> <li>Remove VLAN <ul> <li>Press [Enter] to remove an existing VLAN.</li> </ul> </li> </ul>	

### 5-2-16 MAC IPv6 Network Configuration

Advanced	Aptio Setup - AMI	
Interface Name :	eth0	The 64 bit alternative
Interface Type :	Ethernet	interface ID for the
MAC address :	10-FF-E0-30-99-A6	device. The string is
Host addresses :		colon separated. e.g.
	FE80::12FF:E0FF:FE30:99A6/64	ff:dd:88:66:cc:1:2:3
Route Table :		1997 - Maria Canada
	FE80::/64 >>::	
Gateway addresses :		
DNS addresses :		
DAD Transmit Count	1	
Policy	[automatic]	
Save Changes and Exit		++: Select Screen
Salo shangoo ana Enze		14: Select Item
		K/M: Scroll Help Area
		Up/Down.
		Enter: Select
		+/-: Change Opt.
		F1: General Help
		F3: Previous Values
		F9: Optimized Defaults
		F10: Save & Exit
		ESC: Exit
	Version 2.22.1290 Copyright (C) 2024 AM	T

Parameter	Description	
Enter Configuration Menu	<ul> <li>Press [Enter] to configure advanced items.</li> <li>Displays the MAC Address information.</li> <li>Interface ID <ul> <li>The 64 bit alternative interface ID for the device. The string is colon separated. e.g. ff:dd:88:66:cc:1:2:3.</li> </ul> </li> <li>DAD Transmit Count <ul> <li>The number of consecutive Neighbor solicitation messages sent while performing Duplicate Address Detection on a tentative address. A value of zero indicates that Duplicate Address Detection is not performed.</li> </ul> </li> <li>Policy <ul> <li>Options available: automatic, manual. Default setting is <b>automatic</b>.</li> </ul> </li> <li>Save Changes and Exit <ul> <li>Press [Enter] to save all configurations.</li> </ul> </li> </ul>	

### 5-2-17 MAC IPv4 Network Configuration

Advanced	Aptio Setup – AMI	
Configured Enable DHCP Local IP Address Local Acteway Local Gateway Local DNS Servers Save Changes and Exit	(Enabled) (Disabled)	Indicate whether network address configured successfully or not.
		<pre>++: Select Screen 11: Select Item K/M: Scroll Help Area Uu/Down. Enter: Select +/-: Change Opt. F1: General Help F3: Previous Values F9: Optimized Defaults F10: Save &amp; Exit ESC: Exit</pre>
Ve	rsion 2.22.1294 Copyright (C)	) 2024 AMI

Parameter	Description
Configured	Indicates whether network address is configured successfully or not. Options available: Enabled, Disabled. Default setting is <b>Disabled.</b>
Enable DHCP	Options available: Enabled, Disabled. Default setting is Disabled.
Local IP Address	Press [Enter] to configure local IP address.
Local NetMask	Press [Enter] to configure local NetMask.
Local Gateway	Press [Enter] to configure local Gateway
Local DNS Servers	Press [Enter] to configure local DNS servers
Save Changes and Exit	Press [Enter] to save all configurations.

### 5-2-18 Driver Health

Provides Health Status fo the Drivers/Controllers
++: Select Screen 11: Select Item K/H: Scroll Help Area Up/Down. Enter: Select +/-: Change Opt. F1: General Help F3: Previous Values F9: Optimized Defaults F10: Save & Exit ESC: Exit

Parameter	Description
Driver Health	Displays driver health status of the devices/controllers if installed

# 5-3 Chipset Menu

Chipset Setup menu displays submenu options for configuring the function of Platform Controller Hub(PCH). Select a submenu item, then press <Enter> to access the related submenu screen.

Aptio Setup - AMI		
Main Advanced Chipset Server Mgmt Security Boot Save & Exit		
<ul> <li>Processor Configuration</li> <li>Common RefCode Configuration</li> <li>UPI Configuration</li> <li>Memory Configuration</li> <li>Advanced Power Management Configuration</li> <li>PCH-IO Configuration</li> <li>Miscellaneous Configuration</li> <li>Server ME Configuration</li> <li>Runtime Error Logging</li> <li>Power Policy</li> </ul>	Displays and provides options to change the Processor Settings ++: Select Screen fl: Select Item K/M: Scroll Help Area Up/Down. Enter: Select +/-: Change Opt.	
	F1: General Help F3: Previous Values F9: Optimized Defaults F10: Save & Exit ESC: Exit	
Version 2.22.1290 Copyright (C) 2024 AMI		

## 5-3-1 Processor Configuration

Chipset	Aptio Setup – AMI	
Processor Configuration		▲ Change Per-Socket Settings
<ul> <li>Per-Socket Configuration</li> <li>Processor Socket</li> <li>Processor ID</li> <li>Processor Die Type</li> <li>Processor Frequency</li> <li>Processor Max Ratio</li> <li>Processor Max Ratio</li> <li>Processor Min Ratio</li> <li>Microcode Revision</li> <li>L1 Cache RAM(Per Core)</li> <li>L2 Cache RAM(Per Core)</li> <li>L3 Cache RAM(Per Package)</li> <li>Processor 0 Version</li> <li>Processor 1 Version</li> <li>Enable LP (Global)</li> <li>Hardware Prefetcher</li> <li>L2 RFO Prefetch Disable</li> <li>Adjacent Cache Prefetcher</li> <li>DGU IP Prefetcher</li> <li>Extended APIC</li> </ul>	Socket 0 Socket 1 000C06F2× 000C06F2 XCC XCC 1.9006Hz 1.9006Hz 13H 06H 00H 06H 21000283 21000283 80KB 80KB 80KB 2048KB 2048KB 327680KB 327680KB INTEL(R) XEON(R) PLATIN UM 8592+ INTEL(R) XEON(R) PLATIN UM 8592+ [Chable] [Chable] [Chable] [Chable] [Chable] [Chable] [Chable] [Chable] [Chable] [Chable] [Chable] [Chable]	<pre>++: Select Screen 11: Select Item K/M: Scroll Help Area Up/Doum. Enter: Select +/-: Change Opt. F1: General Help F3: Previous Values F9: Optimized Defaults F10: Save &amp; Exit ESC: Exit </pre>
Vasa	ion 2 22 1290 Conuright (C) 2024	L ANT

Aptio Setup - AMI

Chipset		
Processor O Version Processor 1 Version	INTEL(R) XEON(R) PLATIN UM 8592+ INTEL(R) XEON(R) PLATIN UM 8592+	Displays and provides option to change the Processor CFR Settings
Enable LP [Global] Hardware Prefetcher L2 RFD Prefetch Disable Adjacent Cache Prefetch DCU Streamer Prefetcher DCU IP Prefetcher Extended APIC Enable Intel(R) TXT VMX Enable INTEL(R) TXT VMX Enable SMX AES-NI Debug Consent	(ALL LPS) [Enable] [Disable] [Enable] [Enable] [Enable] [Disable] [Enable] [Disable] [Enable] [Disable]	<pre>→+: Select Screen T1: Select Item K/M: Scroll Help Area Up/Down.</pre>
TME, TME-MT, TDX		Enter: Select +/−: Change Opt. F1: General Help
Memory Encryption (TME) SGX setup configuration precondition: met. Please check TME, MirrorMode or	s for enabling were NOT	F3: Previous Values F9: Optimized Defaults F10: Save & Exit ESC: Exit
Processor CFR Configuration		
Version 2	.22.1290 Copyright (C) 2024 AMI	

Parameter	Description
Processor Configuration	
Pre-Socket Configuration	<ul> <li>Press [Enter] to configure advanced items.</li> <li>CPU Socket 0/1 Configuration <ul> <li>Core Disable Bitmap(Hex)</li> <li>Number of Cores to enable. 0 means all cores. FFFFFFF means to disable all cores. The maximum value depends on the number of CPUs available. Press the numeric keys to adjust desired values.</li> </ul> </li> </ul>
Processor Socket / Processor ID / Processor Die Type / Processor Frequency / Processor Max Ratio / Processor Min Ratio / Microcode Revision / L1 Cache RAM(Per Core) / L2 Cache RAM(Per Core) / L3 Cache RAM(Per Package) / Processor # Version	Displays the technical specifications for the installed processor(s).
Enable LP [Global]	Enables Logical processor (Software Method to Enable/Disable Logical Processor threads). Options available: ALL LPs, Single LP. Default setting is <b>ALL LPs</b> .
Hardware Prefetcher	Select whether to enable the speculative prefetch unit of the processor. Options available: Enable, Disable. Default setting is <b>Enable</b> .
L2 RF0 Prefetch Disable	Options available: Enable, Disable. Default setting is <b>Disable</b> .
Adjacent Cache Prefetch	When enabled, cache lines are fetched in pairs. When disabled, only the required cache line is fetched. Options available: Enable, Disable. Default setting is <b>Enable</b> .
DCU Streamer Prefetcher	Enable/Disable DCU streamer prefetcher. Options available: Enable, Disable. Default setting is <b>Enable</b> .
DCU IP Prefetcher	Enable/Disable DCU IP Prefetcher. Options available: Enable, Disable. Default setting is <b>Enable</b> .
Extended APIC	Enable/Disable extended APIC support. Note: The VT-d will be enabled automatically when x2APIC is enabled. Options available: Enable, Disable. Default setting is <b>Enable</b> .
Enable Intel(R) TXT	Enable/Disable the Intel Trusted Execution Technology support function. Options available: Enable, Disable. Default setting is <b>Disable</b> .
VMX	Enable/Disable the Vanderpool Technology. This will take effect after rebooting the system. Options available: Enable, Disable. Default setting is <b>Enable</b> .
AES-NI	Enable/Disable the AES-NI support. Options available: Enable, Disable. Default setting is <b>Enable</b> .
Debug Consent	Options available: Enable, Disable. Default setting is <b>Disable</b> .

Parameter	Description	
Memory Encryption (TME) <sup>(Note)</sup>	Enable/Disable memory encryption (TME). Options available: Enabled, Disabled. Default setting is <b>Disabled</b> .	
Total Memory Encryption Multi-Tenant (TME-MT)	Options available: Enabled, Disabled. Default setting is <b>Disabled</b> .	
Processor CFR Configuration	<ul> <li>Press [Enter] to configure advanced items.</li> <li>Provision S3M CFR <ul> <li>Options available: Disable, Enable. Default setting is Enable.</li> </ul> </li> <li>Manual Commit S3M FW CFR <ul> <li>Options available: Disable, Enable. Default setting is Enable.</li> </ul> </li> <li>Provision PUcode CFR <ul> <li>Options available: Disable, Enable. Default setting is Enable.</li> </ul> </li> <li>Manual Commit PUcode CFR <ul> <li>Options available: Enable, Disable. Default setting is Enable.</li> </ul> </li> <li>Socket0 CFR Revision Info <ul> <li>Displays CFR Revision Info</li> </ul> </li> </ul>	

## 5-3-2 Common RefCode Configuration

Common RefCode Configuration		Enable or Disable Non
Numa Virtual Numa	(Enable) [Disable]	uniform Memory Access (NUMA).
		++: Select Screen t1: Select Item K/M: Scroll Help Area Up/Down. Enter: Select +/-: Change Opt. F1: General Help F3: Previous Values F9: Optimized Defaults F10: Save & Exit ESC: Exit

Parameter	Description
Common RefCode Configuration	
Numa	Enable or disable Non uniform Memory Address (NUMA). Options available: Enable, Disable. Default setting is <b>Enable</b> .
Virtual Numa	Divide physical NUMA nodes into evenly sized virtual NUMA nodes in ACPI table. This may improve Windows performance on CPUs with more than 64 logical processors. Options available: Enable, Disable. Default setting is <b>Disable</b> .

### 5-3-3 UPI Configuration

Chipset	Aptio Setup — AMI	
UPI General Configuration		UPI Status Help
UPI Status Link Frequency Select SNC Stale AtoS LLC dead line alloc MMCFG Size MMID High Base MMID High Granularity Size Limit CPU PA to 46 bits	(Auto) (Auto) (Enable) (Auto) (Auto) (417) (40966) (Disable)	++: Select Screen 1↓: Select Item K/M: Scroll Help Area Up/Down.
		Enter: Select +/-: Change Opt. F1: General Help F3: Previous Values F9: Optimized Defaults F10: Save & Exit ESC: Exit

Parameter	Description	
UPI General Configuration	<ul> <li>Press [Enter] to configure advanced items.</li> <li>UPI Status <ul> <li>Press [Enter] to view the Uncore status.</li> </ul> </li> <li>Link Frequency Select <ul> <li>Selects the UPI link frequency.</li> <li>Options available: 12.8GT/s, 14.4GT/s, 16.0GT/s, Auto, Use Per Link Setting. Default setting is Auto.</li> </ul> </li> <li>SNC <ul> <li>Enable/Disable Sub NUMA Cluster function.</li> <li>Options available: Auto, Disable, Enable SNC2 (2-clusters), Enable SNC4 (4-clusters). Default setting is Auto.</li> </ul> </li> <li>Stale AtoS <ul> <li>Enable/Disable Stale A to S directory optimization.</li> <li>Options available: Disable, Enable, Auto. Default setting is Auto.</li> </ul> </li> <li>LLC dead line alloc <ul> <li>Enable/Disable fill dead lines in LLC.</li> <li>Options available: Disable, Enable, Auto. Default setting is Enable.</li> </ul> </li> <li>MMCFGG Size <ul> <li>Options available: 128M, 256M, 512M, 1G, 2G, Auto. Default setting is Auto.</li> </ul> </li> </ul>	

Parameter	Description	
UPI General Configuration (continued)	<ul> <li>MMIO High Base <ul> <li>Options available: 56T, 40T, 32T, 24T, 16T, 4T, 2T, 1T, 512G, 3584T.</li> <li>Default setting is 32T.</li> </ul> </li> <li>MMIO High Granularity Size <ul> <li>Selects the allocation size used to assign mmioh resources.</li> <li>Options available: 1G, 4G, 16G, 64G, 256G, 1024G. Default setting is 64G.</li> </ul> </li> <li>Limit CPU PA to 46 bit <ul> <li>Options available: Disable, Enable. Default setting is Disable.</li> </ul> </li> </ul>	

# 5-3-4 Memory Configuration

Integrated Memory Controller (iMC)		Enforces Plan Of Record restrictions for DDR frequency programming. Because (Disable) option is over spec and depend on
Enforce DDR Memory Frequency POR Memory Frequency Enable ADR Legacy ADR Mode Minimum System Memory Size ADR Data Save Mode Assert ADR on Reset	[POR] [Auto] [Enable] [Auto] [268] [NVDIMMS] [Disabled]	memory quality
Assert ADR on S5 Get Memory Timing CXL Type 3 Legacy Memory Topology Memory Map Memory RAS Configuration	(Disəbled) (BIOS Bulid-in) (Disəble)	++: Select Screen 14: Select Item K/M: Scroll Help Area Up/Down. Enter: Select +/-: Change Opt. F1: General Help F3: Previous Values F9: Optimized Defaults F10: Save & Exit

Parameter	Description
Integrated Memory Controller (iMC)	
Enforce DDR Memory Frequency POR	When set to Enable, the system enforces Plan Of Record restrictions for DDR frequency programming. Options available: POR, Disable. Default setting is <b>POR</b> .
Memory Frequency	Configures the maximum memory frequency. If Enforce POR is disabled, user will be able to run at higher frequencies than the memory support (limited by processor support). Default setting is <b>Auto</b> .
Enable ADR	Enables the detecting and enabling of ADR (Asynchronous DRAM Refresh) function. Options available: Enable, Disable. Default setting is <b>Enable</b> .
Legacy ADR Mode	Enable/Disable the Legacy ADR Mode. Options available: Enable, Disable, Auto. Default setting is <b>Auto</b> .
Minimum System Memory Size	Configures the minimum memory size. Options available: 2GB, 4GB, 6GB, 8GB. Default setting is <b>2GB</b> .
ADR Data Save Mode	Specifies the Data Save Mode for ADR. Batterybacked or Type 01 NVDIMM. Options available: Disable, Batterybacked DIMMs, NVDIMMs, Copy to Flash. Default setting is <b>NVDIMMs</b> .
Assert ADR on Reset	Enable/Disable Assert ADR on Reset. Options available: Enabled, Disabled. Default setting is <b>Disabled</b> .

Parameter	Description	
Assert ADR on S5	Enable/Disable Assert ADR on S5. Options available: Enabled, Disabled. Default setting is <b>Disabled</b> .	
Get Memory Timing	Auto is the detected SPD value and use it, otherwise use BIOS Build-in. Options available: Auto, BIOS Build-in. Default setting is <b>BIOS Build-in</b> .	
CXL Type 3 Legacy	Enable or disable CXL type 3 device using CXL type 2 flow. Options available: Enable, Disable. Default setting is <b>Disable</b> .	
Memory Topology	Press [Enter] to view memory topology with DIMM population information.	
Memory Map <sup>(Note1)</sup>	<ul> <li>Press [Enter] to configure advanced items.</li> <li>Volatile Memory Mode <ul> <li>Selects 1LM or 2LM mode for volatile memory.</li> <li>Options available: 1LM, 2LM. Default setting is 2LM.</li> </ul> </li> </ul>	
Memory RAS Configuration	<ul> <li>Press [Enter] to configure advanced items.</li> <li>Mirror Mode<sup>(Note2)</sup> <ul> <li>Mirror Mode will set entire 1LM memory in system to be mirrored, consequently reducing the memory capacity by half. Enables the Mirror Mode will disable the XPT Prefetch.</li> <li>Options available: Disabled, Full Mirror Mode, Partial Mirror Mode. Default setting is <b>Disabled</b>.</li> </ul> </li> <li>Partial Mirror 1 Size (GB) <ul> <li>Selects multiplier of 1GB for the size of the SAD to be created.</li> </ul> </li> <li>Correctable Error Threshold <ul> <li>Correctable Error Threshold (0x01-0x7fff) used for sparing, and leaky bucket.</li> <li>Press the &lt;+&gt; / &lt;-&gt; keys to increase or decrease the desired values.</li> </ul> </li> <li>Trigger SW Error Threshold<sup>(Note2)</sup> <ul> <li>Enable/Disable Sparing trigger SW Error Match Threshold.</li> <li>Options available: Disabled, Enabled. Default setting is <b>Disabled</b>.</li> </ul> </li> <li>SW Per Bank Threshold (1-0x7FFF) used for DDR bank level error.</li> <li>Press the &lt;+&gt; / &lt;-&gt; keys to increase or decrease the desired values.</li> </ul> <li>SW Correctable Error Time Window <ul> <li>SW Correctable Error Time Window</li> <li>SW Correctable Error Time Window based interface in hour (0-24).</li> </ul> </li>	

(Note1) Advanced items prompt when HBM CPU is installed.

(Note2) Advanced items prompt when this item is defined.

Parameter	Description
	Leaky bucket time window based interface <sup>(Note)</sup>
	<ul> <li>Enable/Disable leaky bucket time window based interface.</li> </ul>
	<ul> <li>Options available: Disabled, Enabled. Default setting is</li> </ul>
	Disabled.
	<ul> <li>Leaky bucket time window based interface Hour</li> </ul>
	<ul> <li>Leaky bucket time window based interface hour used for DDR (0-24).</li> </ul>
	<ul> <li>Press the &lt;+&gt; / &lt;-&gt; keys to increase or decrease the desired</li> </ul>
	values.
	<ul> <li>Leaky bucket time window based interface Minute</li> </ul>
	<ul> <li>Leaky bucket time window based interface minute used for DDR (0-60).</li> </ul>
	<ul> <li>Press the &lt;+&gt; / &lt;-&gt; keys to increase or decrease the desired values.</li> </ul>
	Leaky bucket low bit
	<ul> <li>Configures leaky bucket low bit (0x1 - 0x29).</li> </ul>
	<ul> <li>Press the &lt;+&gt; / &lt;-&gt; keys to increase or decrease the desired</li> </ul>
	values.
Memory RAS Configuration	Leaky bucket high bit
(continued)	<ul> <li>Configures leaky bucket high bit (0x1 - 0x29).</li> </ul>
	<ul> <li>Press the &lt;+&gt; / &lt;-&gt; keys to increase or decrease the desired values.</li> </ul>
	ADDDC Sparing <sup>(Note)</sup>
	<ul> <li>ADDDC Sparing</li> <li>Enable/Disable ADDDC Sparing.</li> </ul>
	<ul> <li>Options available: Disabled, Enabled. Default setting is</li> </ul>
	Disabled.
	Enable ADDDC Error Injection
	<ul> <li>Options available: Disabled, Enabled. Default setting is</li> </ul>
	Enabled.
	Patrol Scrub
	<ul> <li>Options available: Disabled, Enable at End of POST. Default</li> </ul>
	setting is Enable at End of POST.
	Patrol Scrub Interval
	<ul> <li>Selects the number of hours (1-24) required to complete full scrub. A value of zero means auto.</li> </ul>
	DDR5 ECS
	<ul> <li>Options available: Disabled, Enabled, Enable ECS with Result Collection. Default setting is <b>Enabled</b>.</li> </ul>

(Note) Advanced items prompt when this item is defined.

## 5-3-5 IIO Configuration

\_\_\_\_

 Press <enter> to bring up the Intel Virtualization for Directed I/O (VT-d) Configuration menu.</enter>
++: Select Screen 11: Select Item K/M: Scroll Help Area Up/Down. Enter: Select +/-: Change Opt. F1: General Help F3: Previous Values F9: Optimized Defaults F10: Save & Exit ESC: Exit
==== (4096B) [Yes]

Parameter	Description	
IIO Configuration		
Intel® VT for Directed I/O (VT-d)	<ul> <li>Press [Enter] to configure advanced items.</li> <li>Intel® VT for Directed I/O <ul> <li>Enable/Disable the Intel VT for Directed I/O (VT-d) support function by reporting the I/O device assignment to VMM through DMAR ACPI Tables.</li> <li>Options available: Enable, Disable. Default setting is Enable.</li> </ul> </li> <li>ACS Control <ul> <li>Enable: Programs ACS only to Chipset PCIe Root Ports Bridges.</li> <li>Disable: Programs ACS to all PCIe bridges.</li> <li>Default setting is Enable.</li> </ul> </li> <li>Cache Allocation <ul> <li>Options available: Enable, Disable. Default setting is Enable.</li> </ul> </li> <li>Opt-Out Illegal MSI Mitigation <ul> <li>Enable/Disable Opt-Out Illegal 0xFEE Platform Mitigation.</li> <li>Options available: Disable, Enable. Default setting is Disable.</li> </ul> </li> <li>DMA Control Opt-In Flag <ul> <li>Enable/Disable DMA_CTRL_PLATFORM_OPT_IN_FLAG in DMAR table in ACPI. Not compatible with Direct Device Assignment (DDA).</li> <li>Options available: Enable, Disable. Default setting is Disable.</li> </ul> </li> </ul>	

Parameter	Description
Intel® VT for Directed I/O (VT-d) (continued)	<ul> <li>Interrupt Remapping         <ul> <li>Enable/Disable the interrupt remapping support function.</li> <li>Options available: Auto, Enable, Disable. Default setting is Auto</li> </ul> </li> <li>x2APIC Opt Out         <ul> <li>Options available: Enable, Disable. Default setting is Disable.</li> </ul> </li> <li>Pre-boot DMA Protection         <ul> <li>Options available: Enable, Disable. Default setting is Disable.</li> </ul> </li> </ul>
Intel® VMD technology	<ul> <li>Press [Enter] to configure advanced items.</li> <li>Intel® VMD Configuration <ul> <li>Enable/Disable Intel® VMD technology.</li> <li>Options available: Enable, Disable. Default setting is Disable.</li> </ul> </li> <li>Intel® VMD for Non-Hotplug NVMe<sup>(Nole)</sup> <ul> <li>Enable/Disable Intel® VMD for Non-Hotplug NVMe.</li> <li>Options available: Enable, Disable. Default setting is Disable.</li> </ul> </li> </ul>
IIO-PCIE Express Global Options	
PCIe Max Read Request Size	Options available: Auto, 128B, 256B, 512B, 1024B, 2048B, 4096B. Default setting is <b>4096B</b> .
Pcie Relaxed Ordering	Options available: No, Yes. Default setting is Yes.

(Note) This item appears when Intel® VMD Configuration is set to Enable.

### 5-3-6 Advanced Power Management Configuration

Chipset	Aptio Setup - AMI	
Advanced Power Management CPU P State Control Handware PM State Control Frequency Prioritization CPU C State Control Package C State Control CPU - Advanced PM Tuning SOCKET RAPL Config	Configuration	P State Control Configuration Sub Menu, include Turbo, XE and etc.
		++: Select Screen 14: Select Item K/M: Scroll Help Area Up/Doum. Enter: Select +/-: Change Opt. F1: General Help F3: Previous Values F9: Optimized Defaults F10: Save & Exit ESC: Exit
	Version 2.22.1290 Copyright (C) 2024 AM	
meter	Description	
	Press [Enter] to configure advanced ite Activate SST-BF	ems.

- Options available: Enable, Disable. Default setting is **Disable**.
- Configure SST-BF
  - This option allows BIOS to configure SST-BF High Priority Cores so that SW does not have to configure
  - Options available: Enable, Disable. Default setting is Disable.
- SpeedStep (Pstates)

CPU P State Control

Par

- Conventional Intel SpeedStep Technology switches both voltage and frequency in tandem between high and low levels in response to processor load.
- Options available: Enable, Disable. Default setting is Enable.
- Turbo Mode

 When this item is enabled, the processor will automatically ramp up the clock speed of 1-2 of its processing cores to improve its performance. When this item is disabled, the processor will not overclock any of its core.

- Options available: Enable, Disable. Default setting is Enable.

Parameter	Description
Hardware PM State Control	<ul> <li>Press [Enter] to configure advanced items.</li> <li>Hardware P-States <ul> <li>When this item is disabled, the processor hardware chooses a P-state based on OS Request (Legacy P-States).</li> <li>In Native mode, the processor hardware chooses a P-state based on OS guidance.</li> <li>In Out of Band mode, the processor hardware autonomously chooses a P-state (with no OS guidance).</li> <li>Options available: Disable, Native Mode, Out of Band Mode, Native Mode with No Legacy Support. Default setting is Native Mode.</li> </ul> </li> </ul>
Frequency Prioritization	<ul> <li>Press [Enter] to configure advanced items.</li> <li>SST-CP <ul> <li>This knob controls whether SST-CP is enabled. When enabled it activates per core power budgeting. NOTE: HWP Native Mode is a pre-requisite for enabling SST-CP.</li> <li>Options available: Disable, Enable. Default setting is <b>Disable</b>.</li> </ul> </li> </ul>
CPU C State Control	<ul> <li>Press [Enter] to configure advanced items.</li> <li>Enable Monitor MWAIT <ul> <li>Allows Monitor and MWAIT instructions.</li> <li>Options available: Disable, Enable, Auto. Default setting is Auto.</li> </ul> </li> <li>CPU C6 Report <ul> <li>Enable/Disable CPU C6(ACPI C3) report to OS.</li> <li>Options available: Disable, Enable, Auto. Default setting is Auto.</li> </ul> </li> <li>Enhanced Halt State (C1E) <ul> <li>Core C1E auto promotion control. Takes effect after reboot.</li> <li>Options available: Enable, Disable. Default setting is Enable.</li> </ul> </li> </ul>
Package C State Control	<ul> <li>Press [Enter] to configure advanced items.</li> <li>Package C State <ul> <li>Configures the state for the C-State package limit.</li> <li>Options available: C0/C1 state, C2 state, C6(non Retention) state, C6(Retention) state, No Limit, Auto. Default setting is Auto.</li> </ul> </li> </ul>
CPU - Advanced PM Tuning	Press [Enter] to configure advanced items.   Energy Perf BIAS  Press [Enter] to configure advanced items.  Power Performance Tuning  Options available: OS Controls EPB, BIOS Controls EPB, PECI Controls EPB. Default setting is <b>OS Controls EPB</b> .  Energy_PERF_BIAS_CFG mode <sup>[Note]</sup> Options available: Performance, Balanced Performance, Balanced Power, Power. Default setting is <b>Balanced Performance</b> .

(Note) This item is configurable when Power Performance Tuning is set to BIOS Controls EPB.

Parameter	Description
SOCKET RAPL Config	<ul> <li>Press [Enter] to configure advanced items.</li> <li>PL1 Power Limit <ul> <li>Press the &lt;+&gt; / &lt;-&gt; keys to increase or decrease the desired values.</li> </ul> </li> <li>PL1 Timer Window <ul> <li>Configure PL1 Timer Window.</li> </ul> </li> <li>PL2 Power Limit <ul> <li>Press the &lt;+&gt; / &lt;-&gt; keys to increase or decrease the desired values.</li> </ul> </li> <li>PL2 Timer Window <ul> <li>Configure PL1 Timer Window.</li> </ul> </li> </ul>

## 5-3-7 PCH Configuration

PCH-IO Configuration	Device Options Settings
	++: Select Screen 11: Select Item K/M: Scroll Help Area Up/Down. Enter: Select +/-: Change Opt. F1: General Help F3: Previous Values F9: Optimized Defaults F10: Save & Exit ESC: Exit

Parameter	Description
PCH-IO Configuration	
SATA And RST Configuration	<ul> <li>Press [Enter] to configure advanced items.</li> <li>SATA Configuration <ul> <li>Enable/Disable SATA controller.</li> <li>Options available: Enabled, Disabled. Default setting is Enabled.</li> </ul> </li> <li>SATA Mode Selection <ul> <li>Configures on chip SATA type.</li> <li>AHCI Mode: When set to AHCI, the SATA controller enables its AHCI functionality. Then the RAID function is disabled and cannot be access the RAID setup utility at boot time.</li> <li>RAID Mode: When set to RAID, the SATA controller enables both its RAID and AHCI functions. You will be allowed to access the RAID setup utility at boot time.</li> <li>Options available: AHCI, RAID. Default setting is AHCI.</li> </ul> </li> <li>RAID Device ID<sup>[Note]</sup> <ul> <li>Choose RAID Device ID.</li> <li>Options available: Client, Alternate, Server. Default setting is Server.</li> </ul> </li> <li>SATA Port 0/1/2/3/4/5/6/7 <ul> <li>The category identifies SATA hard drives that are installed in the computer. System will automatically detect HDD type.</li> </ul> </li> </ul>

Parameter	Description
SATA And RST Configuration (continued)	<ul> <li>Port 0/1/2/3/4/5/6/7         <ul> <li>Enable/Disable Port 0/1/2/3/4/5/6/7 device.</li> <li>Options available: Enabled, Disabled. Default setting is Enabled.</li> </ul> </li> <li>Hot Plug (for Port 0/1/2/3/4/5/6/7)         <ul> <li>Enable/Disable HDD Hot-Plug function.</li> <li>Options available: Enabled, Disabled. Default setting is Enabled.</li> </ul> </li> <li>Spin Up Device (for Port 0/1/2/3/4/5/6/7)         <ul> <li>On an edge detect from 0 to 1, the PCH starts a COM reset initialization to the device.</li> <li>Options available: Enabled, Disabled. Default setting is Disabled.</li> </ul> </li> </ul>

### 5-3-8 Miscellaneous Configuration

Chipset	Aptio Setup — AMI	
Miscellaneous Configuration		Select active Video type
Active Video External SSC - CK440	[Onboard Device] [SSC Off]	
		++: Select Screen 11: Select Item K/M: Scroll Help Area Up/Down. Enter: Select +/-: Change Opt. F1: General Help F3: Previous Values F9: Optimized Defaults F10: Save & Exit ESC: Exit
Ver	sion 2.22.1290 Copyright (C) 20	24 AMI

Parameter	Description
Miscellaneous Configuration	
	Selects the active video type.
Active Video	Options available: Auto, Onboard Device, PCIE Device, Specific PCIE
	Device. Default setting is Onboard Device.
	Enables Spread spectrum - only affects external clock generator.
External SSC - CK440	Options available: SSC Off, SSC = -0.3%, SSC = -0.5%, Hardware.
	Default setting is SSC Off.

## 5-3-9 Server ME Configuration

General ME Configuration Oper. Firmware Version 18:6.1.4.47 ME Firmware Status #1 0x0000355 ME Firmware Status #2 0x8950C026 Current State Dperational Error Code No Error Recovery Cause N/A	Aptio Setup - AMI Chipset		
11: Sel K/M: Sc Up/Doun Enter: 1 +/-: Ch F1: Gen F3: Pre F3: Pre F9: Opt	Select Hange Opt. Meral Help Vevious Values Himized Defaults Have & Exit		

Parameter	Description
General ME Configuration	
Oper. Firmware Version	Displays the operational firmware version.
ME Firmware Status #1/#2	Displays ME Firmware status information.
Current State	Displays ME Firmware current status information.
Error Code	Displays ME Firmware status error code.
Recovery Cause	Displays ME Firmware recovery cause.

### 5-3-10 Runtime Error Logging Settings

Chipset	Aptio Setup – AMI	
Runtime Error Logging		System Error Enable/Disable setup
System Errors • Whea Settings • Memory Error Enabling • IIO Error Enabling • PCIe Error Enabling	(Enable)	options.
		<pre>++: Select Screen 11: Select Item K/M: Scroll Help Area Up/Down. Enter: Select +/-: Change Opt. F1: General Help F3: Previous Values F9: Optimized Defaults F10: Save &amp; Exit ESC: Exit</pre>
Vers	ion 2.22.1290 Copyright (C) 20	24 AMI

Parameter	Description	
Runtime Error Logging		
Custom Erroro	Enable/Disable system error logging function.	
System Errors	Options available: Enable, Disable. Default setting is <b>Enable</b> .	
	Press [Enter] to configure advanced items.	
Whee Settinge	<ul> <li>WHEA (Windows Hardware Error Architecture) Support</li> </ul>	
Whea Settings	<ul> <li>Enable/Disable WHEA Support.</li> </ul>	
	<ul> <li>Options available: Enable, Disable. Default setting is Enable.</li> </ul>	
	Press [Enter] to configure advanced items.	
	Memory Corrected Error	
	<ul> <li>Enable/Disable Memory Corrected Error.</li> </ul>	
Memory Error Enabling	<ul> <li>Options available: Enable, Disable. Default setting is Enable.</li> </ul>	
	Uncorrected Error disable Memory	
	<ul> <li>Enable/Disable the Memory that triggers Uncorrected Error.</li> </ul>	
	<ul> <li>Options available: Enable, Disable. Default setting is <b>Disable</b>.</li> </ul>	
	Press [Enter] to configure advanced items.	
	Os Native AER Support	
IIO Error Enabling	<ul> <li>Select FFM or OS native for AER error handling. If select OS</li> </ul>	
	native, BIOS also initialize FFM first until handshake, which	
	depends on OS capability.	
	<ul> <li>Options available: Enable, Disable. Default setting is <b>Disable</b>.</li> </ul>	

Parameter	Description
PCle Error Enabling	<ul> <li>Press [Enter] to configure advanced items.</li> <li>PCIE Error <ul> <li>Enable/Disable PCIE error.</li> <li>Options available: Enable, Disable. Default setting is <b>Disable</b>.</li> </ul> </li> <li>Uncorrected Error<sup>(Note)</sup> <ul> <li>Enables and escalates Uncorrectable/Recoverable Errors to error pins.</li> <li>Options available: Enable, Disable. Default setting is <b>Enable</b>.</li> </ul> </li> <li>Fatal Error Enable<sup>(Note)</sup> <ul> <li>Enables and escalates Fatal Errors to error pins.</li> <li>Options available: Enable, Disable. Default setting is <b>Enable</b>.</li> </ul> </li> <li>Fatal Error Enable<sup>(Note)</sup> <ul> <li>Enables and escalates Fatal Errors to error pins.</li> <li>Options available: Enable, Disable. Default setting is <b>Enable</b>.</li> </ul> </li> <li>Assert NMI on SERR<sup>(Note)</sup> <ul> <li>Enable/Disable BIOS generates a non-maskable interrupt (NMI) and logs an error when a system error (SERR) occurs.</li> <li>Options available: Enabled, Disabled. Default setting is <b>Enabled</b>.</li> </ul> </li> <li>Assert NMI on PERR<sup>(Note)</sup> <ul> <li>Enable/Disable BIOS generates a non-maskable interrupt (NMI) and logs an error when a system error (SERR) occurs.</li> <li>Options available: Enabled, Disabled. Default setting is <b>Enabled</b>.</li> </ul> </li> <li>Assert NMI on PERR<sup>(Note)</sup> <ul> <li>Enable/Disable BIOS generates a non-maskable interrupt (NMI) and logs an error when a processor bus parity error (PERR) occurs.</li> <li>Options available Enabled, Disabled. Default setting is <b>Enabled</b>.</li> </ul> </li> </ul>

### 5-3-11 Power Policy

Chipset	Aptio Setup – AMI	
Power Policy Quick Settings SpeedStep (Pstates) Turbo Mode CPU G6 report Enhanced Halt State (C1E) Package C State Enable LP [Blobal] Handware Prefetcher Adjacent Cache Prefetch DCU Streamer Prefetcher Intel VT for Directed I/0	[Standard] [Enable] [Auto] [Auto] [Auto] [Auto] [AlL LPS] [Enable] [Enable] [Enable] [Enable] [Enable]	Select a Power Policy Quick Setting(The following items will be set based on the selected power policy)
		<pre>++: Select Screen 14: Select Item K/M: Scroll Help Area Up/Doum. Enter: Select +/-: Change Opt. F1: General Help F3: Previous Values F9: Optimized Defaults F10: Save &amp; Exit ESC: Exit</pre>

Parameter	Description
	Selects a Power Policy Quick Setting.
Power Policy Quick Settings	Options available: Standard, Best Performance, Energy Efficient. Default
	setting is Standard.
	Conventional Intel SpeedStep Technology switches both voltage and
SpeedStep (Pstates)	frequency in tandem between high and low levels in response to processor
Speedslep (Fsiales)	load.
	Options available: Enable, Disable. Default setting is <b>Enable</b> .
	When this item is enabled, the processor will automatically ramp up the
Turbo Mode	clock speed of 1-2 of its processing cores to improve its performance.
	When this item is disabled, the processor will not overclock any of its core.
	Options available: Enable, Disable. Default setting is <b>Enable</b> .
	Enable/Disable the BIOS to enable the report from the CPU C6 state (ACPI
CPU C6 report	C3) to the OS.
	Options available: Disable, Enable, Auto. Default setting is Auto.
	Enable/Disable the C1E support for lower power consumption. Takes effect
Enhanced Halt State (C1E)	after reboot.
	Options available: Enable, Disable. Default setting is <b>Enable</b> .
	Configures the C-State package limit.
Package C State	Options available: C0/C1 state, C2 state, C6(non Retention) state,
	C6(Retention) state, No Limit, Auto. Default setting is Auto.

Parameter	Description
	Enables Logical processor (Software Method to Enable/Disable Logical
Enable LP [Global]	Processor threads).
	Options available: ALL LPs, Single LP. Default setting is ALL LPs.
Hardware Prefetcher	Options available: Enable, Disable. Default setting is <b>Enable</b> .
Adjacent Cache Prefetch	Options available: Enable, Disable. Default setting is <b>Enable</b> .
DCU Streamer Prefetcher	Options available: Enable, Disable. Default setting is <b>Enable</b> .
Intel® VT for Directed I/O	Enable/Disable the Intel VT for Directed I/O (VT-d) support function by reporting the I/O device assignment to VMM through DMAR ACPI Tables. Options available: Enable, Disable. Default setting is <b>Enable</b> .

## 5-4 Server Management Menu

Main Advanced Chipset Server	Aptio Setup – AMI * Mgmt Security Boot Save & Ex	it		
FRB-2 Timer FRB-2 Timer Policy OS Watchdog Timer OS Wit Timer Policy OS Wit Timer Policy Wait BMC Ready > System Event Log > View FRU information > BMC VLAN Configuration > BMC retwork configuration	[Disabled] 6 [Do Nothing] [Disabled] 10 [Reset] [2 minutes]	Enable or Disable FRB-2 timer(FOST timer)		
▶ IPv6 BMC Network Configuration		<pre>++: Select Screen 11: Select Item K/M: Scroll Help Area Up/Doun. Enter: Select +/-: Change Opt. F1: General Help F3: Previous Values F9: Optimized Defaults F10: Save &amp; Exit ESC: Exit</pre>		
Version 2.22.1290 Copyright (C) 2024 AMI				

Parameter	Description
FRB-2 Timer	Enable/Disable FRB-2 timer (POST timer). Options available: Enabled, Disabled. Default setting is <b>Enabled</b> .
FRB-2 Timer <sup>(Note1)</sup> timeout	Configures the FRB2 Timer timeout. The value is between 1 to 30 minutes. Default setting is <b>6 minutes</b> .
FRB-2 Timer Policy <sup>(Note1)</sup>	Configures the FRB2 Timer policy. Options available: Do Nothing, Reset, Power Down, Power Cycle. Default setting is <b>Do Nothing</b> .
OS Watchdog Timer	Enable/Disable OS Watchdog Timer function. Options available: Enabled, Disabled. Default setting is <b>Disabled</b> .
OS Wtd Timer Timeout <sup>(Note2)</sup>	Configures OS Watchdog Timer. The value is between 1 to 30 minutes. Default setting is <b>10 minutes</b> .
OS Wtd Timer Policy <sup>(Note2)</sup>	Configure OS Watchdog Timer Policy. Options available: Reset, Do Nothing, Power Down, Power Cycle. Default setting is <b>Reset</b> .
Wait BMC Ready	POST wait BMC ready and reboot system. Options available: Disabled, 2 minutes, 4 minutes, 6 minutes. Default setting is <b>2 minutes</b> .

(Note1) This item is configurable when **FRB-2 Timer** is set to **Enabled**.

(Note2) This item is configurable when OS Watchdog Timer is set to Enabled.

Parameter	Description	
System Event Log	Press [Enter] to configure advanced items.	
View FRU Information	Press [Enter] to view the FRU information.	
BMC VLAN Configuration	Press [Enter] to configure advanced items.	
BMC network Configuration	Press [Enter] to configure advanced items.	
IPv6 BMC Network Configuration	Press [Enter] to configure advanced items.	

## 5-4-1 System Event Log

Serve	Aptio Setup – AMI r Mgmt	
Enabling/Disabling Options SEL Components		Change this to enable or disable event logging for
Erasing Settings Erase SEL	[No]	error/progress codes during boot.
When SEL is Full	[NO] [Do Nothing]	
Custom EFI Logging Options Log EFI Status Codes	[Error code]	
NOTE: All values changed here d effect until computer is n		
		++: Select Screen 14: Select Item K/M: Scroll Help Area Up/Down.
		Enter: Select +/-: Change Opt. F1: General Help F3: Previous Values
		F9: Optimized Defaults F10: Save & Exit ESC: Exit
Vers	ion 2.22.1290 Copyright (C)	2024 AMI

Parameter	Description	
Enabling / Disabling Options		
SEL Components	Change this item to enable or disable all features of System Event Logging during boot. Options available: Enabled, Disabled. Default setting is <b>Enabled</b> .	
Erasing Settings		
Erase SEL	Choose options for erasing SEL. Options available: No, Yes, On next reset, Yes, On every reset. Default setting is <b>No</b> .	
When SEL is Full	Choose options for reactions to a full SEL. Options available: Do Nothing, Erase Immediately, Delete Oldest Record. Default setting is <b>Do Nothing</b> .	
Custom EFI Logging Options		
Log EFI Status Codes	Enable/Disable the logging of EFI Status Codes (if not already converted to legacy). Options available: Disabled, Both, Error code, Progress code. Default setting is <b>Error code</b> .	

### 5-4-2 View FRU Information

The FRU page is a simple display page for basic system ID information, as well as System product information. Items on this window are non-configurable.

	Aptio Setup – AMI Server Mgmt	
FRU Information		
System Manufacturer System Product Name System Version System Serial Number Board Manufacturer Board Part Number Board Part Number Chassis Manufacturer Chassis Part Number Chassis Part Number	Giga Computing	++: Select Screen 14: Select Item K/M: Scroll Help Area Up/Doun. Enter: Select +/-: Change Opt. F1: General Help F3: Previous Values F9: Optimized Defaults F10: Save & Exit ESC: Exit
	Version 2.22.1290 Copyright (C) 2024 AMI	

## 5-4-3 BMC VLAN Configuration

BMC VLAN Configuration		VLAN ID of new VLAN or existing VLAN, valid valu
EMC VLAN ID BMC VLAN Priority	0	is Off094, O is disable VLAN
		<pre>++: Select Screen 11: Select Item K/M: Scroll Help Area Up/Down. Enter: Select +/-: Change Opt. F1: General Help F3: Previous Values F9: Optimized Defaults F10: Save &amp; Exit ESC: Exit</pre>

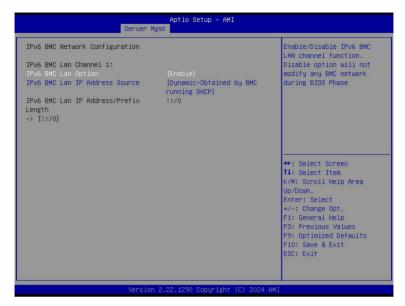
Parameter	Description
BMC VLAN Configuration	
BMC VLAN ID	Select to configure BMC VLAN ID. The valid range is from 0 to 4094. When
2	set to 0, BMC VLAN ID will be disabled.
BMC VLAN Priority	Select to configure BMC VLAN Priority. The valid range is from 0 to 7.
	When BMC VLAN ID is set to 0, BMC VLAN Priority will not be selected.

## 5-4-4 BMC Network Configuration

Server	
BMC network configuration select NCSI and Dedicated LAN Lan channel 1 Configuration Address source Station IP address Subnet mask Router IP address Station MAC address Real-time get BMC network address	Select to configure LAN channel parameters statically or dynamically(DHCP). Do nothing option will not modify any BMC network parameters during BIOS phase
	++: Select Screen 14: Select Item K/M: Scroll Help Area Up/Down. Enter: Select +/-: Change Opt. F1: General Help F3: Previous Values F9: Optimized Defaults F10: Save & Exit ESC: Exit

Parameter	Description
BMC network configuration	
Select NCSI and Dedicated LAN	Options available: Do Nothing, Model1(Dedicated), Model2(NCSI), Mode3(Failover). Default setting is <b>Do Nothing</b> .
Lan Channel 1	
Configuration Address source	Selects to configure LAN channel parameters statically or dynamically (DHCP). Options available: Unspecified, Static, DynamicBmcDhcp. Default setting is <b>DynamicBmcDhcp</b> .
Station IP address	Displays IP Address information.
Subnet mask	Displays Subnet Mask information. Please note that the IP address must be in three digitals, for example, 192.168.000.001.
Router IP address	Displays the Router IP Address information.
Station MAC address	Displays the MAC Address information.
Real-time get BMC network address	Press [Enter] will set LAN mode and Address source and then get IP, Subnet, Gateway and MAC address.

### 5-4-5 IPv6 BMC Network Configuration



Parameter	Description
IPv6 BMC network configuration	
IPv6 BMC Lan Channel 1	
IPv6 BMC Lan Option	Enable/Disable IPv6 BMC LAN channel function. When this item is disabled, the system will not modify any BMC network during BIOS phase. Options available: Unspecified, Disable, Enable. Default setting is Enable.
IPv6 BMC Lan IP Address Source	Selects to configure LAN channel parameters statically or dynamically (by BIOS or BMC). Options available: Unspecified, Static, Dynamic-Obtained by BMC running DHCP. Default setting is <b>Dynamic-Obtained by BMC running DHCP</b> .
IPv6 BMC Lan IP Address/ Prefix Length	Check if the IPv6 BMC LAN IP address matches those displayed on the screen.

# 5-5 Security Menu

The Security menu allows you to safeguard and protect the system from unauthorized use by setting up access passwords.

Main Advanced Chipset	Aptio Setup – AM Server Mgmt <mark>Security Boot</mark>	
Password Description		Sets administrative password
If ONLY the Administrator' then this only limits acce only asked for when enteri If ONLY the User's passwor is a power on password and boot or enter Setup. In Se have Administrator rights.	ss to Setup and is ng Setup. d is set, then this must be entered to tup the User will	
The password length must b in the following range:	e	
Minimum length	3	
Maximum length	20	++: Select Screen
		↑↓: Select Item
Administrator Password		K/M: Scroll Help Area
User Password		Up/Down. Enter: Select +/-: Change Opt.
▶ Media Sanitization		F1: General Help
▶ Secure Boot		F3: Previous Values F9: Optimized Defaults F10: Save & Exit ESC: Exit
	Version 2.22.1290 Copyright	(D) 0004 ANT

There are two types of passwords that you can set:

Administrator Password

Entering this password will allow the user to access and change all settings in the Setup Utility.

User Password

Entering this password will restrict a user's access to the Setup menus. To enable or disable this field, a Administrator Password must first be set. A user can only access and modify the System Time, System Date, and Set User Password fields.

Parameter	Description
Administrator Password	Press [Enter] to configure the administrator password.
User Password	Press [Enter] to configure the user password.
Media Sanitization	Press [Enter] to configure advanced items.
Secure Boot	Press [Enter] to configure advanced items.

#### 5-5-1 Secure Boot

The Secure Boot feature is applicable if supported by your Operating System.

If your Operating System is not supporting Secure Boot, the system will hang when starting the Operating System.

System Mode	Setup	Secure Boot feature is Active if Secure Boot is
	(Disabled) Not Active	Enabled, Platform Key(PK) is
Secure Boot Mode	[Custom]	enrolled and the System is in User mode.
Restore Factory Keys	(Cus (Only	The mode change requires
Reset To Setup Mode		platform reset
Expert Key Management		
		++: Select Screen
		↑↓: Select Item
		K/M: Scroll Help Area Up/Down.
		Enter: Select
		+/-: Change Opt.
		F1: General Help
		F3: Previous Values
		F9: Optimized Defaults F10: Save & Exit
		ESC: Exit

Parameter	Description
System Mode	Displays if the system is in User mode or Setup mode.
Secure Boot	Enable/ Disable the Secure Boot function. Options available: Enabled, Disabled. Default setting is <b>Disabled</b> .
Secure Boot Mode <sup>(Note)</sup>	Secure Boot requires all the applications that are running during the booting process to be pre-signed with valid digital certificates. This way, the system knows all files being loaded before the Operating System loads to the login screen have not been tampered with. When set to Standard, it will automatically load the Secure Boot keys form the BIOS databases. When set to Custom, you can customize the Secure Boot settings and manually load its keys from the BIOS database. Options available: Standard, Custom. Default setting is <b>Standard</b> .
Restore Factory Keys	Forces the system to user mode and installs factory default Secure Boot key database.
Reset To Setup Mode	Reset the system to Setup Mode.

(Note) Advanced items prompt when this item is set to Custom.

Parameter	Description
Expert Key Management	<ul> <li>Press [Enter] to configure advanced items.</li> <li>Please note that this item is configurable when Secure Boot Mode is set to Custom.</li> <li>Factory Key Provision <ul> <li>Allows to provision factory default Secure Boot keys when system is in Setup Mode.</li> <li>Options available: Enabled, Disabled. Default setting is Disabled.</li> </ul> </li> <li>Restore Factory Keys <ul> <li>Installs all factory default keys. It will force the system in User Mode.</li> <li>Options available: Yes, No.</li> </ul> </li> <li>Reset To Setup Mode <ul> <li>Reset the system to Setup Mode.</li> <li>Options available: Yes, No.</li> </ul> </li> <li>Enroll Efi Image <ul> <li>Press [Enter] to enroll SHA256 hash of the binary into Authorized Signature Database (db).</li> </ul> </li> <li>Export Secure Boot variables <ul> <li>Copy NVRAM content of Secure Boot variables to files in a root folder on a file system device.</li> </ul> </li> <li>Secure Boot variable <ul> <li>Displays the current status of the variables used for secure boot.</li> </ul> </li> <li>Platform Key (PK) <ul> <li>Displays the current status of the Platform Key (PK).</li> <li>Press [Enter] to configure a new PK.</li> <li>Options available: Update.</li> </ul> </li> <li>Key Exchange Keys (KEK) <ul> <li>Displays the current status of the Key Exchange Key Database (KEK).</li> <li>Press [Enter] to configure a new KEK or load additional KEK from storage devices.</li> <li>Options available: Update, Append.</li> </ul> </li> <li>Authorized Signatures (DB) <ul> <li>Displays the current status of the Authorized Signature Database.</li> <li>Press [Enter] to configure a new DB or load additional DB from storage devices.</li> <li>Options available: Update, Append.</li> </ul> </li> <li>Forbidden Signatures (DBX) <ul> <li>Displays the current status of the Forbidden Signature Database.</li> <li>Press [Enter] to configure a new dbx or load additional dbx from storage devices.</li> <li>Options available: Update, Append.</li> </ul> </li> </ul>

Parameter	Description
Key Management (continued)	<ul> <li>Authorized TimeStamps (DBT)         <ul> <li>Displays the current status of the Authorized TimeStamps Database.</li> <li>Press [Enter] to configure a new DBT or load additional DBT from storage devices.</li> <li>Options available: Update, Append.</li> </ul> </li> <li>OsRecovery Signatures         <ul> <li>Displays the current status of the OsRecovery Signature Database.</li> <li>Press [Enter] to configure a new OsRecovery Signature or load additional OsRecovery Signature from storage devices.</li> <li>Options available: Update, Append.</li> </ul> </li> </ul>

#### Boot Menu 5-6

The Boot menu allows you to set the drive priority during system boot-up. BIOS setup will display an error message if the legacy drive(s) specified is not bootable.

- Ali March Mairtheann A	Aptio Setup – AMI	1
Main Advanced Chipset Server Mg	nt Security Boot Save & Exit	
Boot Configuration Setup Prompt Timeout Bootup NumLock State Quiet Boot	1 [On] [Enabled]	Set the default timeout before system boot. A value of 65535 will disable the timeout
Endless Retry Boot	[Disable]	completely.
Setup Flash Dump full Setup Data Dump non-default Setup Data Restore Setup Data Driver Option Priorities FIXED BOOT ORDER Priorities Boot Option #1 Boot Option #2 Boot Option #3 Boot Option #4	[Hard Disk] [CD/DVD] [USB Device:ubuntu (Samsung Flash Drive 1100, Partition 1)] [Network:UEFI: PXE IPv4 Intel(R) Ethernet Controller X710 for 106BASE-T 10:FF:E0:30:99:A6]	++: Select Screen 11: Select Item K/M: Scroll Help Area Up/Down. Enter: Select 4/-: Change Ont. F1: General Help F3: Previous Values F9: Optimized Defaults F10: Save & Exit ESC: Exit
Version	2.22.1290 Copyright (C) 2024 AM	I B4
Main Advanced Chipset Server Mg	Aptio Setup – AMI mt Security Boot Save & Exit	
Main Advanced Chipset Server Mg Setup Flash Dump full Setup Data Dump non-default Setup Data Restore Setup Data Driver Option Priorities		Specifies the Boot Device Priority sequence from available UEFI Application.
Setup Flash Dump full Setup Data Dump non-default Setup Data Restore Setup Data	nt Security Boot Save & Exit (Hard Disk) (DD/DVD) (USB Device:ubuntu (Samsung Flash Drive 1100,	Priority sequence from available UEFI Application.
Setup Flash Dump full Setup Data Dump non-default Setup Data Restore Setup Data Driver Option Priorities FIXED BOOT ORDER Priorities Boot Option #1 Boot Option #2	nt Security Boot Save & Exit [Hard Disk] [D2/DVD] [USB Device:ubuntu (Samsung Flash Drive 1100, Partition 1)] [Network:UEFI: PXE IPv4 Intel(R) Ethernet Controller X710 for 1068ASE-T	Priority sequence from available UEFI Application. 
Setup Flash Dump full Setup Data Dump non-default Setup Data Restore Setup Data Driver Option Priorities FIXED BOOT ORDER Priorities Boot Option #1 Boot Option #2 Boot Option #3	nt Security Boot Save & Exit [Hand Disk] [CD/DVD] [USB Device:ubuntu (Samsung Flash Drive 1100, Partition 1)] [Network:UEF1: PXE IPv4 Intel(R) Ethernet Controller X710 for	Priority sequence from available UEFI Application. 
Setup Flash Dump full Setup Data Dump non-default Setup Data Restore Setup Data Driver Option Priorities FIXED BOOT ORDER Priorities Boot Option #1 Boot Option #2 Boot Option #3 Boot Option #4	nt Security Boot Save & Exit [Hand Disk] [CD/DVD] [USB Device:ubuntu (Samsung Flash Drive 1100, Partition 1)] [Network:UEF1: FXE IPv4 Intel(R) Ethernet Controller X710 for 106BASE-T 10:FF:E0:30:99:A6] [UEFI AP:UEF1: Bullt-in	<pre>Priority sequence from available UEFI Application.  ++: Select Screen 11: Select Item K/M: Scroll Help Area Up/Doum. Enter: Select +/-: Change Opt. F1: General Help F3: Previous Values</pre>

Parameter	Description
Boot Configuration	
Setup Prompt Timeout	Number of seconds to wait for setup activation key. 65535 (0xFFFF) means indefinite waiting. Press the numeric keys to input the desired values.
Bootup NumLock State	Enable/Disable the Bootup NumLock function. Options available: On, Off. Default setting is <b>On</b> .
Quiet Boot	Enable/Disable showing the logo during POST. Options available: Enabled, Disabled. Default setting is <b>Enabled</b> .
Endless Retry Boot	Options available: Disable, Enable. Default setting is <b>Disable</b> .
Setup Flash	Press [Enter] to run setup flash.
Dump full Setup Data	Press [Enter] to dump full setup data to file.
Dump non-default Setup Data	Press [Enter] to dump non-default setup data to file.
Restore Setup Data	Press [Enter] to restore setup data from file.
FIXED BOOT ORDER Priorities	
Boot Option #1 / #2 / #3 / #4 / #5	Press [Enter] to configure the boot order priority. By default, the server searches for boot devices in the following sequence: 1. Hard drive. 2. CD-COM/DVD drive. 3. USB device. 4. Network. 5. UEFI.
UEFI Network Drive BBS Priorities	Press [Enter] to configure the boot priority.
UEFI Application Boot Priorities	Press [Enter] to configure the boot priority.

# 5-7 Save & Exit Menu

The Save & Exit menu displays the various options to quit from the BIOS setup. Highlight any of the exit options then press <Enter>.

Aptio Setup – AMI Main Advanced Chipset Server Mgmt Security Boot <mark>Save &amp; Exit</mark>	
Save Options Save & Exit Discard changes & exit Save Changes and Reset	Exit system setup after saving the changes.
Save Changes and Reset	
Discard Changes Default Options Restore Defaults Save the User Default Values Restore the User Default Values Boot Device Priority ubuntu (Samsung Flash Drive 1100, Partition 1) UEFI: PXE IPv4 Intel(R) Ethernet Controller X710 for 10GBASE-T 10:FF:E0:30:99:A6 UEFI: PXE IPv4 Intel(R) Ethernet Controller X710 for 10GBASE-T 10:FF:E0:30:99:A7 UEFI: Built-in EFI Shell Launch EFI Shell	++: Select Screen 1: Select Item K/M: Scroll Help Area Up/Doum. Enter: Select +/-: Change Opt. F1: General Help F3: Potnius2d Defaults F3: Optimized Defaults F10: Save & Exit ESC: Exit

Version 2.22.1290 Copyright (C) 2024 AMI

Parameter	Description
Save Options	
Save and Exit	Saves changes made and closes the BIOS setup. Options available: Yes, No.
Discard changes and exit	Discards changes made and exits the BIOS setup. Options available: Yes, No.
Save Changes and Reset	Restarts the system after saving the changes made. Options available: Yes, No.
Discard Changes and Reset	Restarts the system without saving any changes. Options available: Yes, No.
Save Changes	Saves changes done so far to any of the setup options. Options available: Yes, No.
Discard Changes	Discards changes made and closes the BIOS setup. Options available: Yes, No.
Default Options	

Parameter	Description
Restore Defaults	Loads the default settings for all BIOS setup parameters. Setup Defaults are quite demanding in terms of resources consumption. If you are using low-speed memory chips or other kinds of low-performance components and you choose to load these settings, the system might not function properly. Options available: Yes, No.
Save the User Default Values	Saves the changes made as the user default settings. Options available: Yes, No.
Restore the User Default Values	Loads the user default settings for all BIOS setup parameters. Options available: Yes, No.
Boot Device Priority	Press [Enter] to configure the device as the boot-up drive.
Launch EFI Shell	Attempts to Launch EFI Shell application (Shell.efi) from one of the available file system devices.

# 5-8 BIOS Recovery

The system has an embedded recovery technique. In the event that the BIOS becomes corrupt the boot block can be used to restore the BIOS to a working state. To restore your BIOS, please follow the instructions listed below:

Recovery Instruction:

- 1. Copy the XXX.rom to USB diskette.
- 2. Setting BIOS Recovery jump to enabled status.
- 3. Boot into BIOS recovery.
- 4. Run Proceed with flash update.
- 5. BIOS updated.

