GIGABYTE[™]

G493-SB3-AAP1

HPC/AI Server - 4th/5th Gen Intel® Xeon® Scalable - 4U DP 8 x PCIe Gen5 GPUs

User Manual

Rev. 1.0

Copyright

© 2023 Giga Computing Technology CO., LTD. All rights reserved. The trademarks mentioned in this manual are legally registered to their respective owners.

Disclaimer

Information in this manual is protected by copyright laws and is the property of Giga Computing. Changes to the specifications and features in this manual may be made by Giga Computing without prior notice. No part of this manual may be reproduced, copied, translated, transmitted, or published in any form or by any means without Giga Computing's prior written permission.

Documentation Classifications

In order to assist in the use of this product, Giga Computing provides the following types of documentation:

- User Manual: detailed information & steps about the installation, configuration and use of this product (e.g. motherboard, server barebones), covering hardware and BIOS.
- User Guide: detailed information about the installation & use of an add-on hardware or software component (e.g. BMC firmware, rail-kit) compatible with this product.
- Quick Installation Guide: a short guide with visual diagrams that you can reference easily for installation purposes of this product (e.g. motherboard, server barebones).

Please see the support section of the online product page to check the current availability of these documents.

For More Information

For related product specifications, the latest firmware and software, and other information please visit our website at http://www.gigabyte.com/Enterprise

For GIGABYTE distributors and resellers, additional sales & marketing materials are available from our reseller portal: http://reseller.b2b.gigabyte.com

For further technical assistance, please contact your GIGABYTE representative or visit https://esupport.gigabyte.com/ to create a new support ticket

For any general sales or marketing enquiries, you may also message GIGABYTE server directly by email: server.grp@gigabyte.com

Conventions

The following conventions are used in this user's guide:

L.	NOTE! Gives bits and pieces of additional information related to the current topic.		
	CAUTION! Gives precautionary measures to avoid possible hardware or software problems.		
	WARNING! Alerts you to any damage that might result from doing or not doing specific actions.		

Server Warnings and Cautions

Before installing a server, be sure that you understand the following warnings and cautions.

To reduce the risk of electric shock or damage to the equipment:

- Do not disable the power cord grounding plug. The grounding plug is an important safety feature.
- Plug the power cord into a grounded (earthed) electrical outlet that is easily accessible at all times.
- · Unplug all the power cords from the power supplies to disconnect power to the equipment.



- Shock Hazard! Disconnect all power supply cords before servicing.
- Do not route the power cord where it can be walked on or pinched by items placed against it. Pay particular
 attention to the plug, electrical outlet, and the point where the cord extends from the server.

To reduce the risk of personal injury from hot surfaces, allow the drives and the internal system components to cool before touching them.

This server is equipped with high speed fans. Keep away from hazardous moving fan blades during servicing.



This equipment is intended to be used in Restrict Access Location. The access can only be gained by Skilled person. Only authorized by well trained professional person can access the restrict access location.

The equipment should only be repaired, maintained or replaced by skilled personnel.

- Do not operate the server for long periods with the access panel open or removed. Operating the server in this manner results in improper airflow and improper cooling that can lead to thermal damage.
- · Danger of explosion if battery is incorrectly replaced.
- · Replace only with the same or equivalent type recommended by the manufacturer.
- · Dispose of used batteries according to the manufacturer's instructions.

Risk of explosion if battery is replaced incorrectly or with an incorrect type. Replace the battery only with the same or equivalent type recommended by the manufacturer. Dispose of used batteries according to the manufacturer's instructions.

Electrostatic Discharge (ESD)

ESD CAN DAMAGE DRIVES, BOARDS, AND OTHER PARTS. WE RECOMMEND THAT YOU PERFORM ALL PROCEDURES AT AN ESD WORKSTATION. IF ONE IS NOT AVAILABLE, PROVIDE SOME ESD PROTECTION BY WEARING AN ANTI-STATIC WRIST STRAP AT-TACHED TO CHASSIS GROUND -- ANY UNPAINTED METAL SURFACE -- ON YOUR SERVER WHEN HANDLING PARTS.

Always handle boards carefully. They can be extremely sensitive to ESD. Hold boards only by their edges without any component and pin touching. After removing a board from its protective wrapper or from the system, place the board component side up on a grounded, static free surface. Use a conductive foam pad if available but not the board wrapper. Do not slide board over any surface.

System power on/off: To remove power from system, you must remove the system from rack. Make sure the system is removed from the rack before opening the chassis, adding, or removing any non hot-plug components.

Hazardous conditions, devices and cables: Hazardous electrical conditions may be present on power, telephone, and communication cables. Turn off the system and discon-nect the cables attached to the system before servicing it. Otherwise, personal injury or equipment damage can result.

Electrostatic discharge (ESD) and ESD protection: ESD can damage drives, boards, and other parts. We recommend that you perform all procedures in this chapter only at an ESD workstation. If one is not available, provide some ESD protection by wearing an antistatic wrist strap attached to chassis ground (any unpainted metal surface on the server) when handling parts.

ESD and handling boards: Always handle boards carefully. They can be extremely sensi-tive to electrostatic discharge (ESD). Hold boards only by their edges. After removing a board from its protective wrapper or from the system, place the board component side up on a grounded, static free surface. Use a conductive foam pad if available but not the board wrapper. Do not slide board over any surface.

Installing or removing jumpers: A jumper is a small plastic encased conductor that slips over two jumper pins. Some jumpers have a small tab on top that can be gripped with fin-gertips or with a pair of fine needle nosed pliers. If the jumpers do not have such a tab, take care when using needle nosed pliers to remove or install a jumper; grip the narrow sides of the jumper with the pliers, never the wide sides. Gripping the wide sides can dam-age the contacts inside the jumper, causing intermittent problems with the function con-trolled by that jumper. Take care to grip with, but not squeeze, the pliers or other tool used to remove a jumper, or the pins on the board may bend or break.

Table of Contents

Chapter 1 Hardware Installation			9	
	1-1	Inst	allation Precautions	9
	1-2	Pro	duct Specifications	10
	1-3	Sys	tem Block Diagram	14
Chapter 2	Syste	em A	ppearance	15
	2-1	Fro	nt View	15
	2-2	Rea	ar View	15
	2-3	Fro	nt Panel LED and Buttons	16
	2-3	3-1	RoT LEDs	17
	2-4	Fro	nt Panel System LAN LEDs	19
	2-5	Pov	ver Supply Unit (PSU) LED	
	2-6	Har	d Disk Drive LEDs	21
Chapter 3	Syste	em H	lardware Installation	22
	3-1	Rer	noving and Installing the Chassis Top Cover	23
	3-2	Inst	alling the GPU Card	24
	3-3	Inst	alling the PCI Expansion Card	
	3-4	Rer	noving and Installing the Heat Sink	
	3-5	Inst	alling the CPU	
	3-6	Inst	alling the Memory	30
	3-6	6-1	Eight Channel Memory Configuration	
	3-6	6-2	Installing the Memory	31
	3-6	6-3	Memory Population Table	
	3-6		Processor and Memory Module Matrix Table	
	3-7		alling the Hard Disk Drive	
	3-8		placing the System Fan Module	
	3-9		noving and Installing the Power Supply	
	3-10	Cat	ble Connection	
		0-1		
			PCIe Board to Rear Side Top GPU Card Cable	
Observation 4			Motherboard to HDD Backplane Board and PCIe Board	
Chapter 4			ard Components	
	4-1		herboard Components	
	4-2	Jun	nper Setting	45

	4-3	G-8	SC Module	. 46
	4-3	3-1	CDCG120	46
4-4 Bac			kplane Board Storage Connector	. 47
	4-4	1-1	CBP20C7	47
Chapter 5	BIOS	Set	up	.48
	5-1	The	Main Menu	. 50
	5-2	٨d	vanced Menu	. 53
	5-2	2-1	Trusted Computing	54
	5-2	2-2	Serial Port Console Redirection	55
	5-2	2-3	SIO Configuration	58
	5-2	<u>2</u> -4	PCI Subsystem Settings	59
	5-2	2-5	USB Configuration	61
	5-2	2-6	Network Stack Configuration	62
	5-2	2-7	Post Report Configuration	63
	5-2	2-8	NVMe Configuration	64
	5-2	2-9	Chipset Configuration	65
	5-2	2-10	TIs Auth Configuration	66
	5-2	2-11	iSCSI Configuration	67
	5-2	2-12	Intel(R) Ethernet Controller X710 for 10GBASE-T	68
	5-2	2-13	VLAN Configuration	70
	5-2	2-14	Driver Health	71
	5-3	Chi	pset Menu	.72
	5-3	3-1	Processor Configuration	73
	5-3	3-2	Common RefCode Configuration	76
	5-3	3-3	UPI Configuration	77
	5-3	3-4	Memory Configuration	79
	5-3	3-5	IIO Configuration	82
	5-3	3-6	Advanced Power Management Configuration	84
	5-3	3-7	PCH Configuration	86
	5-3	8-8	Miscellaneous Configuration	88
	5-3	3-9	Server ME Configuration	89
	5-3	3-10	Runtime Error Logging Settings	90
	5-3	3-11	Power Policy	92
	5-4	Ser	ver Management Menu	. 94
	5-4	1-1	System Event Log	96
	5-4	1-2	View FRU Information	97
	5-4	1-3	BMC VLAN Configuration	98
	5-4	1-4	BMC Network Configuration	99
	5-4	1-5	IPv6 BMC Network Configuration	100

5-5	Sec	Security Menu 101				
5-	5-1	Secure Boot				
5-6	Boo	ot Menu	105			
5-7	Sav	Save & Exit Menu				
5-8	BIOS Recovery					
		S POST Beep code (AMI standard)				
5-	9-1	PEI Beep Codes	110			
5-	9-2	DXE Beep Codes	110			

Chapter 1 Hardware Installation

1-1 Installation Precautions

The motherboard/system contain numerous delicate electronic circuits and components which can become damaged as a result of electrostatic discharge (ESD). Prior to installation, carefully read the user manual and follow these procedures:

- Prior to installation, do not remove or break motherboard S/N (Serial Number) sticker or warranty sticker provided by your dealer. These stickers are required for warranty validation.
- Always remove the AC power by unplugging the power cord from the power outlet before installing or removing the motherboard or other hardware components.
- When connecting hardware components to the internal connectors on the motherboard, make sure they are connected tightly and securely.
- When handling the motherboard, avoid touching any metal leads or connectors.
- It is best to wear an electrostatic discharge (ESD) wrist strap when handling electronic components such as a motherboard, CPU or memory. If you do not have an ESD wrist strap, keep your hands dry and first touch a metal object to eliminate static electricity.
- Prior to installing the motherboard, please have it on top of an antistatic pad or within an electrostatic shielding container.
- Before unplugging the power supply cable from the motherboard, make sure the power supply has been turned off.
- Before turning on the power, make sure the power supply voltage has been set according to the local voltage standard.
- Before using the product, please verify that all cables and power connectors of your hardware components are connected.
- To prevent damage to the motherboard, do not allow screws to come in contact with the motherboard circuit or its components.
- Make sure there are no leftover screws or metal components placed on the motherboard or within the computer casing.
- Do not place the computer system on an uneven surface.
- Do not place the computer system in a high-temperature environment.
- Turning on the computer power during the installation process can lead to damage to system components as well as physical harm to the user.
- If you are uncertain about any installation steps or have a problem related to the use of the product, please consult a certified computer technician.

1-2 Product Specifications



NOTE:

We reserve the right to make any changes to the product specifications and product-related information without prior notice.

System	 ◆ 4U 				
Dimension	 448 x 176 x 880 (W x H x D, mm) 				
	5th Generation Intel® Xeon® Scalable Processors				
	4th Generation Intel® Xeon® Scalable Processors				
	Intel® Xeon® CPU Max Series				
	 Dual processor, CPU TDP up to 350W 				
	Note: If only 1 CPU is installed, some PCIe or memory functions might be unavailable				
Socket	• 2 x LGA4677				
	Socket E				
Chipset	Intel® C741 Express Chipset				
Security	UEFI Secure Boot				
	Silicon root of trust (Option)				
	SNMP Support: V3				
Memory	32 x DIMM slots				
	DDR5 memory supported only				
	8-Channel memory architecture				
	 RDIMM modules up to 96GB supported 				
	 3DS RDIMM modules up to 256GB supported 				
	 5th Gen Intel® Xeon®: Up to *5600MHz (1DPC), 4400MHz (2DPC) 4th Gen Intel® Xeon®: Up to 4800MHz (1DPC), 4400MHz (2DPC) 				
	 Intel® Xeon® Max Series: Up to 4800MHz (1DPC), 4400MHz (2DPC) Intel® Xeon® Max Series: Up to 4800MHz (1DPC), 4400MHz (2DPC) 				
	*5600MHz support under 2DPC configuration requires verified memory and BIOS setup. Please refer to the QVL for more information				
	Front Side:				
	 2 x 10Gb/s LAN ports (1 x Intel® X710-AT2) 				
	Support NCSI function				
	 1 x 10/100/1000 Mbps Management LAN 				
Video	 Integrated in Aspeed® AST2600 				
	2D Video Graphic Adapter with PCIe bus interface				
	 1920x1200@60Hz 32bpp, DDR4 SDRAM 				
Storage	Front Side:				
	 12 x 3.5"/2.5" Gen5 NVMe/SATA/SAS hot-swappable bays 				
	- (6 x NVMe from CPU_0, 6 x NVMe from CPU_1)				
	SAS card is required for SAS devices support				
Hardware Installation					

SAS	
	Depends on SAS add-on card
RAID	Intel® SATA RAID 0/1/10/5
	Support optional RAID add-in cards
Expansion Slot	• 4 x PCIe x16 (Gen5 x16) FHFL slots, from PEX89144_0, for GPUs
	 4 x PCIe x16 (Gen5 x16) FHFL slots, from PEX89144_1, for GPUs
	 4 x PCle x16 (Gen5 x16) low-profile slots, from PEX89144_0
	 4 x PCle x16 (Gen5 x16) low-profile slots, from PEX89144_1
	• 1 x PCIe x16 (Gen5 x16) low-profile slot on front side, from CPU_0
	 1 x PCIe x16 (Gen5 x16) low-profile slot on front side, from CPU_1
	- System is validated for population with a uniform GPU model
	- Support is not provided for mixed GPU populations
	1 x M.2 slot:
	- M-key - PCIe Gen3 x1, from PCH
	- Supports 2280/22110 cards
Internal I/O	1 x TPM header
	1 x VROC
Front I/O	• 2 x USB 3.2 Gen1
	◆ 1 x VGA
	• 2 x RJ45
	 1 x MLAN
	1 x Power button with LED
	1 x ID button with LED
	1 x NMI button
	 1 x Reset button 1 x Storage activity LED
	1 x System status LED
Rear I/O	
Trout 1/0	◆ N/A
Backplane I/O	• Speed and bandwidth: PCIe Gen5 x4 or SATA 6Gb/s or SAS 12Gb/s
TPM	1 x TPM header with SPI interface
	- Optional TPM2.0 kit: CTM010

Power Supply

3+1 3000W 80 PLUS Titanium redundant power supplies

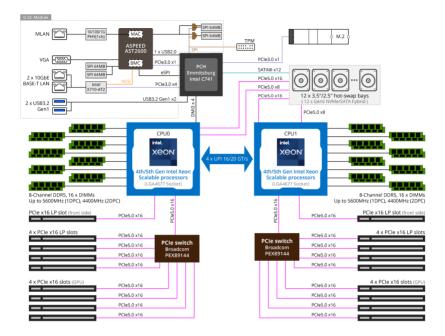
- AC Input:
 - 100-127V~/ 15.5A, 50-60Hz
 - 200-220V~/ 15.5A, 50-60Hz
 - 220-240V~/ 15.5A, 50-60Hz
- DC Input: - 240Vdc/ 15.5A
- DC Output:
 - Max 1000W/ 100-127V~
 - + 12.2V/ 81A
 - + 12Vsb/ 3A
 - Max 2600W/ 200-220V~
 - + 12.2V/ 213A
 - + 12Vsb/ 3A
 - Max 3000W/ 220-240V~
 - + 12.2V/ 240A
 - + 12Vsb/ 3A

NOTE:

- The system power supply requires C19 type power cord
- The power supply specifications provided herein is for the default server configuration. Different SKUs have different PSU specs, so please see the system rating label on the server for the accurate PSU specification.

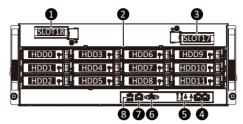
System	Aspeed® AST2600 management controller
Management	GIGABYTE Management Console (AMI MegaRAC SP-X) web interface
	Dashboard
	HTML5 KVM
	Sensor Monitor (Voltage, RPM, Temperature, CPU Statusetc.)
	Sensor Reading History Data
	FRU Information
	 SEL Log in Linear Storage / Circular Storage Policy
	Hardware Inventory
	Fan Profile
	System Firewall
	Power Consumption
	Power Control
	Advanced power capping
	LDAP / AD / RADIUS Support
	Backup & Restore Configuration
	Remote BIOS/BMC/CPLD Update
	Event Log Filter
	User Management
	Media Redirection Settings
	PAM Order Settings
	SSL Settings
	SMTP Settings
System Fans	• 12 x 60x60x38mm (23,000rpm)
Operating	Operating temperature: 10°C to 35°C
Properties	 Operating humidity: 8-80% (non-condensing)
	 Non-operating temperature: -40°C to 60°C
	 Non-operating humidity: 20%-95% (non-condensing)

1-3 System Block Diagram



Chapter 2 System Appearance

2-1 Front View



No. Description

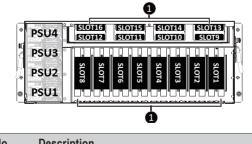
- 1. PCIe Slot
- 2. 3.5"/2.5" Drive Bays
- 3. PCIe Slot
- 4. 10GbE LAN Port x 2
- 5. Front Panel LEDs and Buttons
- 6. VGA Port
- 7. Server Management LAN Port
- 8. USB 3.2 Gen1 Port x 2

NOTE! Drives with green latches support NVMe.



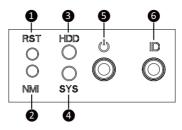
Go to the section 2-3 Front Panel Buttons and LEDs for detail description of function LEDs.

2-2 Rear View



No.	Description
1.	PCIe x16 Slot x16

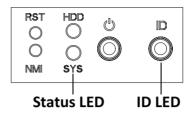
2-3 Front Panel LED and Buttons



No.	Name	Color	Status	Description
1.	Reset Button			Press the button to reset the system.
2.	NMI button			Press the button server generates a NMI to the processor if the multiple-bit ECC errors occur, which effectively halt the server.
		Croon	On	HDD locate
		Green	Blink	HDD access
3.	HDD Status	Amber	On	HDD fault
	LED	Green/ Amber	Blink	HDD rebuilding
		N/A	Off	No HDD access or no HDD fault.
		Green	Solid On	System is operating normally.
			Solid On	Critical condition, may indicate:
				System fan failure
				System temperature
	System	Amber	Blink	Non-critical condition, may indicate:
4.	Status			Redundant power module failure
	LED(Note)			Temperature and voltage issue
				Chassis intrusion
			Off	System is not ready, may indicate:
		N/A		POST error
				NMI error
				Processor or terminator missing
5.	Power button with LED	Green	On	System is powered on
5.		N/A	Off	System is not powered on or in ACPI S5 state (power off)
6.	ID Button ^(Note)			Press the button to activate system identification

(Note) If your server features RoT function, please see the following section for detail LED behavior.

2-3-1 RoT LEDs



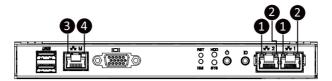
	LED on Front panel(Note5)	
	ID LED	Status LED
EC Firmware (FW) Authentication fail or not exit		
EC FW is broken or not exit (Note1)	OFF	OFF
Authenticating/Recovering BMC/BIOS Images		
Authenticating Images	OFF	OFF
Recovering BMC Active Flash	Blinks Blue 4 times per second	Blinks Green 4 times per second
Recovering BIOS Active Flash	Blinks Blue 4 times per second	Blinks Green 4 times per second
Authentication (AUTH) Pass		
Recovering BIOS Active Flash	OFF	OFF
BMC : AUTH pass after doing recovery BIOS : AUTH pass after doing recovery	OFF	OFF
BMC : AUTH pass after doing recovery BIOS : AUTH pass	OFF	OFF
BMC : AUTH pass BIOS : AUTH pass after doing recovery	OFF	OFF
Active Flash Authentication (AUTH) Fail		
BMC : AUTH Fail ^(Note2)	Blinks Blue 1 time per second	Blinks Green 1 time per second

BIOS : AUTH fail ^(Note2)	Blinks Blue 1 time per second	Blinks Amber 1 time per second	
BMC : AUTH fail after doing recovery ^(Note3)	Blinks Blue 2 times per second [ON OFF OFF]	Blinks Green 2 times per second [ON OFF OFF]	
BIOS : AUTH fail after doing recovery ^(Note3)	Blinks Blue 2 times per second [ON OFF OFF]	Blinks Amber 2 times per second [ON OFF OFF]	
Backup Flash Authentication Fail ^(Note4)			
BMC : AUTH fail	Blinks Blue 2 times per second [ON OFF ON OFF]	Blinks Green 2 times per second [ON OFF ON OFF]	
BIOS : AUTH fail	Blinks Blue 2 times per second [ON OFF ON OFF]	Blinks Amber 2 times per second [ON OFF ON OFF]	

NOTE!

- 1. EC FW is broken or not exited result in Microchip CEC1702 cannot load EC FW for authentication.
- 2 (1) Authentication fail include below scenarios Configuration table is missing or modified Public key is missing or modified Protected area or signature is modified Flash empty
- If active flash is still authentication failed after recovery sequence, Microchip CEC1702 stop the process and showing LED behavior.
- If backup flash authentication is failed cause by configuration table, public key or protected area is broken. Microchip CEC1702 stop the process and showing LED behavior.
- Front panel LED is controlled by BMC or Microchip CEC1702. Once Microchip CEC1702 is working(Auth or recovery), the front panel LED is controlled by Microchip CEC1702 and vice versa.

2-4 Front Panel System LAN LEDs



No.	Name	Color	Status	Description
	1. 10GbE Speed LED	Green	On	10 Gbps data rate
1.		Yellow	On	5Gbps, 2.5Gbps, 1Gbps data rate
	00000 222	N/A	Off	100 Mbps data rate
		Green	On	Link between system and network or no access
2.	2. 10GbE Link / Activity LED	Green	Blink	Data transmission or reception is occurring.
		N/A	Off	No data transmission or reception is occurring.
	3. 1GbE Speed LED	Yellow	On	1 Gbps data rate
3.		Green	On	100 Mbps data rate
		N/A	Off	10 Mbps data rate
		Green	On	Link between system and network or no access
4.	1GbE Link / Activity LED	Green	Blink	Data transmission or reception is occurring.
	,	N/A	Off	No data transmission or reception is occurring.

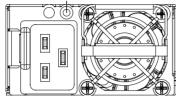
2-5 Power Supply Unit (PSU) LED



NOTE!

The power supply may be vary based on the system configuration.

PSU LED



State	Description
OFF	No AC power to all power supplies
1Hz Green Blinking	AC present / only standby on / Cold redundant mode
2Hz Green Blinking	Power supply firmware updating mode
Amber	AC cord unplugged or AC power lost; with a second power supply in parallel still with AC input power
Amber	Power supply critical event causing shut down: failure, OCP, OVP, fan failure and UVP
1Hz Amber Blinking	Power supply warning events where the power supply continues to operate: high temp, high power, high current and slow fan

2-6 Hard Disk Drive LEDs

RAID	SKU	LED1	Locate	HDD Fault	Rebuilding	HDD Access	HDD Present (No Access)
	Disk LED (LED on	Green	ON(*1)	OFF		BLINK (*2)	OFF
	Back Panel)	Amber	OFF	OFF		OFF	OFF
No RAID configuration (via PCH, HBA)	Removed HDD Slot	Green	ON(*1)	OFF			
	(LED on Back Panel)	Amber	OFF	OFF			
		Green	ON	OFF		BLINK (*2)	OFF
RAID configuration	Disk LED	Amber	OFF	ON	(Low Speed: 2 Hz)	OFF	OFF
(via HW RAID Card or SW RAID Card)	Demond UDD Clat	Green	ON(*1)	OFF	(*3)		
	Removed HDD Slot	Amber	OFF	ON	(*3)		

LED 2	HDD Present	No HDD
Green	ON	OFF

NOTE:

*1: Depends on HBA/Utility Spec.

*2: Blink cycle depends on HDD's activity signal.

*3: If HDD is pulled out during rebuilding, the disk status of this HDD is regarded as faulty.

Chapter 3 System Hardware Installation



Pre-installation Instructions

Computer components and electronic circuit boards can be damaged by electrostatic discharge. Working on computers that are still connected to a power supply can be extremely dangerous. Follow the simple guidelines below to avoid damage to your computer or injury to yourself.

- Always disconnect the computer from the power outlet whenever you are working inside the computer case.
- If possible, wear a grounded wrist strap when you are working inside the computer case. Alternatively, discharge any static electricity by touching the bare metal system of the computer case, or the bare metal body of any other grounded appliance.
- Hold electronic circuit boards by the edges only. Do not touch the components on the board unless it is necessary to do so. Do not flex or stress the circuit board.
- Leave all components inside the static-proof packaging until you are ready to use the component for the installation.

3-1 Removing and Installing the Chassis Top Cover

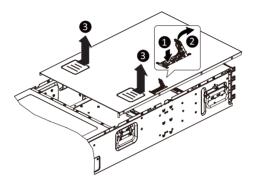


Before you remove or install the system cover

• Make sure the system is not turned on or connected to AC power.

Follow these instructions to remove/install the chassis top cover:

- 1. Push button to unlock the handle.
- 2. Pull the grip handle to open the panel cover.
- 3. Slide the cover towards the rear and remove the cover in the direction indicated.
- 4. Follow steps 1-3 in reverse order to re-install the top cover



3-2 Installing the GPU Card



Before you install/remove the GPU card:

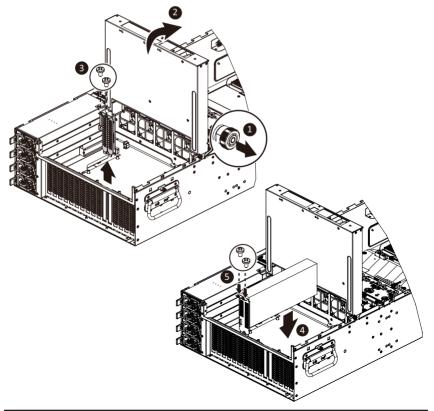
- Voltages can be present within the server whenever an AC power source is connected. This
 voltage is present even when the main power switch is in the off position. Ensure that the
 system is powered down and all power sources have been disconnected from the server prior
 to installing a GPU card. Make sure the system is not turned on or connected to AC power.
- · Failure to observe these warnings could result in personal injury or damage to the equipment.



• The GPU cards need to be purchased.

Follow these instructions to install the GPU card:

- 1. Pull out the thumbnail screw securing the GPU card cage in place.
- 2. Flip over the GPU card cage in the direction indicated.
- Remove the two screws securing the GPU card slot covers in place and remove the GPU card slot covers.
- 4. Insert the GPU card into the selected slot. Make sure the PCIe card is properly seated.
- 5. Install the two screws to secure the GPU card in place.
- 6. Reverse the previous steps to remove the GPU card.



3-3 Installing the PCI Expansion Card



Voltages can be present within the server whenever an AC power source is connected. This voltage is present even when the main power switch is in the off position. Ensure that the system is powered-down and all power sources have been disconnected from the server prior to installing a PCle card.

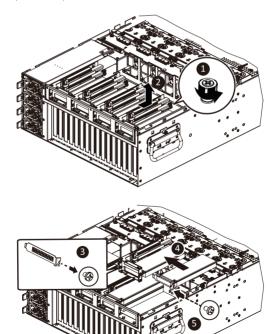
Failure to observe these warnings could result in personal injury or damage to equipment.

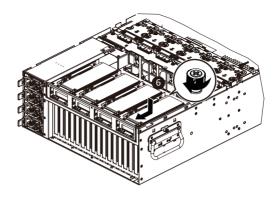


The PCIe riser assembly does not include a riser card or any cabling as standard. To install a PCIe card, a riser card must be installed.

Follow these instructions to install the PCIe card:

- 1. Loosen the thumbscrew securing the PCIe card bracket in place.
- 2. Lift the PCIe card bracket in the direction indicated.
- 3. Remove the screw securing the PCIe card slot cover and remove the PCIe slot cover.
- 4. Insert the PCIe card into the selected slot. Make sure the PCIe card is properly seated.
- 5. Install the screw to secure the PCIe card in place.
- 6. Tighten the thumbscrew to secure the PCIe card bracket in place.
- 7. Reverse the previous steps to remove the PCIe card.





3-4 Removing and Installing the Heat Sink



Read the following guidelines before you begin to remove/install the heat sink:

- Always turn off the computer and unplug the power cord from the power outlet before installing the heat sink to prevent hardware damage.
- Unplug all cables from the power outlets.
- · Disconnect all telecommunication cables from their ports.
- Place the system unit on a flat and stable surface.
- · Open the system according to the instructions.

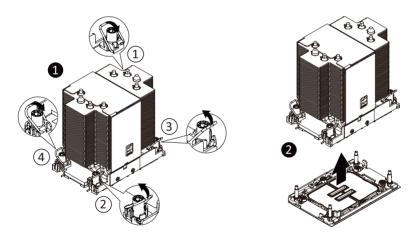


WARNING!

Failure to turn off the server before you start installing components may cause serious damage. Do not attempt the procedures described in the following sections unless you are a qualified service technician.

Follow these instructions to remove/install the heat sink:

- 1. Loosen the captive screws securing the heat sink in place in reverse order (4→3→2→1). Move the rotating wires into the unlatch position.
- 2. Lift and remove the heat sink from the system.
- 3. To reinstall the heat sink reverse steps 1-2 while ensuring that you tighten the captive screws in sequential order $(1\rightarrow 2\rightarrow 3\rightarrow 4)$.





- When installing the heat sink to CPU, use T30-Lobe driver to tighten 4 captive nuts in sequence as 1-4. The screw tightening torque: 8 ± 0.5 kgf-cm.
- To ensure the system operates properly, make sure the heat sink is seated on the processor firmly.

3-5 Installing the CPU



Read the following guidelines before you begin to install the CPU:

- •Make sure that the motherboard supports the CPU.
- Always turn off the computer and unplug the power cord from the power outlet before installing the CPU to prevent hardware damage.
- •Unplug all cables from the power outlets.
- •Disconnect all telecommunication cables from their ports.
- •Place the system unit on a flat and stable surface.
- •Open the system according to the instructions.



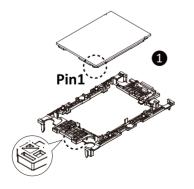
WARNING!

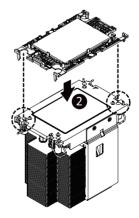
Failure to properly turn off the server before you start installing components may cause serious damage. Do not attempt the procedures described in the following sections unless you are a qualified service technician.

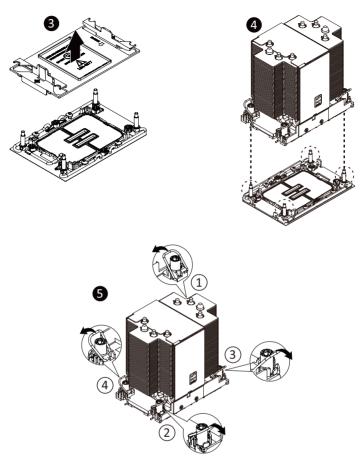
Follow these instructions to Install the CPU:

- Align and install the processor on the carrier. NOTE: Apply thermal compound evenly on the top of the CPU. Remove the protective cover from the underside of the heat sink.
- 2. Carefully flip the heat sink cover. Then install the carrier assembly on the bottom of the heat sink and make sure the gold arrow is located in the correct direction.
- Remove the CPU cover.
 NOTE: Save the CPU cover in the event that you need to remove the CPU from the socket.
- 4. Align the heat sink with the CPU socket by the guide pins and make sure the gold arrow is located in the correct direction. Then place the heat sink onto the top of the CPU socket.
- 5. Position the rotating wires into the latch position. Tighten the screws in sequential order $(1\rightarrow 2\rightarrow 3\rightarrow 4)$.

NOTE: When dissembling the heat sink, loosen the screws in reverse order $(4 \rightarrow 3 \rightarrow 2 \rightarrow 1)$ and then move the rotating wires into the unlatch position.







Carrier Types used for Package Types

Package Type	Xeon [®] SP XCC	Xeon [®] SP MCC	Xeon [®] SP+HBM
Carrier Code	E1A	E1B	E1C

NOTE!

- The carrier code is marked on each carrier and matches a code laser marked on to the IHS(Integrated Heat Spreader) to ensure the right parts are used together
- When installing the heat sink to CPU, use T30-Lobe driver to tighten 4 captive nuts in sequence as 1-4.
- The screw tightening torque: 8 ± 0.5 kgf-cm.

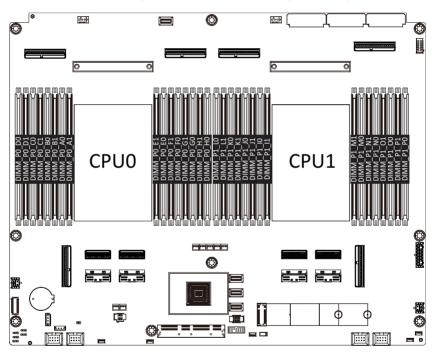
3-6 Installing the Memory

Read the following guidelines before you begin to install the memory:

- Make sure that the motherboard supports the memory. It is recommended that memory of the same capacity, brand, speed, and chips be used.
- Always turn off the computer and unplug the power cord from the power outlet before installing the memory to prevent hardware damage.
- Memory modules have a foolproof design. A memory module can be installed in only one direction. If you are unable to insert the memory, switch the direction.

3-6-1 Eight Channel Memory Configuration

This motherboard provides 32 DDR5 memory slots and supports 8-Channel Technology. After the memory is installed, the BIOS will automatically detect the specifications and capacity of the memory.

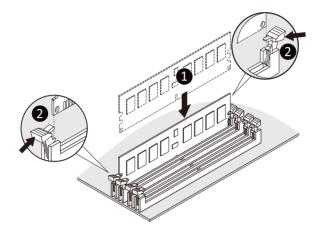


3-6-2 Installing the Memory

Before installing a memory module, make sure to turn off the computer and unplug the power cord from the power outlet to prevent damage to the memory module. Be sure to install DDR5 DIMMs on this motherboard.

Follow these instructions to install the Memory:

- 1 Insert the DIMM memory module vertically into the DIMM slot, and push it down.
- 2 Close the plastic clip at both edges of the DIMM slots to lock the DIMM module.
- 3 Reverse the installation steps when you want to remove the DIMM module.



3-6-3 Memory Population Table

4th Gen Intel Xeon Scalable Processors Memory Support

Туре	Ranks Per DIMM and	DIM	M Capacity	(GB)	Speed (MT/s); Voltage (V); DIMM per Channel (DPC)						
	Data Width				1DPC ¹	2DPC					
		16Gb	24Gb ²	36Gb	1.1	V					
	SRx8 (RC D)	16GB	24GB	NA							
	SRx4 (RC C)	32GB	48GB	NA]						
RDIMM	SRx4 (RC F) 9x4	32GB	NA	NA							
KDIIVIIVI	DRx8 (RC E)	32GB	48GB	NA	4800	4400					
	DRx4 (RC A)	64GB	96GB	128GB	4000	4400					
	DRx4 (RC B) 9x4	64GB	NA	NA]						
	(4R/8R)x4	2H-128GB	NA	NA							
RDIMM 3DS	(RC A)	4H-256GB	INA	NA							

NOTE

1. DPC applies to 1SPC or 2SPC implementations (SPC - Sockets Per Channel)
 2. 24Gb XCC only w/ limited configs: 1DPC all DIMM types, 2DPC 96GB only. Only 8 and 16 DIMM configs, no fallbacks.

5th Gen Intel Xeon Scalable Processors Memory Support

Туре	Ranks Per DIMM and Data Width	DIM	M Capacity	(GB)	Speed (MT/s); Voltage (V); DIMM per Channel (DPC) 1DPC ¹ 2DPC						
		16Gb	24Gb ²	36Gb	1.1	V					
	SRx8 (RC D)	16GB	24GB	NA							
	SRx4 (RC C)	32GB	48GB	NA							
RDIMM	SRx4 (RC F) 9x4	NA	NA	NA	5600 ³						
RDIWIW	DRx8 (RC E)	32GB	48GB	NA	0000	4400 ³					
	DRx4 (RC A)	64GB	96GB	128GB		4400*					
	DRx4 (RC B) 9x4	NA	NA	NA	1						
RDIMM 3DS	(4R/8R)x4	2H-128GB	NA	NA	5600 ⁴						
KDIIVIIVI 3D2	(RC A)	4H-256GB	INA	INA	5000						

NOTE:

NOTE: 1. 10PC applies to 1SPC or 2SPC implementations (SPC - Sockets Per Channel) 2. 24Gb 2DPC not POR w/ 24GB and 48GB DIMMs. 3. DDR5-5600 RDIMMs will be limited to 5600 MT/s 1DPC and 4400 MT/s 2DPC. DDR5-4800 DIMMs will be limited to 4800 MT/s 1DPC and 4400 MT/s 2DPC. 4. DDR5-5600 DIMMS are required for 5600 and 5200 1DPC speeds.

3-6-4 Processor and Memory Module Matrix Table

Memory Q'ty		CPU0																		CP	PU1											
for each CPU	D0	D1	C0	C1	B0	B1	A0	A1	E1	E0	F1	FO	G1	G0	H1	HO	LO	L1	ко	K1	J 0	J1	10	11	M1	MO	N1	NO	01	00	P1	PO
1 DIMM							v																v									
2 DIMM							v							v									v							v		
4 DIMM			v				v			v				v					v				v			v				v		
6 DIMM	v		v				v			v		v		v			v		v				v			v		v		v		
8 DIMM	v		v		v		v			v		v		v		v	v		v		v		v			v		v		v		v
12 DIMM	v		v	v	v		v	v	v	v		v	v	v		v	v		v	v	v		v	v	v	v		v	v	v		v
16 DIMM	v	v	v	v	v	v	v	v	v	v	v	v	v	v	v	v	v	v	v	v	v	v	v	v	v	v	v	v	v	v	v	v

NOTE!

There should be at least one DDR5 DIMM per socket. .

3-7 Installing the Hard Disk Drive

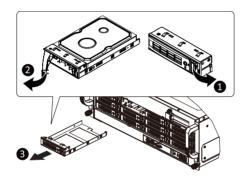


Read the following guidelines before you begin to install the hard disk drive:

- Take note of the drive tray orientation before sliding it out.
- The tray will not fit back into the bay if inserted incorrectly.
- Make sure that the hard disk drive is connected to the hard disk drive connector on the backplane.

Follow these instructions to install a 3.5" Hard Disk Drive:

- 1. Press the release button.
- 2. Extend the locking lever.
- 3. Pull the locking lever in the direction indicated to remove the HDD tray.
- 4. Align the hard disk drive with the positioning stud on the HDD tray.
- 5. Slide the hard disk drive into the HDD tray.
- 6. Reinsert the HDD tray into the slot and close the locking lever.

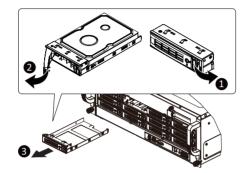


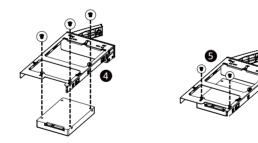




Follow these instructions to install a 2.5" hard disk drive into 3.5" HDD Tray:

- 1. Press the release button.
- 2. Extend the locking lever.
- 3. Pull the locking lever in the direction indicated to remove the HDD tray.
- 4. Align the hard disk drive with the positioning screw on the HDD tray.
- 5. Secure the hard disk drive with five screws.
- 6. Reinsert the HDD tray into the slot and close the locking lever.





3-8 Replacing the System Fan Module



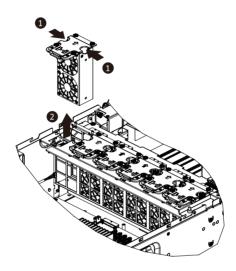
CAUTION!

Before you remove or install the system fans follow these steps:

- Make sure the system is not turned on or connected to AC power.
- Disconnect all necessary cable connections. Failure to observe these warnings could result in personal injury or damage to the equipment.

Follow these instructions to replace the system fan module:

- 1. Grasp the finger slots of the fan module and pull up to remove the fan module.
- 2. Reverse the previous steps to install the replacement fan module.



3-9 Removing and Installing the Power Supply

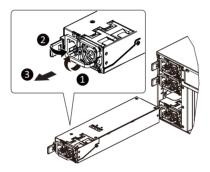


CAUTION!

- In order to reduce the risk of injury from electric shock, disconnect AC power from the power supply before removing the power supply from the system.
- Please see Section 2-2 "Rear View" for installation sequence.

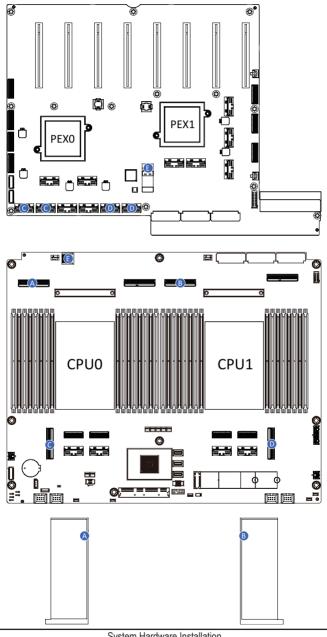
Follow these instructions to replace the power supply:

- 1. Flip and then grasp the power supply handle.
- 2. Press the retaining clip on the top side of the power supply in the direction indicated.
- 3. Pull out the power supply using the handle.
- 4. Insert the replacement power supply firmly into the chassis. Connect the AC power cord to the replacement power supply.



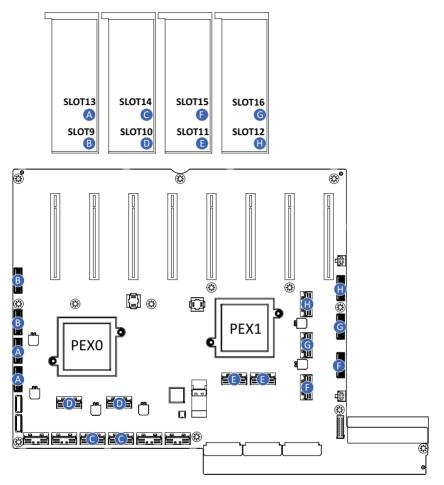
3-10 Cable Connection

3-10-1 Motherboard to PCIe Board and Front Side Low-Profile Card Cable



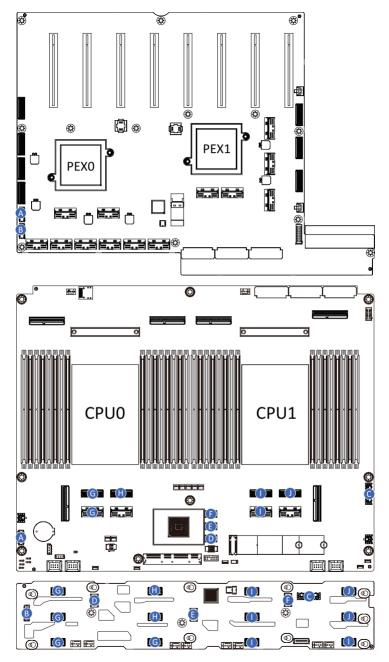
A	PCIe Slot Signal Cable	Motherboard: U2_P0_PE4
		Front Side: SLOT2
в	PCIe Slot Signal Cable	Motherboard: U2_P1_PE4
		Front Side: SLOT1
С	PCIe Slot Signal Cable	Motherboard: U2_P0_PE0
		PCIe Board: U2_1/ U2_2
D	PCIe Slot Signal Cable	Motherboard: U2_P1_PE2
		PCIe Board: U2_5/ U2_6
E	Power Board Side Band Signal Cable	Motherboard: PDB_IO
		PCIe Board: PWR_IO

3-10-2 PCIe Board to Rear Side Top GPU Card Cable

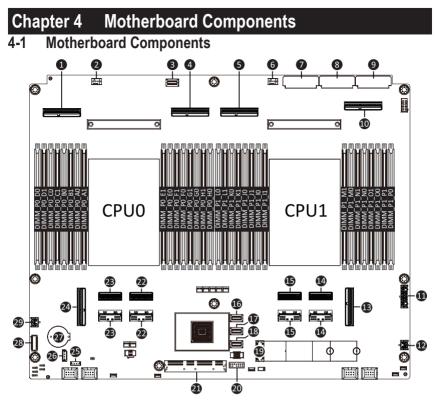


APCle Slot Signal CablePCle Board: MCIO1_1/ MCIO1_2BPCle Slot Signal CablePCle Board: MCIO2_1/ MCIO2_2BPCle Slot Signal CablePCle Board: MCIO3_1/ MCIO3_2CPCle Slot Signal CablePCle Board: MCIO3_1/ MCIO3_2DPCle Slot Signal CablePCle Board: MCIO4_1/ MCIO4_2DPCle Slot Signal CablePCle Board: MCIO5_1/ MCIO5_2EPCle Slot Signal CablePCle Board: MCIO5_1/ MCIO5_2FPCle Slot Signal CablePCle Board: MCIO6_1/ MCIO5_2FPCle Slot Signal CablePCle Board: MCIO6_1/ MCIO6_2FPCle Slot Signal CablePCle Board: MCIO6_1/ MCIO6_2GPCle Slot Signal CablePCle Board: MCIO7_1/ MCIO7_2GPCle Slot Signal CablePCle Board: MCIO7_1/ MCIO7_2HPCle Slot Signal CablePCle Board: MCIO8_1/ MCIO8_2Rear Side: SLOT16PCle Board: MCIO8_1/ MCIO8_2Rear Side: SLOT12PCle Board: MCIO8_1/ MCIO8_2			
Rear Side: SLOT13 B PCle Slot Signal Cable PCle Board: MCIO2_1/ MCIO2_2 Rear Side: SLOT9 PCle Board: MCIO3_1/ MCIO3_2 C PCle Slot Signal Cable PCle Board: MCIO3_1/ MCIO3_2 D PCle Slot Signal Cable PCle Board: MCIO4_1/ MCIO4_2 D PCle Slot Signal Cable PCle Board: MCIO5_1/ MCIO5_2 E PCle Slot Signal Cable PCle Board: MCIO5_1/ MCIO5_2 F PCle Slot Signal Cable PCle Board: MCIO6_1/ MCIO6_2 F PCle Slot Signal Cable PCle Board: MCIO6_1/ MCIO6_2 G PCle Slot Signal Cable PCle Board: MCIO7_1/ MCIO7_2 H PCle Slot Signal Cable PCle Board: MCIO8_1/ MCIO8_2	A	PCIe Slot Signal Cable	PCIe Board: MCIO1_1/ MCIO1_2
B PCle Slot Signal Cable Rear Side: SLOT9 C PCle Slot Signal Cable PCle Board: MCIO3_1/ MCIO3_2 D PCle Slot Signal Cable PCle Board: MCIO4_1/ MCIO4_2 D PCle Slot Signal Cable PCle Board: MCIO5_1/ MCIO5_2 E PCle Slot Signal Cable PCle Board: MCIO5_1/ MCIO5_2 F PCle Slot Signal Cable PCle Board: MCIO6_1/ MCIO6_2 F PCle Slot Signal Cable PCle Board: MCIO6_1/ MCIO6_2 G PCle Slot Signal Cable PCle Board: MCIO7_1/ MCIO7_2 H PCle Slot Signal Cable PCle Board: MCIO8_1/ MCIO8_2			Rear Side: SLOT13
Point Status Rear Side: SLOT9 Rear Side: SLOT9 PCle Board: MCIO3_1/ MCIO3_2 Rear Side: SLOT14 PCle Board: MCIO4_1/ MCIO4_2 D PCle Slot Signal Cable PCle Board: MCIO4_1/ MCIO4_2 E PCle Slot Signal Cable PCle Board: MCIO5_1/ MCIO5_2 F PCle Slot Signal Cable PCle Board: MCIO6_1/ MCIO5_2 F PCle Slot Signal Cable PCle Board: MCIO6_1/ MCIO6_2 G PCle Slot Signal Cable PCle Board: MCIO7_1/ MCIO7_2 H PCle Slot Signal Cable PCle Board: MCIO8_1/ MCIO8_2	В	PCIe Slot Signal Cable	PCIe Board: MCIO2_1/ MCIO2_2
C PCle Slot Signal Cable Rear Side: SLOT14 D PCle Slot Signal Cable PCle Board: MCIO4_1/ MCIO4_2 E PCle Slot Signal Cable PCle Board: MCIO5_1/ MCIO5_2 F PCle Slot Signal Cable PCle Board: MCIO6_1/ MCIO6_2 F PCle Slot Signal Cable PCle Board: MCIO6_1/ MCIO6_2 G PCle Slot Signal Cable PCle Board: MCIO7_1/ MCIO7_2 H PCle Slot Signal Cable PCle Board: MCIO8_1/ MCIO8_2			Rear Side: SLOT9
Rear Side: SLOT14 D PCle Slot Signal Cable PCle Board: MCIO4_1/ MCIO4_2 E PCle Slot Signal Cable PCle Board: MCIO5_1/ MCIO5_2 F PCle Slot Signal Cable PCle Board: MCIO6_1/ MCIO6_2 F PCle Slot Signal Cable PCle Board: MCIO6_1/ MCIO6_2 G PCle Slot Signal Cable PCle Board: MCIO7_1/ MCIO7_2 H PCle Slot Signal Cable PCle Board: MCIO8_1/ MCIO8_2		PCIe Slot Signal Cable	PCIe Board: MCIO3_1/ MCIO3_2
D PCle Slot Signal Cable Rear Side: SLOT10 E PCle Slot Signal Cable PCle Board: MCIO5_1/ MCIO5_2 F PCle Slot Signal Cable PCle Board: MCIO6_1/ MCIO6_2 G PCle Slot Signal Cable PCle Board: MCIO7_1/ MCIO7_2 G PCle Slot Signal Cable PCle Board: MCIO7_1/ MCIO7_2 H PCle Slot Signal Cable PCle Board: MCIO8_1/ MCIO8_2			Rear Side: SLOT14
Rear Side: SLOT10 E PCle Slot Signal Cable PCle Board: MCIO5_1/ MCIO5_2 F PCle Slot Signal Cable PCle Board: MCIO6_1/ MCIO6_2 G PCle Slot Signal Cable PCle Board: MCIO7_1/ MCIO7_2 G PCle Slot Signal Cable PCle Board: MCIO7_1/ MCIO7_2 H PCle Slot Signal Cable PCle Board: MCIO8_1/ MCIO8_2		PCIe Slot Signal Cable	PCIe Board: MCIO4_1/ MCIO4_2
E PCIe Slot Signal Cable Rear Side: SLOT11 F PCIe Slot Signal Cable PCIe Board: MCIO6_1/ MCIO6_2 G PCIe Slot Signal Cable PCIe Board: MCIO7_1/ MCIO7_2 G PCIe Slot Signal Cable PCIe Board: MCIO7_1/ MCIO7_2 H PCIe Slot Signal Cable PCIe Board: MCIO8_1/ MCIO8_2			Rear Side: SLOT10
F PCle Slot Signal Cable Rear Side: SLOT11 G PCle Slot Signal Cable PCle Board: MCIO6_1/ MCIO6_2 Rear Side: SLOT15 PCle Board: MCIO7_1/ MCIO7_2 Rear Side: SLOT16 PCle Board: MCIO8_1/ MCIO8_2	_	PCle Slot Signal Cable	PCIe Board: MCIO5_1/ MCIO5_2
F PCle Slot Signal Cable Rear Side: SLOT15 G PCle Slot Signal Cable PCle Board: MCIO7_1/ MCIO7_2 H PCle Slot Signal Cable PCle Board: MCIO8_1/ MCIO8_2			Rear Side: SLOT11
For Store Signal Cable Rear Side: SLOT15 G PCIe Slot Signal Cable PCIe Board: MCIO7_1/ MCIO7_2 H PCIe Slot Signal Cable PCIe Board: MCIO8_1/ MCIO8_2	E	PCIe Slot Signal Cable	PCIe Board: MCIO6_1/ MCIO6_2
G PCIe Slot Signal Cable Rear Side: SLOT16 H PCIe Slot Signal Cable PCIe Board: MCIO8_1/ MCIO8_2			Rear Side: SLOT15
H PCle Slot Signal Cable Rear Side: SLOT16 PCle Board: MCIO8_1/ MCIO8_2	6	PCIe Slot Signal Cable	PCIe Board: MCIO7_1/ MCIO7_2
H PCIe Slot Signal Cable	G		Rear Side: SLOT16
	L	PCIe Slot Signal Cable	PCIe Board: MCIO8_1/ MCIO8_2
			Rear Side: SLOT12





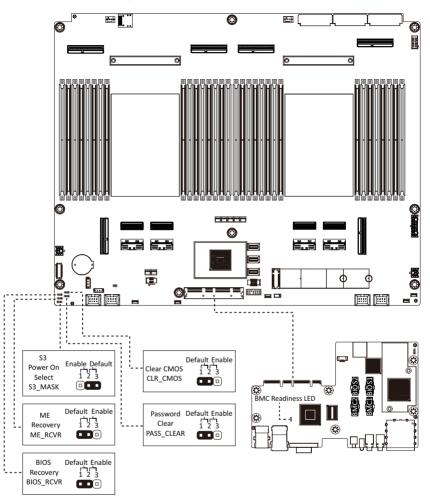
•	Backplane Board Signal	Motherboard: BP_1
A	Cable	PCle Board: BP_1
В	Backplane Board Signal	PCIe Board: BP_SERIES1
	Cable	Backplane Board: BP_1
С	Backplane Board Power Cable	Motherboard Board: BPB_PWR
		Backplane Board: ATX1
D	SATA Cable	Motherboard: SL_CN1
		Backplane Board: SL_SAS0
F	SATA Cable	Motherboard: SL_CN2
		Backplane Board: SL_SAS1
F	SATA Cable	Motherboard: SL_CN3
		Backplane Board: SL_SAS2
G	NVMe Cable	Motherboard: U2_P0_PE1A/ U2_P0_PE1B
		Backplane Board: U.2 0/ U.2 1/ U.2 2/ U.2 3
н	NVMe Cable	Motherboard: U2_P0_PE2A
		Backplane Board: U.2 4/ U.2 5
	NVMe Cable	Motherboard: U2_P1_PE0A/ U2_P1_PE0B
		Backplane Board: U.2 6/ U.2 7/ U.2 8/ U.2 9
J	NVMe Cable	PCle Board: U2_P1_PE1B
J		Backplane Board: U.2 10/ U.2 11



Item	Description
1	MCIO Connector (U2_P0_PE4/PCIe Gen5)
2	CPU0 Fan Connector (for CPU0 Heatsink)
3	SlimLine Connector (for Power Board Side Band Signal)
4	MCIO Connector (U2_P0_PE3/PCIe Gen5)
5	MCIO Connector (U2_P1_PE4/PCIe Gen5)
6	CPU1 Fan Connector (for CPU1 Heatsink)
7	System Power Connector (PWR1)
8	System Power Connector (PWR2)
9	System Power Connector (PWR3)
10	MCIO Connector (U2_P1_PE3/PCIe Gen5)
11	2 x 7 Pin HDD Backplane Board Power Connector
12	2 x 2 Pin PCIe Power Connector
13	MCIO Connector (U2_P1_PE2/PCIe Gen5)
14	MCIO Connector (U2_P1_PE1B/U2_P1_PE1A/PCIe Gen5)
15	MCIO Connector (U2_P1_PE0B/U2_P1_PE0A/PCIe Gen5)
16	SlimLine Connector (sSATA #0 - #3)
17	SlimLine Connector (SATA #4 - #7)
18	SlimLine Connector (SATA #0 - #3)
19	M.2 Slot (PCIe Gen3 x1, Support NGFF-22110)

Item	Description
20	TPM Module Connector
21	G-SC Module Connector
22	MCIO Connector (U2_P0_PE2A/U2_P0_PE2B/PCIe Gen5)
23	MCIO Connector (U2_P0_PE1A/U2_P0_PE1B/PCIe Gen5)
24	MCIO Connector (U2_P0_PE0/PCIe Gen5)
25	VROC Module Connector
26	IPMB Connector
27	Battery Socket
28	HDD Backplane Board Connector
29	2 x 2 Pin PCIe Power Connector

4-2 Jumper Setting

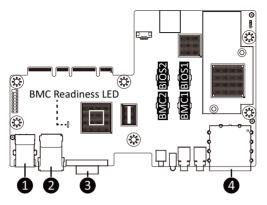


NOTE!

- ME_RCVR: Setting "ME_RCVR" to [2-3] will force the system's Management Engine into recovery
 mode. This allows the user to update the BIOS image, including the Management Engine region. Note
 that this jumper does not need to be set when using the Gigabyte BIOS update utility and SOP.
- S3 Mask: Reserve for development only.
- BIOS Recovery please refer to page 109.
- CLR_CMOS used to reset the BIOS settings of a computer to their default values.
- **PASS_CLEAR** used to clear or reset the BIOS password, which can be necessary if you've forgotten the password and need to regain access to the system.

4-3 G-SC Module

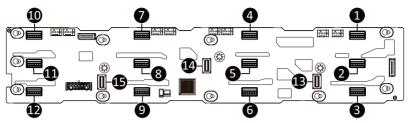
4-3-1 CDCG120



Item	Description
1	USB 3.2 Gen1 Port x 2
2	10/100/1000 Server Management LAN Port
3	VGA Port
4	10GbE LAN Port x 2

4-4 Backplane Board Storage Connector

4-4-1 CBP20C7



Item	Description
1.	MCIO 4i (SFF-TA-1016 / U.2_0)
2.	MCIO 4i (SFF-TA-1016 / U.2_1)
3.	MCIO 4i (SFF-TA-1016 / U.2_2)
4.	MCIO 4i (SFF-TA-1016 / U.2_3)
5.	MCIO 4i (SFF-TA-1016 / U.2_4)
6	MCIO 4i (SFF-TA-1016 / U.2_5)
7.	MCIO 4i (SFF-TA-1016 / U.2_6)
8.	MCIO 4i (SFF-TA-1016 / U.2_7)
9.	MCIO 4i (SFF-TA-1016 / U.2_8)
10.	MCIO 4i (SFF-TA-1016 / U.2_9)
11.	MCIO 4i (SFF-TA-1016 / U.2_10)
12.	MCIO 4i (SFF-TA-1016 / U.2_11)
13.	SlimSAS 4i Connector (SFF-8654 / SL_SAS0)
14.	SlimSAS 4i Connector (SFF-8654 / SL_SAS1)
15.	SlimSAS 4i Connector (SFF-8654 / SL_SAS2)

Chapter 5 BIOS Setup

BIOS (Basic Input and Output System) records hardware parameters of the system in the EFI on the motherboard. Its major functions include conducting the Power-On Self-Test (POST) during system startup, saving system parameters, loading the operating system etc. The BIOS includes a BIOS Setup program that allows the user to modify basic system configuration settings or to activate certain system features. When the power is turned off, the battery on the motherboard supplies the necessary power to the CMOS to keep the configuration values in the CMOS.

To access the BIOS Setup program, press the key during the POST when the power is turned on.



BIOS flashing is potentially risky, if you do not encounter any problems when using the current BIOS version, it is recommended that you don't flash the BIOS. To flash the BIOS, do it with caution. Inadequate BIOS flashing may result in system malfunction.

 It is recommended that you not alter the default settings (unless you need to) to prevent system instability or other unexpected results. Inadequately altering the settings may result in system's failure to boot. If this occurs, try to clear the CMOS values and reset the board to default values. (Refer to the Exit section in this chapter or introductions of the battery/clearing CMOS jumper in Chapter 4 for how to clear the CMOS values.)

BIOS Setup Program Function Keys

	•
<←><→>	Move the selection bar to select the screen
<↑><↓>	Move the selection bar to select an item
<+>	Increase the numeric value or make changes
<->	Decrease the numeric value or make changes
<enter></enter>	Execute command or enter the submenu
<esc></esc>	Main Menu: Exit the BIOS Setup program
	Submenus: Exit current submenu
<f1></f1>	Show descriptions of general help
<f3></f3>	Restore the previous BIOS settings for the current submenus
<f9></f9>	Load the Optimized BIOS default settings for the current submenus
<f10></f10>	Save all the changes and exit the BIOS Setup program

Main

This setup page includes all the items of the standard compatible BIOS.

Advanced

This setup page includes all the items of AMI BIOS special enhanced features.

(ex: Auto detect fan and temperature status, automatically configure hard disk parameters.)

Chipset

This setup page includes all the submenu options for configuring the functions of the Platform Controller Hub.

Server Management

Server additional features enabled/disabled setup menus.

Security

Change, set, or disable supervisor and user password. Configuration supervisor password allows you to restrict access to the system and BIOS Setup.

A supervisor password allows you to make changes in BIOS Setup.

A user password only allows you to view the BIOS settings but not to make changes.

Boot

This setup page provides items for configuration of the boot sequence.

Save & Exit

Save all the changes made in the BIOS Setup program to the CMOS and exit BIOS Setup. (Pressing <F10> can also carry out this task.)

Abandon all changes and the previous settings remain in effect. Pressing <Y> to the confirmation message will exit BIOS Setup. (Pressing <Esc> can also carry out this task.)

5-1 The Main Menu

Once you enter the BIOS Setup program, the Main Menu (as shown below) appears on the screen. Use arrow keys to move among the items and press <Enter> to accept or enter other sub-menu.

Main Menu Help

The on-screen description of a highlighted setup option is displayed on the bottom line of the Main Menu.

Submenu Help

While in a submenu, press <F1> to display a help screen (General Help) of function keys available for the menu. Press <Esc> to exit the help screen. Help for each item is in the Item Help block on the right side of the submenu.



When the system is not stable as usual, select the **Restore Defaults** item to set your system to its defaults.

The BIOS Setup menus described in this chapter are for reference only and may differ by BIOS version.

Main Advanced Chipset S	Aptio Setup – AMI erver Mgmt Security Boot Save & Ex:	it
BIOS Information		*
Project Name	MSB3-G40-000	
Project Version	D26	
Build Date and Time	11/28/2022 19:39:01	
BMC Information		
BMC Firmware Version	13.04.11	
Processor Information		
CPU 0 Brand String	Intel(R) Xeon(R) Platinum 8468V	
CPU 1 Brand String	Intel(R) Xeon(R) Platinum 8468V	
Max CPU Speed	2400 MHz	↔: Select Screen
CPU Signature	806F8	↑↓: Select Item
Processor Core	96	K/M: Scroll Help Area
Microcode Patch	2B0000C0	Up/Down.
		Enter: Select
Platform Information		+/-: Change Opt.
Processor	SPR E5	F1: General Help
PCH	EBG - B1	F3: Previous Values
RC Revision	91.D05	F9: Optimized Defaults
		F10: Save & Exit
Memory Information		ESC: Exit
Total Memory	1048576 MB	· · ·
	Version 2.22.1287 Copyright (C) 2022 6	AMI B4
	10/010101/ E-EE-120/ 00pgi 18/10 (0/ 2022)	64

Main Advanced Chipset Serv	er Mgmt Security Boot Save & Ex	it
CPU 0 Brand String	Intel(R) Xeon(R) Platinum 8468V	▲ Set the Time. Use Tab to switch between Time
CPU 1 Brand String	Intel(R) Xeon(R) Platinum 8468V	elements.
Max CPU Speed	2400 MHz	
CPU Signature	806F8	
Processor Core	96	
Microcode Patch	28000000	
Platform Information		
Processor	SPR E5	
PCH	EBG - B1	
RC Revision	91.D05	
		→+: Select Screen
Memory Information		↑↓: Select Item
Total Memory	1048576 MB	K/M: Scroll Help Area
Usable Memory	1048576 MB	Up/Down.
Memory Frequency	4800 MHz	Enter: Select
		+/-: Change Opt.
Onboard LAN Information		F1: General Help
LAN1 MAC Address	00-00-00-01-00	F3: Previous Values
LAN2 MAC Address	00-00-00-01-01	F9: Optimized Defaults
		F10: Save & Exit
System Date	[Mon 09/07/2922]	ESC: Exit
System Time	[21:57:32]	-

Parameter	Description	
BIOS Information		
Project Name	Displays the project name information.	
Project Version	Displays version number of the BIOS setup utility.	
Build Date and Time	Displays the date and time when the BIOS setup utility was created.	
BMC Information ^(Note1)		
BMC Firmware Version ^(Note1)	Displays BMC firmware version information.	
Processor Information		
CPU Brand String/ Max CPU Speed / CPU Signature / Processor Core / Microcode Patch	Displays the technical information for the installed processor(s).	
Platform Information		
Processor/ PCH/ RC Revision	Displays the information of the installed processor(s) and PCH.	
Memory Information ^(Note2)		
Total Memory	Displays the total memory size of the installed memory.	
Usable Memory	Displays the usable memory size of the installed memory.	

(Note1) Functions available on selected models.

(Note2) This section will display capacity and frequency information of the memory that the customer has installed.

Parameter	Description
Memory Frequency	Displays the frequency information of the installed memory.
Onboard LAN Information(Note3)	
LAN# MAC Address	Displays LAN MAC address information.
System Date	Sets the date following the weekday-month-day-year format.
System Time	Sets the system time following the hour-minute-second format.

(Note3) The number of LAN ports listed will depend on the motherboard / system model.

5-2 Advanced Menu

The Advanced Menu displays submenu options for configuring the function of various hardware components. Select a submenu item, then press <Enter> to access the related submenu screen.

Main Advanced Chipset Server Mgmt	Aptio Setup – AMI Security Boot S	
 Trusted Computing Serial Port Console Redirection SID Configuration PCI Subsystem Settings USB Configuration Network Stack Configuration NVME Configuration Chipset Configuration Tis Auth Configuration Intel(R) Ethernet Controller X710 for 00:00:00:00:01:00 VLAN Configuration (MAC:00000000100) Intel(R) Ethernet Controller X710 for 00:00:00:00:01:01 VLAN Configuration (MAC:000000000101) Driver Health 		Trusted Computing Settings ++: Select Screen 1: Select Item K/M: Scroll Help Area Up/Doun. Enter: Select +/-: Change Opt. F1: General Help F3: Previous Values F9: Optimized Defaults F10: Save & Exit ESC: Exit
Version 2.2	22.1287 Copyright ((C) 2022 AMI B

5-2-1 Trusted Computing

Advanced	Aptio Setup – AMI	
Configuration TPM v1.2 Support NO Security Device Found	(Enable)	Enables or Disables BIOS support for security device. 0.8. will not sho Security Device. TGG EFI protocol and INTIA interface will not be available.
		++: Select Screen 11: Select Item K/M: Scroll Help Area Up/Down. Enter: Select 4/-: Change Opt. F1: General Help F3: Previous Values F9: Optimized Defaults F10: Save & Exit ESC: Exit
Versl	lon 2.22.1287 Copyright (C) 20	022 AMI

Parameter	Description
Configuration	
TPM v1.2 Support	Enable/Disable BIOS support for security device. OS will not show security device. TCG EFI protocol and INT1A interface will not be available. Options available: Disable, Enable. Default setting is Enable .

5-2-2 Serial Port Console Redirection

Advanced	Aptio Setup — AMI	
COM1 Console Redirection Serial Port for Out-of-Band Managem Windows Emergency Management Servic Console Redirection EMS ► Console Redirection Settings		Console Redirection Enable or Disable.
		<pre>++: Select Screen 11: Select Item K/M: Scroll Help Area Up/Down. Enter: Select +/-: Change Opt. F1: General Help F3: Previous Values F3: Optimized Defaults F10: Save & Exit ESC: Exit</pre>
Version	2.22.1287 Copyright (C) 2022 (AMI

Parameter	Description
COM1 Console Redirection ^(Note)	Console redirection enables the users to manage the system from a remote location. Options available: Enabled, Disabled. Default setting is Disabled .
COM1 Console Redirection Settings	 Press [Enter] to configure advanced items. Please note that this item is configurable when COM1 Console Redirection is set to Enabled. Terminal Type Selects a terminal type to be used for console redirection. Options available: VT100, VT100PLUS, VT-UTF8, ANSI. Default setting is VT100PLUS. Bits per second Selects the transfer rate for console redirection. Options available: 9600, 19200, 38400, 57600, 115200. Default setting is 115200. Data Bits Selects the number of data bits used for console redirection.
	 Options available: 7, 8. Default setting is 8.

(Note) Advanced items prompt when this item is defined.

Parameter	Description
COM1 Console Redirection Settings (continued)	 Parity A parity bit can be sent with the data bits to detect some transmission errors. Even: parity bit is 0 if hum of 1's in the data bits is even. Odd: parity bit is 0 if num of 1's in the data bits is odd. Mark: parity bit is always 1. Space: Parity bit is always 0. Mark and Space Parity do not allow for error detection. Options available: None, Even, Odd, Mark, Space. Default setting is None. Stop Bits Stop bits indicate the end of a serial data packet. (A start bit indicates the beginning). The standard setting is 1 stop bit. Communication with slow devices may require more than 1 stop bit. Options available: 1, 2. Default setting is 1. Flow Control Flow Control can prevent data loss from buffer overflow. When sending data, if the receiving buffers are full, a 'stop' signal can be sent to re-start the flow. Hardware flow control uses two wires to send start/stop signals. Options available: None, Hardware RTS/CTS. Default setting is None. VT-UTF8 Combo Key Support Enable/Disable the VT-UTF8 Combo Key Support. Options available: Enabled, Disabled. Default setting is Disabled. Recorder Mode When this mode enabled, only texts will be send. This is to capture Terminal data. Options available: Enabled, Disabled. Default setting is Disabled. Resolution 100x31 Enable/Disable extended terminal resolution. Options available: Enabled, Disabled. Default setting is Enabled. Putty KeyPad

Parameter	Description
Serial Port for Out-of-Band Management / Windows Emergency Management Services (EMS) Console Redirection ^(Note)	EMS console redirection allows the user to configure Console Redirection Settings to support Out-of-Band Serial Port management. Options available: Enabled, Disabled. Default setting is Disabled .
Serial Port for Out-of-Band EMS Console Redirection Settings	 Press [Enter] to configure advanced items. Please note that this item is configurable when Serial Port for Out-of-Band Management EMS Console Redirection is set to Enabled. Out-of-Band Mgmt Port Microsoft Windows Emergency Management Service (EMS) allows for remote management of a Windows Server OS through a serial port. Default setting is COM1. Terminal Type EMS Selects a terminal type to be used for console redirection. Options available: VT100, VT100PLUS, VT-UTF8, ANSI. Default setting is VT10PLUS. Bits per second EMS Selects the transfer rate for console redirection. Options available: 9600, 19200, 57600, 115200. Default setting is 115200. Flow Control EMS Flow control EMS Flow control can prevent data loss from buffer overflow. When sending data, if the receiving buffers are full, a 'stop' signal can be sent to stop the data flow. Once the buffers are empty, a 'start' signal can be sent to re-start the flow. Hardware flow control uses two wires to send start/stop signals. Options available: None, Hardware RTS/CTS, Software Xon/Xoff. Default setting is None.

5-2-3 SIO Configuration

Aptio Setup - AMI Advanced	
AMI SID Driver Version : A5.18.00 Super IO Chip Logical Device(s) Configuration > [*Active*] Serial Port WARNING: Logical Devices state on the left side of the control, reflects the current Logical Device state. Changes made during Setup Session will be shown after you restart the system.	View and Set Basic properties of the SIO Logical device. Like IO Base, IRQ Range, DNA Channel and Device Mode.
	<pre>++: Select Screen 11: Select Item K/M: Scroll Help Area Up/Down. Enter: Select +/-: Change Opt. F1: General Help F3: Previous Values F9: Optimized Defaults F10: Save & Exit ESC: Exit</pre>
Version 2.22.1287 Copyright (C) 2022 AM	I

Parameter	Description
AMI SIO Driver Version	Displays the AMI SIO driver version information.
Super IO Chip Logical Device(s) Configuration	Press [Enter] to configure advanced items.
[*Active*] Serial Port	 Use This Device When set to Enabled allows you to configure the serial port settings. When set to Disabled, displays no configuration for the serial port. Options available: Enabled, Disabled. Default setting is Enabled. Logical Device Settings/Current: Displays the serial port base I/O address and IRQ. Possible: Configures the serial port base I/O address and IRQ. Use Automatic Settings

5-2-4 PCI Subsystem Settings

Advanced	Aptio Setup – AMI	
PCI Bus Driver Version SLOT3 I/O ROM	A5.01.29 [Enabled]	▲ Enable/Disable SLOT3 I/O ROM
GPUO I/O ROM	[Enabled]	
NVSW I/O ROM	[Enabled]	
SLOT2 I/O ROM	[Enabled]	
GPU1 I/O ROM	[Enabled]	
SLOT4 I/O ROM	[Enabled]	
GPU2 I/O ROM	[Enabled]	++: Select Screen 14: Select Item
SLOT1 I/O ROM	[Enabled]	K/M: Scroll Help Area
GPU3 I/O ROM	[Enabled]	Enter: Select +/-: Change Opt.
SLOT9 I/O ROM	[Enabled]	F1: General Help
SLOT9 Lanes	[Auto]	F3: Previous Values
SLOT9 Max Link Speed	[Auto]	F9: Optimized Defaults
		F10: Save & Exit
SLOT10 I/O ROM	[Enabled]	ESC: Exit
SLOT10 Lanes	[Auto]	
Ver	esion 2 22 1287 Conuright (C	1 2022 AMT B4

Advanced	Aptio Setup – AMI	
SLOT6 I/O ROM	[Enabled]	 If system has SR-IOV capable PCIe Devices, this option Enables or Disables
GPU6 I/O ROM	[Enabled]	Single Root IO Virtualization Support.
SLOT5 I/O ROM	[Enabled]	
GPU7 I/O ROM	[Enabled]	
SLOT12 I/O ROM	[Enabled]	
SLOT12 Lanes	[Auto]	
SLOT12 Max Link Speed	[Auto]	
SLOT13 I/O ROM	[Enabled]	++: Select Screen
SLOT13 Lanes	[Auto]	t↓: Select Item
SLOT13 Max Link Speed	[Auto]	K/M: Scroll Help Area Up/Down.
Onboard LAN1 I/O ROM	[Enabled]	Enter: Select
Onboard LAN2 I/O ROM	[Enabled]	+/-: Change Opt.
		F1: General Help
PCI Devices Common Settings:	(The state of the	F3: Previous Values
Above 4G Decoding	[Enabled] [Enabled]	F9: Optimized Defaults F10: Save & Exit
SR-IOV Support		ESC: Exit
		V LATC
Vens	sion 2.22.1287 Copyright (C)	2022 AMI 84

Parameter	Description
PCI Bus Driver Version	Displays the PCI Bus Driver version information.
SLOT#/GPU# I/O ROM ^(Note1)	When enabled, this setting will initialize the device expansion ROM for the related PCI-E slot. Options available: Enabled, Disabled. Default setting is Enabled .
SLOT# Lanes ^(Note1)	Change the PCIe lanes. Default setting is Auto.
SLOT# Max Link Speed ^(Note1)	Configure PCIe max link speed. Options available: Auto, Gen1, Gen2, Gen3, Gen4, Gen5. Default setting is Auto .
Onboard LAN1/ LAN2 I/O Controller ^(Note2)	Enable/Disable the onboard LAN controller. Options available: Enabled, Disabled. Default setting is Enabled .
Onboard LAN1/ LAN2 I/O ROM ^(Note2)	Enable/Disable the onboard LAN devices, and initializes device expansion ROM. Options available: Enabled, Disabled. Default setting is Enabled .
PCI Devices Common Settings	
Above 4G Decoding	Enable/Disable memory mapped I/O to 4GB or greater address space (Above 4G Decoding). Options available: Enabled, Disabled. Default setting is Enabled .
SR-IOV Support	If the system has SR-IOV capable PCIe devices, this item Enable/Disable Single Root IO Virtualization Support. Options available: Enabled, Disabled. Default setting is Enabled .

(Note2) This section is dependent on the available LAN controller.

5-2-5 USB Configuration

Advanced	Aptio Setup – AMI	
USB Configuration		This is a workaround for OSes without XHCI hand-off
USB Devices:		support. The XHCI
8 Drives, 2 Keyboards, 1 Mouse	, 3 Hubs	ownership change should be claimed by XHCI driver.
XHCI Hand–off		
USB Mass Storage Driver Support	[Enabled]	
Port 60/64 Emulation	[Enabled]	
		++: Select Screen 14: Select Item K/H: Scroll Help Area Up/Doun. Enter: Select +/-: Change Opt. F1: General Help F3: Previous Values F9: Optimized Defaults F9: Optimized Defaults ESC: Exit
Version 2	.22.1287 Copyright (C) 2022 AMI	

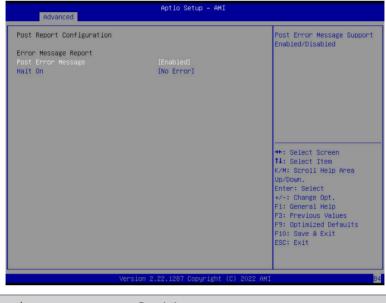
Parameter	Description
USB Configuration	
USB Devices:	Displays the USB devices connected to the system.
XHCI Hand-off	Enable/Disable the XHCI (USB 3.0) Hand-off support. Options available: Enabled, Disabled. Default setting is Enabled .
USB Mass Storage Driver Support ^(Note)	Enable/Disable the USB Mass Storage Driver Support. Options available: Enabled, Disabled. Default setting is Enabled .
Port 60/64 Emulation	Enables the I/O port 60h/64h emulation support. This should be enabled for the complete USB Keyboard Legacy support for non- USB aware OSes. Options available: Enabled, Disabled. Default setting is Enabled .

5-2-6 Network Stack Configuration

Advanced	Aptio Setup – AMI	
Network Stack IPv4 PXE Support IPv4 HTTP Support IPv6 FXE Support IPv6 HTTP Support PXE boot wait time Media detect count	[Enabled] [Enabled] [Disabled] [Disabled] [Disabled] O 1	Enable/Disable UEFI Network Stack
		++: Select Screen f1: Select Item K/H: Scroll Help Area Up/Down. Enter: Select +/-: Change Opt. F1: General Help F3: Previous Values F9: Optimized Defaults F10: Save & Exit ESC: Exit

Parameter	Description
Network Stack	Enable/Disable the UEFI network stack. Options available: Enabled, Disabled. Default setting is Enabled .
Ipv4 PXE Support	Enable/Disable the Ipv4 PXE feature. Options available: Enabled, Disabled. Default setting is Enabled .
Ipv4 HTTP Support	Enable/Disable the Ipv4 HTTP feature. Options available: Enabled, Disabled. Default setting is Disabled .
Ipv6 PXE Support	Enable/Disable the Ipv6 PXE feature. Options available: Enabled, Disabled. Default setting is Disabled .
Ipv6 HTTP Support	Enable/Disable the Ipv6 HTTP feature. Options available: Enabled, Disabled. Default setting is Disabled .
PXE boot wait time	Wait time in seconds to press ESC key to abort the PXE boot. Press the <+> / <-> keys to increase or decrease the desired values.
Media detect count	Number of times the presence of media will be checked. Press the <+> / <-> keys to increase or decrease the desired values.

5-2-7 Post Report Configuration



Parameter	Description
Post Report Configuration	
Error Message Report	
Post Error Message	Enable/Disable the POST Error Message support. Options available: Enabled, Disabled. Default setting is Enabled .
Halt On	Options available: No Error, All Error. Default setting is No Error.

5-2-8 NVMe Configuration

NVMe Configuration	BIOS Build-In is default
NVME OPROM Select No NVME Device Found	setting. Select Device Itself, then this NVMe page will not display any NVMe device. Unless the device doesn't have OPROM it will show.
	++: Select Screen 14: Select Item K/M: Scroll Help Area Up/Down. Enter: Select +/-: Change Opt. F1: General Help F3: Previous Values F9: Optimized Defaults F10: Save & Exit ESC: Exit

Parameter	Description
NVMe Configuration	Displays the NVMe devices connected to the system.
NVMe OPROM Select	Options available: BIOS Build-In, NVMe Device. Default setting is BIOS Build-In.

5-2-9 Chipset Configuration

		Specify what state when
P2P Bridge IO Size	[0×1000]	power is re-applied after a power failure (G3 state
SATA HDD Security Frozen	[Enabled]	
NVMe SSD Security Frozen Chassis Opened Warning	[Enabled] [Disabled]	
		↔: Select Screen ↓: Select Item
		K/M: Scroll Help Area
		Up/Down. Enter: Select
		+/-: Change Opt.
		F1: General Help
		F3: Previous Values F9: Optimized Defaults
		F10: Save & Exit
		ESC: Exit

Parameter	Description	
Restore on AC Power Loss ^(Note)	Defines the power state to resume to after a system shutdown that is due to an interruption in AC power. When set to Last State, the system will return to the active power state prior to shutdown. When set to Power Off, the system remains off after power shutdown. Options available: Last State, Power Off, Power On, Unspecified. The default setting depends on the BMC setting.	
P2P Bridge IO Size	Specifies P2P Bridge IO aligned to the size. Options available: 0x100, 0x150, 0x1000. Default setting is 0x1000 .	
SATA HDD Security Frozen	Enable/Disable this item to send freeze lock command to SATA HDD. Options available: Enabled, Disabled. Default setting is Enabled .	
NVMe SSD Security Frozen	Attempt to send freeze lock command to NVMe SSDs during boot. Options available: Enabled, Disabled. Default setting is Enabled .	
Chassis Opened Warning	Enable/Disable the chassis intrusion alert function. Options available: Enabled, Disabled, Clear. Default setting is Disabled .	

(Note) When the power policy is controlled by BMC, please wait for 15-20 seconds for BMC to save the last power state.

5-2-10 TIs Auth Configuration

Aptio Se	tup – AMI
▶ Server CA Configuration	Press <enter> to configure Server CA.</enter>
▶ Client Cert Configuration	
	<pre>++: Select Screen 11: Select Item K/H: Scroll Help Area Up/Down. Enter: Select +/-: Change Opt. F1: General Help F3: Previous Values F3: Optimized Defaults F10: Save & Exit ESC: Exit</pre>
Version 2.22.1287 Co	opyright (C) 2022 AMI

Parameter	Description
	Press [Enter] for configuration of advanced items.
	Enroll Cert
	 Press [Enter] to enroll a certificate
	Enroll Cert Using File
Conver CA Configuration	Cert GUID
Server CA Configuration	Input digit character in 1111111-2222-3333-4444-1234567890ab
	format.
	 Commit Changes and Exit
	- Discard Changes and Exit
	Delete Cert
Client Cert Configuration	Press [Enter] for configuration of advanced items.

5-2-11 iSCSI Configuration

	Change the priority using
▶ Host ISCSI Configuration	+/- keys. Use arrow keys to select the attempt then press +/- to move the attempt up/down in the attempt order list.
	++: Select Screen 14: Select Item K/M: Scroll Help Area Up/Down. Enter: Select +/-: Change Opt. F1: General Help F3: Previous Values F9: Optimized Defaults F10: Save & Exit ESC: Exit

Parameter	Description
Attempt Priority	 Press [Enter] configure advanced items. Attempt Priority Use arrow keys to select the attempt, then press +/- keys to move the attempt up/down in the attempt order list. Commit Changes and Exit
Host iSCSI Configuration	 Press [Enter] to configure advanced items. iSCSI Initiator Name Only IQN format is accepted. Range: from 4 to 223 Add an Attempt Delete Attempts Change Attempt Order

5-2-12 Intel(R) Ethernet Controller X710 for 10GBASE-T

Advanced	Aptio Setup – AMI	
 Firmware Image Properties NIC Configuration 		View device firmware version information.
Blink LEDs	0	
UEFI Driver Adapter PBA Device Name Chip Type PCI Device ID PCI Address Link Status MAC Address Virtual MAC Address	Intel(R) 406bE 4.9.13 H64862-000 Intel(R) Ethernet Controller X710 for 10GBASE-T Intel X710 15FF 03:00:00 [Connected] 00:00:00:00:01:00 00:00:00:00:00:00	<pre>++: Select Screen f4: Select Item K/M: Scroll Help Area Up/Down. Enter: Select +/-: Change Opt. F1: General Help F3: Previous Values F9: Optimized Defaults F10: Save & Exit ESC: Exit</pre>
Versio	n 2.22.1287 Copyright (C) 2022	AMI
Advanced	Aptio Setup - AMI	
Link Speed Wake On LAN LLDP Agent	[Auto Negotiated] [Enabled] [Enabled]	Enables power on of the system via LAN. Note that configuring Wake on LAN in the operating system does not change the value of this setting, but does override the behavior of Wake on LAN in OS controlled power states.
		<pre>++: Select Screen 11: Select Item K/M: Scroll Help Area Up/Doun. Enter: Select +/-: Change Opt. F1: General Help F3: Previous Values F9: Optimized Defaults F10: Save & Exit ESC: Exit</pre>
		AMI

Parameter	Description
Firmware Image Properties	Press [Enter] to view device firmware version information.
NIC Configuration	 Press [Enter] to configure advanced items. Link Speed Default setting is Auto Negotiated. Wake On LAN Enables power on of the system via LAN. Note that configuring Wake on LAN in the operating system does not change the value of this setting, but does override the behavior of Wake on LAN in OS controlled power states. Options available: Enabled, Disabled. Default setting is Enabled. LLDP Agent Enable/Disable firmware's LLDP Agent. Options available: Enabled, Disabled. Default setting is Enabled
Blink LEDs	Identifies the physical network port by blinking the associated LED. Press the numeric keys to adjust desired values (up to 15 seconds).
UEFI Driver	Displays the technical specifications for the Network Interface Controller.
Adapter PBA	Displays the technical specifications for the Network Interface Controller.
Device Name	Displays the technical specifications for the Network Interface Controller.
Chip Type	Displays the technical specifications for the Network Interface Controller.
PCI Device ID	Displays the technical specifications for the Network Interface Controller.
PCI Address	Displays the technical specifications for the Network Interface Controller.
Link Status	Displays the technical specifications for the Network Interface Controller.
MAC Address	Displays the technical specifications for the Network Interface Controller.
Virtual MAC Address	Displays the technical specifications for the Network Interface Controller.

5-2-13 VLAN Configuration

- Contract of States of States		and the second second second second
Create new VLAN VLAN ID		VLAN ID of new VLAN or
Priority	0	existing VLAN, valid valu is 0~4094
Add VLAN	v	15 0 4034
Configured VLAN List		
Remove VLAN		
		++: Select Screen
		↑↓: Select Item
		K/M: Scroll Help Area
		Up/Down.
		Enter: Select +/-: Change Opt.
		F1: General Help
		F3: Previous Values
		F9: Optimized Defaults
		F10: Save & Exit
		ESC: Exit

Parameter	Description
Enter Configuration Menu	 Press [Enter] to configure advanced items. Create new VLAN VLAN ID Sets VLAN ID for a new VLAN or an existing VLAN. Press the <+> / <-> keys to increase or decrease the desired values. The valid range is from 0 to 4094. Priority Sets 802.1Q Priority for a new VLAN or an existing VLAN. Press the <+> / <-> keys to increase or decrease the desired values. The valid range is from 0 to 7. Add VLAN Press [Enter] to create a new VLAN or update an existing VLAN. Configured VLAN List Remove VLAN Press [Enter] to remove an existing VLAN.

5-2-14 Driver Health

		Provides Health Status for
Intel(R) 406bE 4.9.13	Healthy	the Drivers/Controllers
		++: Select Screen †1: Select Ttem K/M: Scrooll Help Area
		Up/Down. Enter: Select +/−: Change Opt. F1: General Helo
		F3: Previous Values F9: Optimized Defaults F10: Save & Exit ESC: Exit

Parameter	Description
Driver Health	Displays driver health status of the devices/controllers if installed

5-3 Chipset Menu

Chipset Setup menu displays submenu options for configuring the function of Platform Controller Hub(PCH). Select a submenu item, then press <Enter> to access the related submenu screen.

Aptio Setup - AMI	
Main Advanced Chipset Server Mgmt Security Boot Save & Exit	
 Processor Configuration Common RefCode Configuration UPI Configuration Memory Configuration IIO Configuration Advanced Power Management Configuration PCH-IO Configuration Miscellaneous Configuration Server ME Configuration Runtime Error Logging Power Policy 	Displays and provides options to change the Processor Settings
	+: Select Screen 1: Select Item K/M: Scroll Help Area Up/Down. Enter: Select +/-: Change Opt. F1: General Help F3: Previous Values F9: Optimized Defaults F10: Save & Exit ESC: Exit
Version 2.22.1287 Copyright (C) 2022 AMI	

5-3-1 Processor Configuration

Chipset	Aptio Setup — AMI	
Processor Configuration		▲ Change Per-Socket Settings
 Per-Socket Configuration Processor Socket Processor ID Processor Frequency Processor Frequency Processor Max Ratio Processor Max Ratio Microcode Revision L1 Cache RAM(Per Core) L2 Cache RAM(Per Core) L3 Cache RAM(Per Package) Processor 0 Version Processor 1 Version Enable LP [Global] Hardware Prefetcher L2 R Prefetch Olsable 	Socket 0 Socket 1 000806F8+ 000806F8 XCC XCC 2.400GH2 2.400GH2 10H 10H 00H 00H 280000C0 280000C0 80KB 80KB 2048KB 2048KB 93840KB 99840KB 93840KB 99840KB Intel(R) Xeon(R) Platin um 8468V Intel(R) Xeon(R) Platin um 8468V [ALL LPS] [Enable] [Disable] [Disable]	<pre>+*: Select Screen 14: Select Item K/M: Scroll Help Area Up/Opum. Enter: Select +/-: Change Opt. F1: General Help F3: Previous Values</pre>
Adjacent Cache Prefetch DCU Streamer Prefetcher DCU IP Prefetcher Extended APIC	[Enable] [Enable] [Disable]	F9: Optimized Defaults F10: Save & Exit ESC: Exit

rsion 2.22.1287 Copyright (C) 202 Aptio Setup - AMI

Chipset Intel(R) Xeon(R) Platin ▲ Displays and provides um 8468V option to change the Intel(R) Xeon(R) Platin Processor CFR Settings um 8468V

↔: Select Screen †↓: Select Item

Up/Down.

Enter: Select +/-: Change Opt.

F1: General Help

F10: Save & Exit ESC: Exit

F3: Previous Values F9: Optimized Defaults

K/M: Scroll Help Area

[ALL LPS] [Enable] [Disable] [Enable]

DCU Streamer Prefetcher [Enable] DCU IP Prefetcher [Enable] Extended APIC [Disable] Enable Intel(R) TXT [Disable] [Enable] [Disable] [Enable] Debug Consent [Disable] TME, TME-MT, TDX Memory Encryption (TME) [Disabled] SGX setup configuration preconditions for enabling were NOT

met. Please check TME, MirrorMode or Extended APIC settings.

Processor 0 Version

Processor 1 Version

Enable LP [Global]

VMX

AES-NI

Enable SMX

Hardware Prefetcher

L2 RFO Prefetch Disable

Adjacent Cache Prefetch

Parameter	Description
Processor Configuration	
Pre-Socket Configuration	 Press [Enter] to configure advanced items. CPU Socket 0 Configuration Core Disable Bitmap(Hex) Number of Cores to enable. 0 means all cores. FFFFFFF means to disable all cores. The maximum value depends on the number of CPUs available. Press the numeric keys to adjust desired values.
Processor Socket / Processor ID / Processor Die Type / Processor Frequency / Processor Max Ratio / Processor Min Ratio / Microcode Revision / L1 Cache RAM(Per Core) / L2 Cache RAM(Per Core) / L3 Cache RAM(Per Package) / Processor # Version	Displays the technical specifications for the installed processor(s).
Enable LP [Global]	Enables Logical processor (Software Method to Enable/Disable Logical Processor threads). Options available: ALL LPs, Single LP. Default setting is ALL LPs .
Hardware Prefetcher	Select whether to enable the speculative prefetch unit of the processor. Options available: Enable, Disable. Default setting is Enable .
L2 RF0 Prefetch Disable	Options available: Enable, Disable. Default setting is Disable .
Adjacent Cache Prefetch	When enabled, cache lines are fetched in pairs. When disabled, only the required cache line is fetched. Options available: Enable, Disable. Default setting is Enable .
DCU Streamer Prefetcher	Enable/Disable DCU streamer prefetcher. Options available: Enable, Disable. Default setting is Enable .
DCU IP Prefetcher	Enable/Disable DCU IP Prefetcher. Options available: Enable, Disable. Default setting is Enable .
Extended APIC	Enable/Disable extended APIC support. Note: The VT-d will be enabled automatically when x2APIC is enabled. Options available: Enable, Disable. Default setting is Enable .
Enable Intel(R) TXT	Enable/Disable the Intel Trusted Execution Technology support function. Options available: Enable, Disable. Default setting is Disable .
VMX	Enable/Disable the Vanderpool Technology. This will take effect after rebooting the system. Options available: Enable, Disable. Default setting is Enable .
Enable SMX	Enable/Disable the Safer Mode Extensions (SMX) support function. Options available: Enable, Disable. Default setting is Disable .
AES-NI	Enable/Disable the AES-NI support. Options available: Enable, Disable. Default setting is Enable .
Debug Consent	Options available: Enable, Disable. Default setting is Disable .

Parameter	Description	
Memory Encryption (TME) ^(Note)	Enable/Disable memory encryption (TME). Options available: Enabled, Disabled. Default setting is Disabled .	
Total Memory Encryption Multi-Tenant (TME-MT)	Options available: Enabled, Disabled. Default setting is Disabled .	
Processor CFR Configuration	 Press [Enter] to configure advanced items. Provision S3M CFR Options available: Disable, Enable. Default setting is Enable. Manual Commit S3M FW CFR Options available: Disable, Enable, Auto. Default setting is Auto. Provision PUcode CFR Options available: Disable, Enable. Default setting is Enable. Manual Commit PUcode CFR Options available: Enable, Disable, Auto. Default setting is Auto. Socket0/1 CFR Revision Info Displays CFR Revision information of the socket. 	

5-3-2 Common RefCode Configuration

Common RefCode Configuration		Enable or Disable Non
Numa Virtual Numa	(Enable) (Enable)	uniform Memory Access (NUMA).
		<pre>++: Select Screen fi: Select Item K/M: Scroll Help Area Up/Down. Enter: Select +/-: Change Opt. F1: General Help F3: Previous Values F9: Optimized Defaults F10: Save & Exit ESC: Exit</pre>

Parameter	Description
Common RefCode Configuration	
Numa	Enable/Disable Non uniform Memory Access(NUMA). Options available: Enable, Disable. Default setting is Enable .
Virtual Numa	Divide physical NUMA nodes into evenly sized virtual NUMA nodes in ACPI table. This may improve Windows performance on CPUs with more than 64 logical processors. Options available: Enable, Disable. Default setting is Disable .

5-3-3 UPI Configuration

UPI Configuration	Displays and provides
UPI General Configuration	option to change the UPI General Settings
	++: Select Screen 14: Select Item K/M: Scroll Help Area Up/Doun. Enter: Select +/-: Change Qot. F1: General Help F3: Previous Values F9: Optimized Defaults F10: Save & Exit ESC: Exit

Parameter	Description	
UPI General Configuration	 Press [Enter] to configure advanced items. UPI Status Press [Enter] to view the Uncore status. Link Frequency Select Selects the UPI link frequency. Options available: 12.8GT/s, 14.4GT/s, 16.0GT/s, Auto, Use Per Link Setting. Default setting is Auto. SNC Enable/Disable Sub NUMA Cluster function. Options available: Auto, Disable, Enable SNC2 (2-clusters), Enable SNC4 (4-clusters). Default setting is Auto. Stale AtoS Enable/Disable Stale A to S directory optimization. Options available: Disable, Enable, Auto. Default setting is Auto. LLC dead line alloc Enable/Disable fill dead lines in LLC. Options available: Disable, Enable, Auto. Default setting is Enable. MMIO High Base Options available: 56T, 40T, 32T, 24T, 16T, 4T, 2T, 1T, 512G, 3584T. Default setting is 32T. 	

Parameter	Description	
	•	MMIO High Granularity Size
		 Selects the allocation size used to assign mmioh resources.
UPI General Configuration		- Options available: 1G, 4G, 16G, 64G, 256G, 1024G. Default setting is
(continued)		64G.
	•	Clock Modulation Enabled
		- Options available: Disable, Enable, Auto. Default setting is Auto.

5-3-4 Memory Configuration

Integrated Memory Controller (iMC)		Enforces Plan Of Record restrictions for DDR frequency programming.
Enforce DDR Memory Frequency POR Memory Frequency Enable ADR Legacy ADR Mode Minimum System Memory Size ADR Data Save Mode Assert ADR on Reset Assert ADR on SS	[POR] [Auto] [Enable] [Auto] [268] [NVDIMMS] [Disabled] [Disabled]	
Get Memory Timing Memory Topology Memory Map Memory RAS Configuration	[BIOS Build-in]	<pre>++: Select Screen 11: Select Item K/M: Scroll Help Area Up/Down. Enter: Select +/-: Change Opt. F1: General Help F3: Previous Values F9: Optimized Defaults F10: Save & Exit ESC: Exit</pre>

Parameter	Description
Integrated Memory Controller (iMC)	
Enforce DDR Memory Frequency POR	When set to Enable, the system enforces Plan Of Record restrictions for DDR frequency programming. Options available: POR, Disable. Default setting is POR .
Memory Frequency	Configures the maximum memory frequency. If Enforce POR is disabled, user will be able to run at higher frequencies than the memory support (limited by processor support). Default setting is Auto .
Enable ADR	Enables the detecting and enabling of ADR (Asynchronous DRAM Refresh) function. Options available: Enable, Disable. Default setting is Enable .
Legacy ADR Mode	Enable/Disable the Legacy ADR Mode. Options available: Enable, Disable, Auto. Default setting is Auto .
Minimum System Memory Size	Configures the minimum memory size. Options available: 2GB, 4GB, 6GB, 8GB. Default setting is 2GB .
ADR Data Save Mode	Specifies the Data Save Mode for ADR. Batterybacked or Type 01 NVDIMM. Options available: Disable, Batterybacked DIMMs, NVDIMMs, Copy to Flash. Default setting is NVDIMMs .
Assert ADR on Reset	Enable/Disable Assert ADR on Reset. Options available: Enabled, Disabled. Default setting is Disabled .

Parameter	Description		
Assert ADR on S5	Enable/Disable Assert ADR on S5. Options available: Enabled, Disabled. Default setting is Disabled .		
Get Memory Timing	Auto is the detected SPD value and use it, otherwise use BIOS Build-in. Options available: Auto, BIOS Build-in. Default setting is BIOS Build-in .		
Memory Topology	Press [Enter] to view memory topology with DIMM population information.		
Memory Map ^(Note1)	 Press [Enter] to configure advanced items. Volatile Memory Mode Selects 1LM or 2LM mode for volatile memory. Options available: 1LM, 2LM. Default setting is 2LM. 		
Memory RAS Configuration	 Press [Enter] to configure advanced items. Mirror Mode^(Note2) Mirror Mode will set entire 1LM memory in system to be mirrored, consequently reducing the memory capacity by half. Enables the Mirror Mode will disable the XPT Prefetch. Options available: Disabled, Full Mirror Mode, Partial Mirror Mode. Default setting is Disabled. Partial Mirror 1 Size (GB) Selects multiplier of 1GB for the size of the SAD to be created. Correctable Error Threshold Correctable Error Threshold (0x01-0x7fff) used for sparing, and leaky bucket. Press the <+> / <>> keys to increase or decrease the desired values. Trigger SW Error Threshold^(Note2) Enable/Disable Sparing trigger SW Error Match Threshold. Options available: Disabled, Enabled. Default setting is Disabled. SW Per Bank Threshold SW Per Bank Threshold (1-0x7FFF) used for DDR bank level error. Press the <+> / <>> keys to increase or decrease the desired values. SW Correctable Error Time Window SW Correctable Error Time Window SW Correctable Error Time window based interface in hour (0-24). Press the <+> / <>> keys to increase or decrease the desired values. 		

(Note1) Advanced items prompt when HBM CPU is installed.

(Note2) Advanced items prompt when this item is defined.

(Note) Advanced items prompt when this item is defined.

5-3-5 IIO Configuration

Aptio Setup - Chipset	AMI
IIO Configuration → Intel VT for Directed I/O (VT-d) ▶ Intel VMD technology	Press <enter> to bring up the Intel Virtualization for Directed I/O (VT-d) Configuration menu.</enter>
	++: Select Screen fl: Select Item K/H: Scroll Help Area Up/Down. Enter: Select +/-: Change Opt. Fl: General Help F3: Previous Values F9: Optimized Defaults F10: Save & Exit ESC: Exit
Version 2.22.1287 Copyri	ght (C) 2022 AMI

Parameter	Description
IIO Configuration	
Intel® VT for Directed I/O (VT-d)	 Press [Enter] to configure advanced items. Intel® VT for Directed I/O Enable/Disable the Intel VT for Directed I/O (VT-d) support function by reporting the I/O device assignment to VMM through DMAR ACPI Tables. Options available: Enable, Disable. Default setting is Enable. ACS Control Enable: Programs ACS only to Chipset PCIe Root Ports Bridges. Disable: Programs ACS to all PCIe bridges. Default setting is Enable. Cache Allocation Options available: Enable, Disable. Default setting is Enable. Opt-Out Illegal MSI Mitigation Enable/Disable Opt-Out Illegal 0xFEE Platform Mitigation. Options available: Disable, Enable. Default setting is Disable. DMA Control Opt-In Flag Enable/Disable DMA_CTRL_PLATFORM_OPT_IN_FLAG in DMAR table in ACPI. Not compatible with Direct Device Assignment (DDA). Options available: Enable, Disable. Default setting is Disable.

Parameter	Description	
	 Interrupt Remapping Enable/Disable the interrupt remapping support function. Options available: Auto, Enable, Disable. Default setting is Auto x2APIC Opt Out Options available: Enable, Disable. Default setting is Disable. Pre-boot DMA Protection Options available: Enable, Disable. Default setting is Disable. 	
Intel® VMD technology	Press [Enter] to configure advanced items. Intel® VMD Configuration Enable/Disable Intel® VMD technology. Options available: Enable, Disable. Default setting is Disable. Intel® VMD for Non-Hotplug NVMe ^(Note) Enable/Disable Intel® VMD for Non-Hotplug NVMe. Options available: Enable, Disable. Default setting is Disable.	

5-3-6 Advanced Power Management Configuration

Advanced Power Management Configuration	P State Control
CPU P State Control Hardware PM State Control CPU C State Control Package C State Control CPU - Advanced PM Tuning	Configuration Sub Menu, include Turbo, XE and etc
	++: Select Screen T1: Select Item K/M: Scroll Help Area Up/Down. Enter: Select +/-: Change Opt. F1: General Help F3: Previous Values F9: Optimized Defaults F10: Save & Exit ESC: Exit

Parameter	Description
CPU P State Control	 Press [Enter] to configure advanced items. SpeedStep (Pstates) Conventional Intel SpeedStep Technology switches both voltage and frequency in tandem between high and low levels in response to processor load. Options available: Enable, Disable. Default setting is Enable. Turbo Mode When this item is enabled, the processor will automatically ramp up the clock speed of 1-2 of its processing cores to improve its performance. When this item is disabled, the processor will not overclock any of its core. Options available: Enable, Disable. Default setting is Enable.
Hardware PM State Control	 Press [Enter] to configure advanced items. Hardware P-States When this item is disabled, the processor hardware chooses a P-state based on OS Request (Legacy P-States). In Native mode, the processor hardware chooses a P-state based on OS guidance. In Out of Band mode, the processor hardware autonomously chooses a P-state (with no OS guidance). Options available: Disable, Native Mode, Out of Band Mode, Native Mode with No Legacy Support. Default setting is Native Mode.

Parameter	Description
CPU C State Control	 Press [Enter] to configure advanced items. Enable Monitor MWAIT Allows Monitor and MWAIT instructions. Options available: Disable, Enable, Auto. Default setting is Auto. CPU C6 Report Enable/Disable CPU C6(ACPI C3) report to OS. Options available: Disable, Enable, Auto. Default setting is Auto. Enhanced Halt State (C1E) Core C1E auto promotion control. Takes effect after reboot. Options available: Enable, Disable. Default setting is Enable.
Package C State Control	 Press [Enter] to configure advanced items. Package C State Configures the state for the C-State package limit. Options available: C0/C1 state, C2 state, C6(non Retention) state, C6(Retention) state, No Limit, Auto. Default setting is Auto.
CPU - Advanced PM Tuning	 Press [Enter] to configure advanced items. Energy Perf BIAS Press [Enter] to configure advanced items. Power Performance Tuning Options available: OS Controls EPB, BIOS Controls EPB, PECI Controls EPB. Default setting is OS Controls EPB. Energy_PERF_BIAS_CFG mode^[Note] Options available: Performance, Balanced Performance, Balanced Power, Power. Default setting is Balanced Performance.

5-3-7 PCH Configuration

PCH-IO Configuration	Device Options Settings
	<pre>++: Select Screen 11: Select Item K/M: Scroll Help Area Up/Down. Enter: Select +/-: Change Opt. F1: General Help F3: Previous Values F9: Optimized Defaults F10: Save & Exit ESC: Exit</pre>

Parameter	Description
PCH-IO Configuration	
SATA And RST Configuration/ SATA Controller And RST Configuration	 Press [Enter] to configure advanced items. SATA Configuration Enable/Disable SATA controller. Options available: Enabled, Disabled. Default setting is Enabled. SATA Mode Selection Configures on chip SATA type. AHCI Mode: When set to AHCI, the SATA controller enables its AHCI functionality. Then the RAID function is disabled and cannot be access the RAID setup utility at boot time. RAID Mode: When set to RAID, the SATA controller enables both its RAID and AHCI functions. You will be allowed to access the RAID setup utility at boot time. Options available: AHCI, RAID. Default setting is AHCI. RAID Device ID^[Note] Choose RAID Device ID. Options available: Client, Alternate, Server. Default setting is Server. SATA Port 0/1/2/3/4/5/6/7 The category identifies SATA hard drives that are installed in the computer. System will automatically detect HDD type.

Parameter	Description
SATA And RST Configuration/ SATA Controller And RST Configuration (continued)	 Port 0/1/2/3/4/5/6/7 Enable/Disable Port 0/1/2/3/4/5/6/7 device. Options available: Enabled, Disabled. Default setting is Enabled. Hot Plug (for Port 0/1/2/3/4/5/6/7) Enable/Disable HDD Hot-Plug function. Options available: Enabled, Disabled. Default setting is Enabled. Spin Up Device (for Port 0/1/2/3/4/5/6/7) On an edge detect from 0 to 1, the PCH starts a COM reset initialization to the device. Options available: Enabled, Disabled. Default setting is Disabled.
SATA And RST Configuration/ sSATA Controller And RST Configuration	 SATA Configuration Enable/Disable SATA controller. Options available: Enabled, Disabled. Default setting is Enabled. SATA Mode Selection Configures on chip SATA type. AHCI Mode: When set to AHCI, the SATA controller enables its AHCI functionality. Then the RAID function is disabled and cannot be access the RAID setup utility at boot time. RAID Mode: When set to RAID, the SATA controller enables both its RAID and AHCI functions. You will be allowed to access the RAID setup utility at boot time. Options available: AHCI, RAID. Default setting is AHCI. RAID Device ID^(Note) Choose RAID Device ID. Options available: Client, Alternate, Server. Default setting is Server. SATA Port 4/5/6/7 The category identifies sSATA hard drives that are installed in the computer. System will automatically detect HDD type. SATA Port 4/5/6/7 Enable/Disable Port 4/5/6/7 device. Options available: Enabled, Disabled. Default setting is Enabled. Hot Plug (for Port 4/5/6/7) Enable/Disable HDD Hot-Plug function. Options available: Enabled, Disabled. Default setting is Enabled. Spin Up Device (for Port 4/5/6/7) On an edge detect from 0 to 1, the PCH starts a COM reset initialization to the device. Options available: Enabled, Disabled. Default setting is Disabled.

5-3-8 Miscellaneous Configuration

Miscellaneous Configuration		Select active Video type
Active Video External SSC – CK440	[Auto] [SSC Off]	
		<pre>++: Select Screen f4: Select Item K/M: Scroll Help Area Up/Down. Enter: Select +/-: Change Opt. F1: General Help F3: Previous Values F9: Optimized Defaults F10: Save & Exit ESC: Exit</pre>

Parameter	Description
Miscellaneous Configuration	
	Selects the active video type.
Active Video	Options available: Auto, Onboard Device, PCIE Device, Specific PCIE
	Device. Default setting is Auto.
	Enables Spread spectrum - only affects external clock generator.
External SSC - CK440	Options available: SSC Off, SSC = -0.3%, SSC = -0.5%, Hardware.
	Default setting is SSC Off.

5-3-9 Server ME Configuration

	Aptio Setup - AMI Chipset		
General ME Configuration Oper. Firmware Version 18:6.0.3.256 ME Firmware Status #1 0x0000355 ME Firmware Status #2 0x80500606 Current State Operational Error Code No Error Recovery Cause N/A	+: Select Screen 14: Select Item K/M: Scroll Help Area Up/Doum. Enter: Select +/-1 Change Opt. F1: General Help F3: Previous Values F9: Optimized Defaults F10: Save & Exit ESC: Exit		

Parameter	Description
General ME Configuration	
Oper. Firmware Version	Displays the operational firmware version.
ME Firmware Status #1/#2	Displays ME Firmware status information.
Current State	Displays ME Firmware current status information.
Error Code	Displays ME Firmware status error code.
Recovery Cause	Displays ME Firmware recovery cause.

5-3-10 Runtime Error Logging Settings

Runtime Error Logging		System Error
System Errors S/W Error Injection Support Whea Settings Memory Error Enabling PCIe Error Enabling	(Enable) (Disable)	Enable/Disable setup options.
		++: Select Screen 14: Select Item K/M: Scroll Help Area Up/Doun. Enter: Select +/-: Change Opt. F1: General Help F3: Previous Values F9: Optimized Defaults F10: Save & Exit ESC: Exit

Parameter	Description
Runtime Error Logging	
0	Enable/Disable system error logging function.
System Errors	Options available: Enable, Disable. Default setting is Enable.
CAN/Error Injection Cunnert	Enable/Disable software injection error logging function.
S/W Error Injection Support	Options available: Enable, Disable. Default setting is Disable .
	Press [Enter] to configure advanced items.
Whea Settings	WHEA (Windows Hardware Error Architecture) Support
whea Settings	 Enable/Disable WHEA Support.
	 Options available: Enable, Disable. Default setting is Enable.
	Press [Enter] to configure advanced items.
	Memory Corrected Error
	 Enable/Disable Memory Corrected Error.
Memory Error Enabling	 Options available: Enable, Disable. Default setting is Enable.
	Uncorrected Error disable Memory
	 Enable/Disable the Memory that triggers Uncorrected Error.
	 Options available: Enable, Disable. Default setting is Disable.

Parameter	Description
PCle Error Enabling	 Press [Enter] to configure advanced items. PCIE Error Enable/Disable PCIE error. Options available: Enable, Disable. Default setting is Disable. Uncorrected Error^(Note) Enables and escalates Uncorrectable/Recoverable Errors to error pins. Options available: Enable, Disable. Default setting is Enable. Fatal Error Enable^(Note) Enables and escalates Fatal Errors to error pins. Options available: Enable, Disable. Default setting is Enable. Fatal Error Enable^(Note) Enables and escalates Fatal Errors to error pins. Options available: Enable, Disable. Default setting is Enable. Assert NMI on SERR^(Note) Enable/Disable BIOS generates a non-maskable interrupt (NMI) and logs an error when a system error (SERR) occurs. Options available: Enabled, Disabled. Default setting is Enabled. Assert NMI on PERR^(Note) Enable/Disable BIOS generates a non-maskable interrupt (NMI) and logs an error when a system error (SERR) occurs. Options available: Enabled, Disabled. Default setting is Enabled. Assert NMI on PERR^(Note) Enable/Disable BIOS generates a non-maskable interrupt (NMI) and logs an error when a processor bus parity error (PERR) occurs. Options available Enabled, Disabled. Default setting is Enabled.

5-3-11 Power Policy

Chipset	Aptio Setup — AMI	
Power Policy Quick Settings SpeedStep (Pstates) Turbo Mode CPU C6 report Enhanced Halt State (CIE) Package C State Enable LP [Global] Hardware Prefetcher Adjacent Cache Prefetch DCU Streamer Prefetcher Intel VT for Directed I/O	[Standard] [Enable] [Auto] [Auto] [Auto] [Auto] [ALL LPS] [Enable] [Enable] [Enable] [Enable] [Enable]	Select a Power Policy Quick Setting(The following items will be set based on the selected power policy)
		<pre>++: Select Screen 11: Select Item K/M: Scroll Help Area Up/Down. Enter: Select 4/-: Change Opt. F1: General Help F3: Previous Values F9: Optimized Defaults F10: Save & Exit ESC: Exit</pre>

Parameter	Description
	Selects a Power Policy Quick Setting.
Power Policy Quick Settings	Options available: Standard, Best Performance, Energy Efficient. Default
	setting is Standard.
	Conventional Intel SpeedStep Technology switches both voltage and
SpeedStep (Pstates)	frequency in tandem between high and low levels in response to processor
SpeedStep (1 states)	load.
	Options available: Enable, Disable. Default setting is Enable .
	When this item is enabled, the processor will automatically ramp up the
Turbo Mode	clock speed of 1-2 of its processing cores to improve its performance.
TUIDO MODE	When this item is disabled, the processor will not overclock any of its core.
	Options available: Enable, Disable. Default setting is Enable.
	Enable/Disable the BIOS to enable the report from the CPU C6 state (ACPI
CPU C6 report	C3) to the OS.
	Options available: Disable, Enable, Auto. Default setting is Auto.
	Enable/Disable the C1E support for lower power consumption. Takes effect
Enhanced Halt State (C1E)	after reboot.
	Options available: Enable, Disable. Default setting is Enable .
	Configures the C-State package limit.
Package C State	Options available: C0/C1 state, C2 state, C6(non Retention) state,
	C6(Retention) state, No Limit, Auto. Default setting is Auto.

Parameter	Description
	Enables Logical processor (Software Method to Enable/Disable Logical
Enable LP [Global]	Processor threads).
	Options available: ALL LPs, Single LP. Default setting is ALL LPs.
Hardware Prefetcher	Options available: Enable, Disable. Default setting is Enable .
Adjacent Cache Prefetch	Options available: Enable, Disable. Default setting is Enable .
DCU Streamer Prefetcher	Options available: Enable, Disable. Default setting is Enable .
Intel® VT for Directed I/O	Enable/Disable the Intel VT for Directed I/O (VT-d) support function by reporting the I/O device assignment to VMM through DMAR ACPI Tables. Options available: Enable, Disable. Default setting is Enable .

5-4 Server Management Menu

Main Advanced Chipset Server Mg	Aptio Setup – AMI mt Security Boot Save & Exit	
FRB-2 Timer FRB-2 Timer timeout FRB-2 Timer Policy OS Watchdog Timer OS Wtd Timer Policy Wait BMC Ready > System Event Log > View FRU information > BMC VLAN Configuration > BMC network configuration > IPv6 BMC Network Configuration	[Enabled] 6 [Do Nothing] [Disabled] 10 [Reset] [2 minutes]	Enable or Disable FRB-2 timer(POST timer)
F 1FVD DHC HELWUIK CUIHIIgu aliun		++: Select Screen f1: Select Item K/M: Scroll Help Area Up/Doun. Enter: Select +/-: Change Opt. F1: General Help F3: Previous Values F9: Optimized Defaults F10: Save & Exit ESC: Exit
Version	2.22.1287 Copyright (C) 2022 AMI	

Parameter	Description
FRB-2 Timer	Enable/Disable FRB-2 timer (POST timer). Options available: Enabled, Disabled. Default setting is Enabled .
FRB-2 Timer ^(Note1) timeout	Configures the FRB2 Timer timeout. The value is between 1 to 30 minutes. Default setting is 6 minutes .
FRB-2 Timer Policy ^(Note1)	Configures the FRB2 Timer policy. Options available: Do Nothing, Reset, Power Down, Power Cycle. Default setting is Do Nothing .
OS Watchdog Timer	Enable/Disable OS Watchdog Timer function. Options available: Enabled, Disabled. Default setting is Disabled .
OS Wtd Timer Timeout ^(Note2)	Configures OS Watchdog Timer. The value is between 1 to 30 minutes. Default setting is 10 minutes .
OS Wtd Timer Policy ^(Note2)	Configure OS Watchdog Timer Policy. Options available: Reset, Do Nothing, Power Down, Power Cycle. Default setting is Reset .
Wait BMC Ready	POST wait BMC ready and reboot system. Options available: Disabled, 2 minutes, 4 minutes, 6 minutes. Default setting is 2 minutes .

(Note1) This item is configurable when FRB-2 Timer is set to Enabled.

(Note2) This item is configurable when OS Watchdog Timer is set to Enabled.

Parameter	Description
System Event Log	Press [Enter] to configure advanced items.
View FRU Information	Press [Enter] to view the FRU information.
BMC VLAN Configuration	Press [Enter] to configure advanced items.
BMC network Configuration	Press [Enter] to configure advanced items.
IPv6 BMC Network Configuration	Press [Enter] to configure advanced items.

5-4-1 System Event Log

Enabling/Disabling Options		Change this to enable or
		disable event logging for
Encount Cottings		error/progress codes
Erasing Settings Erase SEL	[No]	during boot.
When SEL is Full	[Do Nothing]	
	100 H0011181	
Custom EFI Logging Options		
Log EFI Status Codes	[Error code]	
		↓: Select Item K/M: Scroll Help Area
		++: Select Screen
		K/M: Scroll Help Area
		Up/Down.
		Enter: Select
		+/-: Change Opt.
		F1: General Help
		F1: General Help F3: Previous Values
		F1: General Help
		F1: General Help F3: Previous Values F9: Optimized Defaults

Parameter	Description
Enabling / Disabling Options	
SEL Components	Change this item to enable or disable all features of System Event Logging during boot. Options available: Enabled, Disabled. Default setting is Enabled .
Erasing Settings	
Erase SEL	Choose options for erasing SEL. Options available: No, Yes, On next reset, Yes, On every reset. Default setting is No .
When SEL is Full	Choose options for reactions to a full SEL. Options available: Do Nothing, Erase Immediately, Delete Oldest Record. Default setting is Do Nothing .
Custom EFI Logging Options	
Log EFI Status Codes	Enable/Disable the logging of EFI Status Codes (if not already converted to legacy). Options available: Disabled, Both, Error code, Progress code. Default setting is Error code .

5-4-2 View FRU Information

The FRU page is a simple display page for basic system ID information, as well as System product information. Items on this window are non-configurable.

FRU Information	
1 K U E F F F F F F F F F F F F F F	 Select Screen Select Item Scroll Help Area p/Down. nter: Select Change Opt. General Help Previous Values Optimized Defaults Save & Exit SC: Exit

5-4-3 BMC VLAN Configuration

BMC VLAN Configuration		VLAN ID of new VLAN or existing VLAN, valid valu
	0	is 0~4094, 0 is disable
BMC VLAN Priority	0	VLAN
		++: Select Screen
		↑↓: Select Item
		K/M: Scroll Help Area Up/Down.
		Enter: Select
		+/-: Change Opt. F1: General Help
		F3: Previous Values
		F9: Optimized Defaults
		F10: Save & Exit ESC: Exit
		LOOT EXIT

Parameter	Description
BMC VLAN Configuration	
BMC VLAN ID	Select to configure BMC VLAN ID. The valid range is from 0 to 4094. When
	set to 0, BMC VLAN ID will be disabled.
BMC VLAN Priority	Select to configure BMC VLAN Priority. The valid range is from 0 to 7.
	When BMC VLAN ID is set to 0, BMC VLAN Priority will not be selected.

5-4-4 BMC Network Configuration

BMC network configuration		Select to configure LAN
Select NCSI and Dedicated LAN Lan channel 1 Configuration Address source Station IP address Subnet mask Router IP address	[Do Nothing] [DynamicBmcDhcp] 10.1.6.117 255.255.255.0 10.1.6.253	channel parameters statically or dynamically(DHCP). Do nothing option will not modify any BMC network parameters during BIOS phase
Station MAC address Real-time get BMC network address	D8-5E-D3-E9-0D-D3	
		<pre>++: Select Screen 11: Select Item K/M: Scroll Help Area Up/Down. Enter: Select +/-: Change Opt. F/-: Change Opt.</pre>
		F1: General Help F3: Previous Values F9: Optimized Defaults F10: Save & Exit ESC: Exit

Parameter	Description
BMC network configuration	
Select NCSI and Dedicated LAN	Options available: Do Nothing, Model1(Dedicated), Model2(NCSI), Mode3(Failover). Default setting is Do Nothing .
Lan Channel 1	
Configuration Address source	Selects to configure LAN channel parameters statically or dynamically (DHCP). Options available: Unspecified, Static, DynamicBmcDhcp. Default setting is DynamicBmcDhcp .
Station IP address	Displays IP Address information.
Subnet mask	Displays Subnet Mask information. Please note that the IP address must be in three digitals, for example, 192.168.000.001.
Router IP address	Displays the Router IP Address information.
Station MAC address	Displays the MAC Address information.
Real-time get BMC network address	Press [Enter] will set LAN mode and Address source and then get IP, Subnet, Gateway and MAC address.

5-4-5 IPv6 BMC Network Configuration



Parameter	Description
IPv6 BMC network configuration	
IPv6 BMC Lan Channel 1	
IPv6 BMC Lan Option	Enable/Disable IPv6 BMC LAN channel function. When this item is disabled, the system will not modify any BMC network during BIOS phase. Options available: Unspecified, Disable, Enable. Default setting is Enable.
IPv6 BMC Lan IP Address Source	Selects to configure LAN channel parameters statically or dynamically (by BIOS or BMC). Options available: Unspecified, Static, Dynamic-Obtained by BMC running DHCP. Default setting is Dynamic-Obtained by BMC running DHCP .
IPv6 BMC Lan IP Address/ Prefix Length	Check if the IPv6 BMC LAN IP address matches those displayed on the screen.

5-5 Security Menu

The Security menu allows you to safeguard and protect the system from unauthorized use by setting up access passwords.

Main Advanced Chipset S	Aptio Setup – AMI erver Mgmt Security Boot	
Password Description If ONLY the Administrator's then this only limits acces only asked for when enterin If ONLY the User's password	s to Setup and is g Setup.	Sets administrative password
is a power on password and boot or enter Setup. In Set have Administrator rights. The password length must be in the following range:	up the User will	
Minimum length Maximum length	3 20	++: Select Screen
Maximum length 20 Administrator Password User Password ▶ Secure Boot		<pre>fl: Select Item K/M: Scroll Help Area Up/Down. Enter: Select +/-: Change Opt. F1: General Help F3: Previous Values F9: Optimized Defaults F10: Save & Exit ESC: Exit</pre>
	Version 2.22.1287 Copyright	

There are two types of passwords that you can set:

Administrator Password

Entering this password will allow the user to access and change all settings in the Setup Utility.

User Password

Entering this password will restrict a user's access to the Setup menus. To enable or disable this field, a Administrator Password must first be set. A user can only access and modify the System Time, System Date, and Set User Password fields.

Parameter	Description
Administrator Password	Press [Enter] to configure the administrator password.
User Password	Press [Enter] to configure the user password.
Secure Boot	Press [Enter] to configure advanced items.

5-5-1 Secure Boot

The Secure Boot feature is applicable if supported by your Operating System. If your Operating System is not supporting Secure Boot, the system will hang when starting the Operating System.

System Mode	Setup	Secure Boot feature is Active if Secure Boot is
		Enabled,
	Not Active	Platform Key(PK) is enrolled and the System i
Secure Boot Mode	[Custom]	in User mode.
Restore Factory Keys		The mode change requires
Reset To Setup Mode		platform reset
Key Management		
		++: Select Screen
		14: Select Item
		K/M: Scroll Help Area
		Up/Down.
		Enter: Select
		+/-: Change Opt.
		F1: General Help F3: Previous Values
		F9: Optimized Defaults
		F10: Save & Exit
		ESC: Exit

Parameter	Description
System Mode	Displays if the system is in User mode or Setup mode.
Secure Boot	Enable/ Disable the Secure Boot function. Options available: Enabled, Disabled. Default setting is Disabled .
Secure Boot Mode ^(Note)	Secure Boot requires all the applications that are running during the booting process to be pre-signed with valid digital certificates. This way, the system knows all files being loaded before the Operating System loads to the login screen have not been tampered with. When set to Standard, it will automatically load the Secure Boot keys form the BIOS databases. When set to Custom, you can customize the Secure Boot settings and manually load its keys from the BIOS database. Options available: Standard, Custom. Default setting is Custom .
Restore Factory Keys	Forces the system to user mode and installs factory default Secure Boot key database.
Reset To Setup Mode	Reset the system to Setup Mode.

(Note) Advanced items prompt when this item is set to Custom.

Parameter	Description
Key Management	 Press [Enter] to configure advanced items. Please note that this item is configurable when Secure Boot Mode is set to Custom. Factory Key Provision Allows to provision factory default Secure Boot keys when system is in Setup Mode. Options available: Enabled, Disabled. Default setting is Disabled. Restore Factory Keys Installs all factory default keys. It will force the system in User Mode. Options available: Yes, No. Reset To Setup Mode Reset To Setup Mode. Options available: Yes, No. Reset the system to Setup Mode. Options available: Yes, No. Enroll Efi Image Press [Enter] to enroll SHA256 hash of the binary into Authorized Signature Database (db). Export Secure Boot variables Copy NVRAM content of Secure Boot variables to files in a root folder on a file system device. Secure Boot variable Displays the current status of the variables used for secure boot. Platform Key (PK) Displays the current status of the Platform Key (PK). Press [Enter] to configure a new PK. Options available: Update. Key Exchange Keys (KEK) Displays the current status of the Key Exchange Key Database (KEK). Press [Enter] to configure a new KEK or load additional KEK from storage devices. Options available: Update, Append. Authorized Signatures (DB) Displays the current status of the Authorized Signature Database. Press [Enter] to configure a new DB or load additional DB from storage devices. Options available: Update, Append. Forbidden Signatures (DBX) Displays the current status of the Forbidden Signature Database. Press [Enter] to configure a new bB or load additional DB from storage devices. Options availabl

Parameter	Description
Key Management (continued)	 Authorized TimeStamps (DBT) Displays the current status of the Authorized TimeStamps Database. Press [Enter] to configure a new DBT or load additional DBT from storage devices. Options available: Update, Append. OsRecovery Signatures Displays the current status of the OsRecovery Signature Database. Press [Enter] to configure a new OsRecovery Signature or load additional OsRecovery Signature from storage devices. Options available: Update, Append.

5-6 Boot Menu

The Boot menu allows you to set the drive priority during system boot-up. BIOS setup will display an error message if the legacy drive(s) specified is not bootable.

Aptio Setup – AMI Main Advanced Chipset Server Mgmt Security <mark>Boot</mark> Save 8 Exit		
Boot Configuration Setup Prompt Timeout Bootup NumLock State Quiet Boot	1 [On] [Enabled]	Set the default timeout before system boot. A value of 65535 will disable the timeout
Endless Retry Boot	[Disable]	completely.
Setup Flash Dump full Setup Data Dump non-default Setup Data Restore Setup Data		
FIXED BOOT ORDER Priorities Boot Option #1	[Hard Disk:UEFI: SATA PO: SAMSUNG M27KM1T9HMJP-00005, Partition 1]	++: Select Screen †4: Select Item K/M: Scroll Help Area Up/Down. Enter: Select
Boot Option #2 Boot Option #3 Boot Option #4	[CO/DVO] [USB Device] [Network:UEFI: PXE IPv4 Intel(R) Ethernet Controller X710 for 106BASE-T 00:00:00:00:01:00]	Erica - Select F1: General Help F3: Previous Values F9: Optimized Defaults F10: Save & Exit ESC: Exit

Version 2.22.1287 Copyright (C) 2022 AMI

Parameter	Description
Boot Configuration	
Setup Prompt Timeout	Number of seconds to wait for setup activation key. 65535 (0xFFFF) means indefinite waiting. Press the numeric keys to input the desired values.
Bootup NumLock State	Enable/Disable the Bootup NumLock function. Options available: On, Off. Default setting is On .
Quiet Boot	Enable/Disable showing the logo during POST. Options available: Enabled, Disabled. Default setting is Enabled .
Endless Retry Boot	Options available: Disable, Enable. Default setting is Disable .
Setup Flash	Press [Enter] to run setup flash.
Dump full Setup Data	Press [Enter] to dump full setup data to file.
Dump non-default Setup Data	Press [Enter] to dump non-default setup data to file.
Restore Setup Data	Press [Enter] to restore setup data from file.

Parameter	Description	
FIXED BOOT ORDER Priorities		
Boot Option #1 / #2 / #3 / #4 / #5	Press [Enter] to configure the boot order priority. By default, the server searches for boot devices in the following sequence: 1. Hard drive. 2. CD-COM/DVD drive. 3. USB device. 4. Network. 5. UEFI.	
UEFI Network Drive BBS Priorities	Press [Enter] to configure the boot priority.	
UEFI Application Boot Priorities	Press [Enter] to configure the boot priority.	

5-7 Save & Exit Menu

The Save & Exit menu displays the various options to quit from the BIOS setup. Highlight any of the exit options then press <Enter>.

Aptio Setup – AMI Main Advanced Chipset Server Mgmt Security Boot <mark>Save & Exit</mark>	
Save Options Save & Exit Discand changes & exit Save Changes and Reset Discand Changes and Reset Save Changes Discand Changes Default Options Restore Defaults	Exit system setup after saving the changes.
Save the User Default Values Restore the User Default Values Boot Device Priority UEFI: FXE IPV4 Intel(R) Ethernet Controller X710 for 10GBASE-T DB:5E:03:E9:00:01 UEFI: PXE IPV4 Intel(R) Ethernet Controller X710 for 10GBASE-T DB:5E:03:E9:00:02 UEFI: Built-in EFI Shell Launch EFI Shell	<pre>++: Select Screen fl: Select Item K/H: Scroll Help Area Uy/Doun. Enter: Select +/-: Change Opt. Fl: General Help F3: Previous Values F3: Optimized Defaults F10: Save & Exit ESC: Exit</pre>

Version 2.22.1287 Copyright (C) 2022 AMI

Parameter	Description
Save Options	
Save and Exit	Saves changes made and closes the BIOS setup. Options available: Yes, No.
Discard changes and exit	Discards changes made and exits the BIOS setup. Options available: Yes, No.
Save Changes and Reset	Restarts the system after saving the changes made. Options available: Yes, No.
Discard Changes and Reset	Restarts the system without saving any changes. Options available: Yes, No.
Save Changes	Saves changes done so far to any of the setup options. Options available: Yes, No.
Discard Changes	Discards changes made and closes the BIOS setup. Options available: Yes, No.
Default Options	

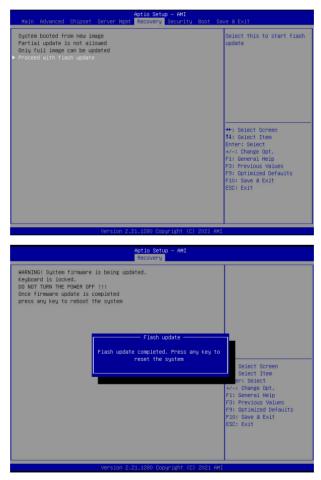
Parameter	Description
Restore Defaults	Loads the default settings for all BIOS setup parameters. Setup Defaults are quite demanding in terms of resources consumption. If you are using low-speed memory chips or other kinds of low-performance components and you choose to load these settings, the system might not function properly. Options available: Yes, No.
Save the User Default Values	Saves the changes made as the user default settings. Options available: Yes, No.
Restore the User Default Values	Loads the user default settings for all BIOS setup parameters. Options available: Yes, No.
Boot Device Priority	Press [Enter] to configure the device as the boot-up drive.
Launch EFI Shell	Attempts to Launch EFI Shell application (Shell.efi) from one of the available file system devices.

5-8 BIOS Recovery

The system has an embedded recovery technique. In the event that the BIOS becomes corrupt the boot block can be used to restore the BIOS to a working state. To restore your BIOS, please follow the instructions listed below:

Recovery Instruction:

- 1. Copy the XXX.rom to USB diskette.
- 2. Setting BIOS Recovery jump to enabled status.
- 3. Boot into BIOS recovery.
- 4. Run Proceed with flash update.
- 5. BIOS updated.



5-9 BIOS POST Beep code (AMI standard)

5-9-1 PEI Beep Codes

# of Beeps	Description
1	Memory not Installed.
1	Memory was installed twice (InstallPeiMemory routine in PEI Core called twice)
2	Recovery started
3	DXEIPL was not found
3	DXE Core Firmware Volume was not found
4	Recovery failed
4	S3 Resume failed
7	Reset PPI is not available

5-9-2 DXE Beep Codes

# of Beeps	Description
1	Invalid password
4	Some of the Architectural Protocols are not available
5	No Console Output Devices are found
5	No Console Input Devices are found
6	Flash update is failed
7	Reset protocol is not available
8	Platform PCI resource requirements cannot be met