GIGABYTE[™] G241-G40

HPC Server - 2U 4 x GPU Server

User Manual

Rev. 1.0

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Documentation Classifications

In order to assist in the use of this product, GIGABYTE provides the following types of documentation:

- User Manual: detailed information & steps about the installation, configuration and use of this product (e.g. motherboard, server barebones), covering hardware and BIOS.
- User Guide: detailed information about the installation & use of an add-on hardware or software component (e.g. BMC firmware, rail-kit) compatible with this product.
- Quick Installation Guide: a short guide with visual diagrams that you can reference easily for installation purposes of this product (e.g. motherboard, server barebones).

Please see the support section of the online product page to check the current availability of these documents.

For More Information

For related product specifications, the latest firmware and software, and other information please visit our website at http://www.gigabyte.com

For GIGABYTE distributors and resellers, additional sales & marketing materials are available from our reseller portal: http://reseller.b2b.gigabyte.com

For further technical assistance, please contact your GIGABYTE representative or visit https://esupport.gigabyte.com/ to create a new support ticket

For any general sales or marketing enquiries, you may also message GIGABYTE server directly by email: server.grp@gigabyte.com

Conventions

The following conventions are used in this user's guide:

E	NOTE! Gives bits and pieces of additional information related to the current topic.
	CAUTION! Gives precautionary measures to avoid possible hardware or software problems.
	WARNING! Alerts you to any damage that might result from doing or not doing specific actions.

Server Warnings and Cautions

Before installing a server, be sure that you understand the following warnings and cautions.

To reduce the risk of electric shock or damage to the equipment:

- Do not disable the power cord grounding plug. The grounding plug is an important safety feature.
- Plug the power cord into a grounded (earthed) electrical outlet that is easily accessible at all times.
- Unplug the power cord from the power supply to disconnect power to the equipment.
- Do not route the power cord where it can be walked on or pinched by items placed against it. Pay particular attention to the plug, electrical outlet, and the point where the cord extends from the server.

WARNING!

To reduce the risk of personal injury from hot surfaces, allow the drives and the internal system components to cool before touching them.

This server is equipped with high speed fans. Keep away from hazardous moving fan blades during servicing.



This equipment is not suitable for use in locations where children are likely to be present.

- Do not operate the server for long periods with the access panel open or removed. Operating the server in this manner results in improper airflow and improper cooling that can lead to thermal damage.
- Danger of explosion if battery is incorrectly replaced.
- Replace only with the same or equivalent type recommended by the manufacturer.
- · Dispose of used batteries according to the manufacturer's instructions.

Electrostatic Discharge (ESD)

ESD CAN DAMAGE DRIVES, BOARDS, AND OTHER PARTS. WE RECOMMEND THAT YOU PERFORM ALL PROCEDURES AT AN ESD WORKSTATION. IF ONE IS NOT AVAILABLE, PROVIDE SOME ESD PROTECTION BY WEARING AN ANTI-STATIC WRIST STRAP AT-TACHED TO CHASSIS GROUND -- ANY UNPAINTED METAL SURFACE -- ON YOUR SERVER WHEN HANDLING PARTS.

Always handle boards carefully. They can be extremely sensitive to ESD. Hold boards only by their edges without any component and pin touching. After removing a board from its protective wrapper or from the system, place the board component side up on a grounded, static free surface. Use a conductive foam pad if available but not the board wrapper. Do not slide board over any surface.

System power on/off: To remove power from system, you must remove the system from rack. Make sure the system is removed from the rack before opening the chassis, adding, or removing any non hot-plug components.

Hazardous conditions, devices and cables: Hazardous electrical conditions may be present on power, telephone, and communication cables. Turn off the system and discon-nect the cables attached to the system before servicing it. Otherwise, personal injury or equipment damage can result.

Electrostatic discharge (ESD) and ESD protection: ESD can damage drives, boards, and other parts. We recommend that you perform all procedures in this chapter only at an ESD workstation. If one is not available, provide some ESD protection by wearing an antistatic wrist strap attached to chassis ground (any unpainted metal surface on the server) when handling parts.

ESD and handling boards: Always handle boards carefully. They can be extremely sensi-tive to electrostatic discharge (ESD). Hold boards only by their edges. After removing a board from its protective wrapper or from the system, place the board component side up on a grounded, static free surface. Use a conductive foam pad if available but not the board wrapper. Do not slide board over any surface.

Installing or removing jumpers: A jumper is a small plastic encased conductor that slips over two jumper pins. Some jumpers have a small tab on top that can be gripped with fin-gertips or with a pair of fine needle nosed pliers. If the jumpers do not have such a tab, take care when using needle nosed pliers to remove or install a jumper; grip the narrow sides of the jumper with the pliers, never the wide sides. Gripping the wide sides can dam-age the contacts inside the jumper, causing intermittent problems with the function con-trolled by that jumper. Take care to grip with, but not squeeze, the pliers or other tool used to remove a jumper, or the pins on the board may bend or break.



Risk of explosion if battery is replaced incorrectly or with an incorrect type. Replace the battery only with the same or equivalent type recommended by the manufacturer. Dispose of used batteries according to the manufacturer's instructions.

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Chapter 1 Hardware Installation

1-1 Installation Precautions

The motherboard/system contain numerous delicate electronic circuits and components which can become damaged as a result of electrostatic discharge (ESD). Prior to installation, carefully read the service guide and follow these procedures:

- Prior to installation, do not remove or break motherboard S/N (Serial Number) sticker or warranty sticker provided by your dealer. These stickers are required for warranty validation.
- Always remove the AC power by unplugging the power cord from the power outlet before installing or removing the motherboard or other hardware components.
- When connecting hardware components to the internal connectors on the motherboard, make sure they are connected tightly and securely.
- When handling the motherboard, avoid touching any metal leads or connectors.
- It is best to wear an electrostatic discharge (ESD) wrist strap when handling electronic components such as a motherboard, CPU or memory. If you do not have an ESD wrist strap, keep your hands dry and first touch a metal object to eliminate static electricity.
- Prior to installing the motherboard, please have it on top of an antistatic pad or within an electrostatic shielding container.
- Before unplugging the power supply cable from the motherboard, make sure the power supply has been turned off.
- Before turning on the power, make sure the power supply voltage has been set according to the local voltage standard.
- Before using the product, please verify that all cables and power connectors of your hardware components are connected.
- To prevent damage to the motherboard, do not allow screws to come in contact with the motherboard circuit or its components.
- Make sure there are no leftover screws or metal components placed on the motherboard or within the computer casing.
- Do not place the computer system on an uneven surface.
- Do not place the computer system in a high-temperature environment.
- Turning on the computer power during the installation process can lead to damage to system components as well as physical harm to the user.
- If you are uncertain about any installation steps or have a problem related to the use of the product, please consult a certified computer technician.

1-2 Product Specifications

System	 ◆ 2U
Dimension	• 438mm (W) x 87.5mm (H) x 820mm (D)
CPU	 2nd Generation Intel® Xeon® Scalable Processors Intel® Xeon® Platinum Processor, Intel® Xeon® Gold Processor, Intel® Xeon® Silver Processor and Intel® Xeon® Bronze Processor System TDP up to 165W NOTE! If only 1 CPU is installed, some PCIe or memory functions might be
	unavailable
Socket	 2 x LGA 3647 Socket P
Chipset	Intel® C622 Express Chipset
Memory	 12 x DIMM slots DDR4 memory supported only 6-channel memory architecture RDIMM modules up to 64GB supported LRDIMM modules up to 128GB supported 1.2V modules: 2933/2666/2400 MHz
	NOTE! Memory frequency 2933MHz is for 2nd Generation Intel® Xeon® Scalable Processors only
	 2 x 10Gb/s BASE-T LAN ports (1 x Intel® X557-AT2) 2 x 1Gb/s LAN ports 1 x 10/100/1000 management LAN
Expansion Slot	 4 x PCIe x16 slots (Gen3 x16 bus) for GPUs (Slot_1 / Slot_3 / Slot_5 / Slot_7) Pre-installed CRSG02A in each slot Slot_2: 1 x PCIe x8 (Gen3 x8 bus) slot from CPU_0, shared with M.2 PCIe x4 bus Slot_6: 1 x PCIe x16 (Gen3 x16 bus) slot from CPU_1 2 x M.2 slots: M-key PCIe Gen3 x4 per slot Supports NGFF-22110/2280 cards From CPU_0
	 System is validated for population with a uniform GPU model Support is not provided for mixed GPU populations

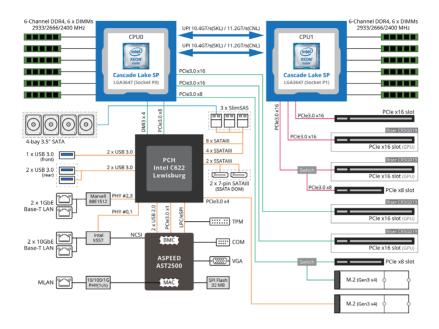
Video	 Integrated in Aspeed® AST2500 2D Video Graphic Adapter with PCIe bus interface 1920x1200@60Hz 32bpp, DDR4 SDRAM 			
 Storage 4 x 3.5" SATA/SAS hot-swappable HDD/SSD bays 2.5" HDD/SSD supported SAS card is required for SAS devices support 				
SATA	Supported			
Internal I/O	 1 x TPM header 1 x Front panel header 			
Front I/O	 1 x USB 3.0 1 x Power button with LED 1 x ID button with LED 1 x Reset button 1 x System status LED 1 x HDD activity LED 2 x LAN activity LEDs 			
Rear I/O • 2 x USB 3.0 • 1 x VGA • 4 x RJ45 • 1 x MLAN • 1 x ID button with LED				
Backplane I/O	Speed and bandwidth: SATA 6Gb/s, SAS 12Gb/s per port			
TPM • 1 x TPM header with LPC interface • Optional TPM2.0 kit: CTM000				

System	 Aspeed® AST2500 management controller
Management	AMI MegaRAC SP-X Solution web interface
Ŭ	·
	Dashboard
	JAVA Based Serial Over LAN
	 HTML5 KVM
	 Sensor Monitor (Voltage, RPM, Temperature, CPU Statusetc.)
	Sensor Reading History Data
	FRU Information
	SEL Log in Linear Storage / Circular Storage Policy
	 Hardware Inventory
	Fan Profile
	System Firewall
	Power Consumption
	Power Control
	LDAP / AD / RADIUS Support
	Backup & Restore Configuration
	Remote BIOS/BMC/CPLD Update
	Event Log Filter
	User Management
	Media Redirection Settings
	PAM Order Settings
	SSL Settings
	SMTP Settings
Power Supply	2 x 2000W redundant PSU
Power Supply	80 PLUS Platinum
	our corridinam
	AC Input:
	•
	 100-120V~/ 12A, 50-60Hz
	 - 180-240V~/ 10A, 50-60Hz
	DC output:
	 - 240Vdc/ 10A
	DC output:
	 - 1000W@100-120V, +12.2V/ 81.5A, +12Vsb/ 2.5A
	 - 1600W@180-199V, +12.2V/ 131A, +12Vsb/ 2.5A
	 - 1800W@200-220V, +12.2V/ 137A, +12Vsb/ 2.5A
	- 1000VV@200-220V, +12.2V/ 147.3A, +12VSD/ 2.3A

- 2000W@221-240V, +12V/ 163.5A, +12Vsb/ 2.5A

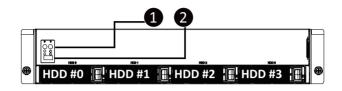
Environment Ambient	 Operating temperature: 10°C to 35°C Operating humidity: 8-80% (non-condensing) 		
Temperature			
	 Non-operating temperature: -40°C to 60°C 		
Relative	 Non-operating humidity: 20%-95% (non-condensing) 		
Humidity			
* We reserve the right to make any changes to the product specifications and product-related information without prior notice.			

1-3 System Block Diagram



Chapter 2 System Appearance

2-1 Front View



No.	Description	
1.	Front Panel LEDs and Buttons	
2.	Front USB 3.0 Port	

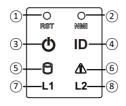


Please Go to Chapter 2-3 Front Panel LED and Buttons for detail description of function LEDs.

2-2 Rear View

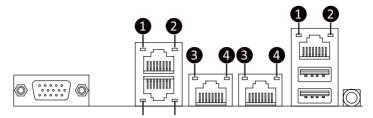
PSU:	2 PSU1 3 4 5 6 7		
No.	Description		
1.	VGA Port		
2.	GbE LAN Port x 2		
3.	10GbE LAN Port x 2		
4.	USB 3.0 Port x 2		
5.	10/100/10000 Server Management LAN Port		
6.	ID LED		
7.	Full-Height Half-Length PCIe Card Slot x 7		

2-3 Front Panel LED and Buttons



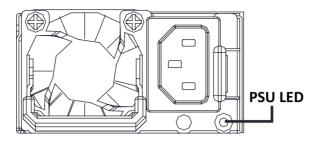
No.	Name	Color	Status	Description
1.	Reset Button			Press the button to reset the system.
2.	NMI button			Press the button server generates a NMI to the processor if the multiple-bit ECC errors occur, which effectively halt the server.
		Green	On	Indicates the system is powered on.
3.	Power button	Green	Blink	System is in ACPI S1 state (sleep mode).
	with LED	N/A	Off	 System is not powered on or in ACPI S5 state (power off) System is in ACPI S4 state (hibernate mode)
4.	ID Button			Press the button to activate system identification
		0	On	Indicates locating the HDD.
		Green	Blink	Indicates accessing the HDD.
5.	HDD Status	Amber	On	Indicates HDD error.
	LED	Green/ Amber	Blink	Indicates HDD rebuilding.
		N/A	Off	Indicates no HDD access or no HDD error.
		Green	On	Indicates system is operating normally.
			On	Indicates a critical condition, may include:
				-System fan failure
	System Status LED	Amber	Blink	-System temperature Indicates non-critical condition, may include:
6.				-Redundant power module failure
•••				-Temperature and voltage issue
				-Chassis intrusion
			Off	Indicates system is not ready, may include: -POST error
		N/A		-NMI error
				-Processor or terminator is missing
7/0	LAN 1/2	Green	On	Indicates a link between the system and the network or no access.
7/8.	Active/Link	Green	Blink	Indicates data trasmission or receiving is occuring.
	LEDs	N/A	Off	Indicates no data transmission or receiving is occuring.
				- 10 - System Appearance

2-4 Rear System LAN LEDs



No.	Name	Color	Status	Description
	1015	Yellow	On	1 Gbps data rate
1.	1GbE Speed LED	Green	On	100 Mbps data rate
		N/A	Off	10 Mbps data rate
	1GbE	Green	On	Link between system and network or no access
2.	Link/ Activity		Blink	Data transmission or receiving is occurring
LED		N/A	Off	No data transmission or receiving is occurring
	(0.0) =	Yellow	On	10 Gbps data rate
3.	10GbE Speed LED	Green	On	1000 Mbps data rate
		N/A	Off	100 Mbps data rate
10GbE 4. Link/ Activity LED		Green	On	Link between system and network or no access
			Blink	Data transmission or receiving is occurring
		N/A	Off	No data transmission or receiving is occurring

2-5 Power Supply Unit (PSU) LED



State	Description
OFF	Indicates no AC power to all power supplies
0.5Hz Blink GREEN	Indicates AC present/ only standby on/ Cold redundant mode
2Hz Blink GREEN	Indicates power supply firmware in updating mode
Amber	Indicates AC cord unplugged or AC power lost; with a second power supply in parallel still with AC input power
	Indicates power supply critical event causing shut down: failure, OCP, OVP, Fan Fail, UVP
0.5Hz Blink Amber	Indicates power supply warning events where the power supply continues to operate: high temp, high power, high current, slow fan

2-6 Hard Disk Drive LEDs

	ED1 ED2
--	------------

RAID SKU		LED1	Locate	HDD Fault	Rebuilding	HDD Access	HDD Present (No Access)
No RAID configuration (via HBA)	Disk LED (LED on Back Panel)	Green	ON(*1)	OFF		BLINK (*2)	OFF
		Amber	OFF	OFF		OFF	OFF
	Removed HDD Slot (LED on Back Panel)	Green	ON(*1)	OFF			
		Amber	OFF	OFF		-	
RAID configuration (via HW RAID Card or SW RAID Card)	Disk LED	Green	ON	OFF		BLINK (*2)	OFF
		Amber	OFF	ON	(Low Speed: 2 Hz)	OFF	OFF
	Removed HDD Slot	Green	ON(*1)	OFF	(*3)	-	
		Amber	OFF	ON	(*3)	-	

LED 2	HDD Present	No HDD	
Green	ON	OFF	

NOTE:

*1: Depends on HBA/Utility Spec.

*2: Blink cycle depends on HDD's activity signal.

*3: If HDD is pulled out during rebuilding, the disk status of this HDD is regarded as faulty.

Chapter 3 System Hardware Installation



Pre-installation Instructions

Computer components and electronic circuit boards can be damaged by discharges of static electricity. Working on computers that are still connected to a power supply can be extremely dangerous. Follow the simple guidelines below to avoid damage to your computer or injury to yourself.

- Always disconnect the computer from the power outlet whenever you are working inside the computer case.
- If possible, wear a grounded wrist strap when you are working inside the computer case. Alternatively, discharge any static electricity by touching the bare metal system of the computer case, or the bare metal body of any other grounded appliance.
- Hold electronic circuit boards by the edges only. Do not touch the components on the board unless it is necessary to do so. Do not flex or stress the circuit board.
- Leave all components inside the static-proof packaging until you are ready to use the component for the installation.

3-1 Removing Chassis Cover

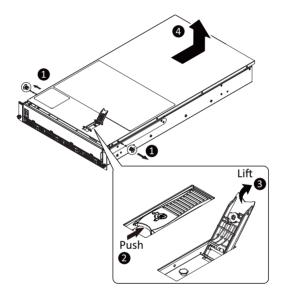


Before you remove or install the system cover

· Make sure the system is not turned on or connected to AC power.

Follow these instructions to remove the chassis cover:

- 1. Remove the two screws on the sides of the top cover.
- 2. Unlock the plastic handle and pull the grip handle to open the panel cover.
- Slide the cover cover to the rear of the system and then remove the cover in the direction indicated by the arrow.
- 4. To reinstall the chassis cover reverse steps 1-3.

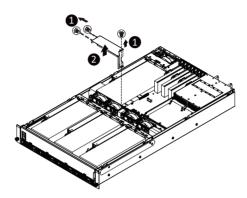


3-2 Removing and Installing the Fan Duct

Follow these instructions to remove/install the fan duct:

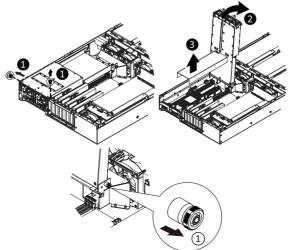
GPU Fan Duct:

- 1. Remove the screws securing the mental fanduct.
- 2. Lift up to remove the fan duct.
- 3. To install the fan duct, align the fan duct with the guiding groove. Push down the fan duct into chassis until its firmly seats



CPU Fan Duct:

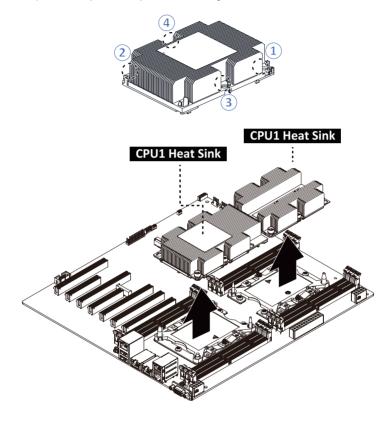
- 1. Remove the screws securing the mental fanduct.
- 2. Flip over the tray to 90 degree untill it clicks.
- 3. Lift up to remove the CPU fan duct.
- 4. To install the fan duct, align the fan duct with the guiding groove. Push down the fan duct into chassis until its firmly seats.
- 5. To re-install the tray, pull outward the thumbscrew.



3-3 Removing the Heat Sink

Follow these instructions to remove/install the fan duct:

- 1. Loosen the captive screws securing the heatsink in place in reverse order $(4 \rightarrow 3 \rightarrow 2 \rightarrow 1)$.
- 2. Lift and remove the heat sink from the system.
- 3. To reinstall the heat sink reverse steps 1-2 while ensuring that you tighten the captive screws in sequential order $(1\rightarrow 2\rightarrow 3\rightarrow 4)$ as seen in the image below.



3-4 Installing the CPU



Read the following guidelines before you begin to install the CPU:

- · Make sure that the motherboard supports the CPU.
- Always turn off the computer and unplug the power cord from the power outlet before installing the CPU to prevent hardware damage.
- Unplug all cables from the power outlets.
- · Disconnect all telecommunication cables from their ports.
- Place the system unit on a flat and stable surface.
- Open the system according to the instructions.



WARNING!

Failure to properly turn off the server before you start installing components may cause serious damage. Do not attempt the procedures described in the following sections unless you are a qualified service technician.



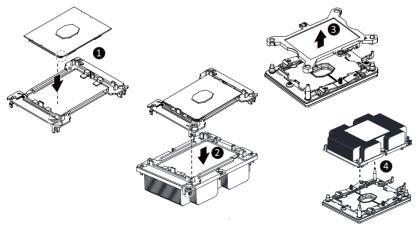
 When installing the heatsink to CPU, use PHILLIPS #2-Lobe driver to tighten 4 captive nuts in sequence as 1-4. The screw tightening torque: 14 ± 0.5 kgf-cm (30.0± 1.0 lbf-in).

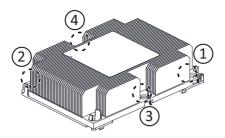
Follow these instructions to install the CPU:

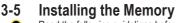
 Align the processor to the carrier so that the gold triangle on the processor aligns with the triangle on the carrier, and then install the processor into the carrier.

NOTE: Apply thermal compound evenly on the top of the CPU.

- Carefully flip the heatsink over. Align the carrier assembly so that the triangle on the carrier aligns with the triangle on the heatsink, and then install the carrier assembly onto the bottom of the heatsink.
- Remove the CPU socket cover.
 NOTE: Save and replace the CPU socket cover if the processor is removed from its socket.
- 4. Align the heatsink to the CPU socket using the guide pins and make sure the gold triangle is in the correct orientation. Then place the heatsink onto the top of the CPU socket.
- Secure the heatsink by tightening the screws in sequential order (1→2→3→4).
 NOTE: When removing the heatsink, loosen the screws in reverse order (4→3→2→1).





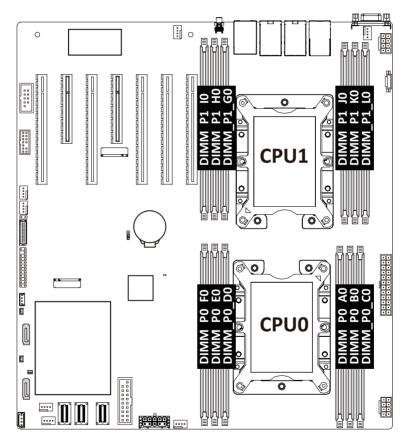


Read the following guidelines before you begin to install the memory:

- Make sure that the motherboard supports the memory. It is recommended that memory of the same capacity, brand, speed, and chips be used.
- Always turn off the computer and unplug the power cord from the power outlet before installing the memory to prevent hardware damage.
- Memory modules have a foolproof design. A memory module can be installed in only one direction. If you are unable to insert the memory, switch the direction.

3-5-1 Six Channel Memory Configuration

This motherboard provides 12 DDR4 memory sockets and supports Eight Channel Technology. After the memory is installed, the BIOS will automatically detect the specifications and capacity of the memory.



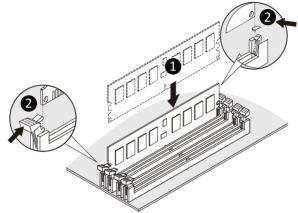
3-5-2 Installing a Memory

Before installing a memory module, make sure to turn off the computer and unplug the power cord from the power outlet to prevent damage to the memory module.

Be sure to install DDR4 DIMMs on this motherboard.

Follow these instructions to install the Memory:

- 1. Insert the DIMM memory module vertically into the DIMM slot, and push it down.
- 2. Close the plastic clip at both edges of the DIMM slots to lock the DIMM module.
- 3. Reverse the installation steps when you want to remove the DIMM module.



3-5-3 DIMM Population Table

Туре	Ranks Per DIMM and Data Width				Speed (MT/s); Voltage (V); Slots per Channel(SPC) and DIMM per Channel (DPC) 1 Slot Per Channel
					1DPC
		4Gb*	8Gb	16Gb	1.2V
RDIMM	SRx8	4GB	8GB	16GB	
RDIMM	SRx4	8GB	16GB	32GB	
RDIMM	DRx8	8GB	16GB	32GB	
RDIMM	DRx4	16GB	32GB	64GB	
RDIMM 3DS	QRx4	N/A	2H-64GB	2H-128GB	2933
RDIIVIIVI SDS	8Rx4	N/A	4H-128GB	4H-256GB	
LRDIMM	QRx4	32GB	64GB	128GB	
LRDIMM 3DS	QRx4	N/A	2H-64GB	2H-128GB	
	8Rx4	N/A	4H-128GB	4H-256GB	

3-6 Installing the PCI Expansion Card



Voltages can be present within the server whenever an AC power source is connected. This voltage is present even when the main power switch is in the off position. Ensure that the system is powered-down and all power sources have been disconnected from the server prior to installing a PCI card.

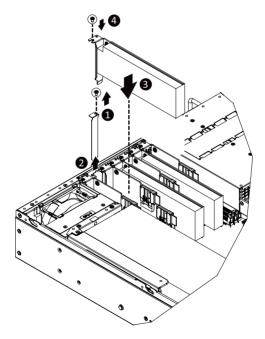
Failure to observe these warnings could result in personal injury or damage to equipment.



The PCI riser assembly does not include a riser card or any cabling as standard. To install a PCI card, a riser card must be installed.

Follow these instructions to PCI Expansion card:

- 1. Loosen the thumbscrew securing the riser bracket to the system.
- 2. Pull the riser bracket in the direction indicated to unlock the riser bracket.
- 3. Remove the screw securing the slot cover to the riser bracket.
- 4. Remove the slot covers from the riser bracket.
- Orient the PCI-E card with the riser guide slot and push in the direction of the arrow until the PCI-E card sits in the PCI card connector.
- 6. Secure the PCI-E card with the screw.
- 7. Reverse the steps 3 1 to install the riser bracket.



3-7 Installing the GPU Card



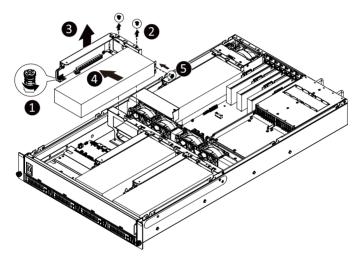
Read the following guidelines before you begin to install the GPU Card:

Voltages can be present within the server whenever an AC power source is connected. This voltage is present even when the main power switch is in the off position. Ensure that the system is powered down and all power sources have been disconnected from the server prior to installing a PCIe card. Make sure the system is not turned on or connected to AC power.

Failure to observe these warnings could result in personal injury or damage to the equipment.

Follow these instructions to install the GPU card:

- 1. Loosen the thumbnail screw securing the GPU card cage in place.
- Remove the fourscrews securing the GPU card slot bracket and covers in place and remove the PCIe card slot covers.
- 3. Insert the GPU card into the selected slot. Make sure the GPU card is properly seated.
- 4. Install thefour screws to secure the GPU card in place.



3-8 Installing the Hard Disk Drive

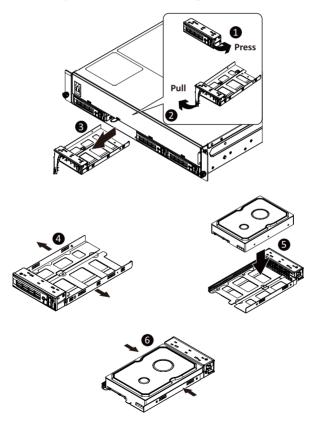


Read the following guidelines before you begin to install the Hard disk drive:

- Take note of the drive tray orientation before sliding it out.
- · The tray will not fit back into the bay if inserted incorrectly.
- Make sure that the HDD is connected to the HDD connector on the backplane.

Follow these instructions to install a 3.5" hard disk drive:

- 1. Press the release button.
- 2. Extend the locking lever.
- 3. Pull the locking lever in the direction indicated to remove the 3.5" HDD tray.
- 4. Pull the sides of the HDD tray in the direction indicated.
- 5. Slide the hard disk drive into the HDD tray.
- 6. Push the sides of the HDD tray back in the direction indicated to secure the hard disk drive in place.
- 7. Reinsert the HDD tray into the slot and close the locking lever.



3-9 Installing the M.2 Device and Heat Sink



WARNING:

Installation of the thermal pad over the M.2 device is required when installing an M.2 device. Lack of the thermal pad may result in system overheat and throttle the system performance.



CAUTION

The position of the stand-off screw will depend on the size of the M.2 device. The stand-off screw is pre-installed for 22110 cards as standard. Refer to the size of the M.2 device and change the position of the stand-off screw accordingly.



CAUTION

CPU TDP is limited to 165W if using M.2 device.

Follow these instructions to install the M.2 device and heat sink:

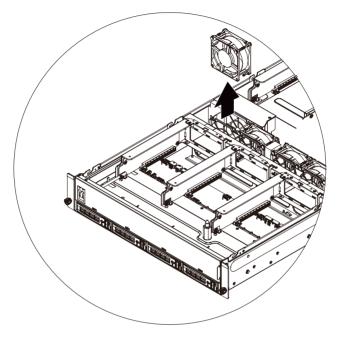
- 1. Insert the M.2 device into the M.2 connector.
- 2. Press down on the M.2 device.
- 3. Install the thermal pad of the M.2 device to the M.2 device.
- 4. Press down on the thermal pad.
- 5. Secure the M.2 device and its thermal pad to the motherboard with a single screw.
- 6. Reverse steps 1-4 to remove the M.2 device.



3-10 Replacing the Fan Assembly

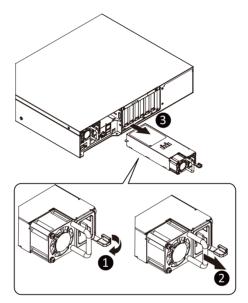
Follow these instructions to replace the fan assembly:

- 1. Lift up the fan assembly from the chassis.
- 2. Reverse the previous steps to install the replacement fan assembly.



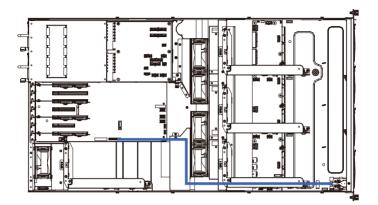
3-11 Replacing the Power Supply Follow these instructions to replace the power supply:

- 1. Press the retaining clip on the right side of the power supply along the direction of the arrow.
- 2. Pull up the power supply handle at the same time and pull out the power supply.
- 3. Insert the replacement power supply firmly into the chassis. Connect the AC power cord to the replacement power supply.

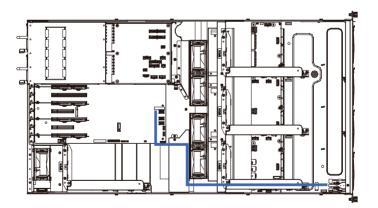


3-12 Cable Routing

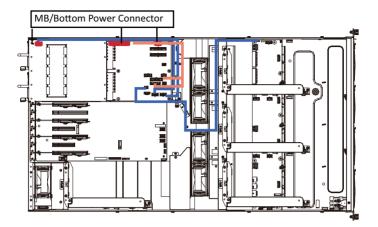
Front Panel LEDs and Buttons



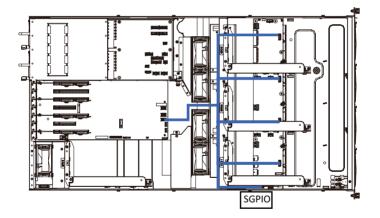
Front Panel USB 3.0 Port

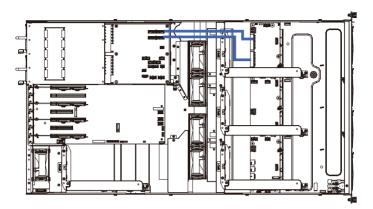


System Main Power

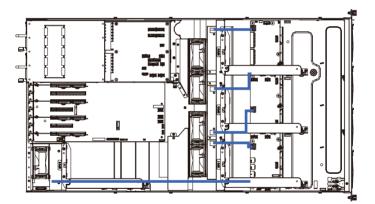


Onboard SATA

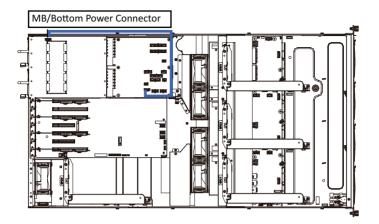




HDD Backplane Board Fan Power

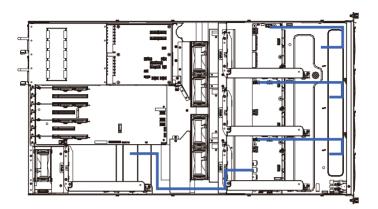


PMBus Signal

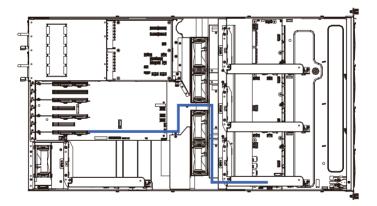


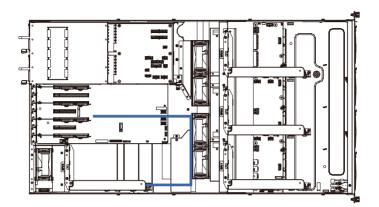
GPU Riser Card Power



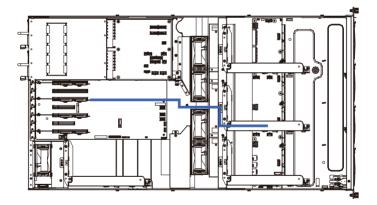


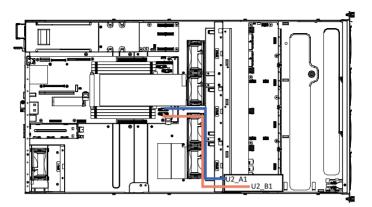
GPU Signal #0





GPU Signal #2

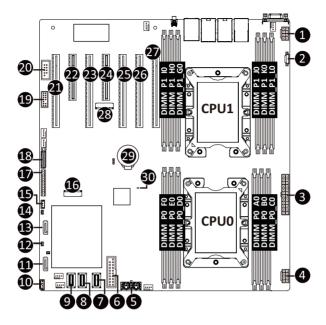




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Chapter 4 Motherboard Components

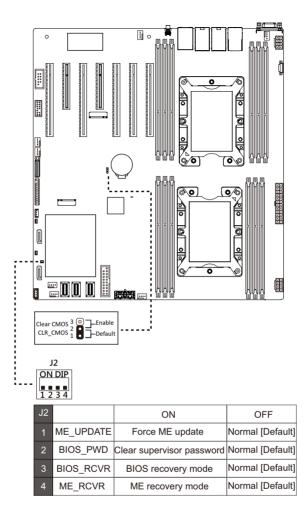
4-1 Motherboard Components



ltem	Description
1	2 x 4 Pin CPU1 Power Connector
2	PMBus Connector
3	2 x 13 Pin Power Connector
4	2 x 4 Pin CPU0 Power Connector
5	2 x 5 Pin GPU Power Connector
6	Front Panel USB 3.0 Connector
7	SlimLine SAS Connector (SATA1/SATA 6Gb/s)
8	SlimLine SAS Connector (SATA0/SATA 6Gb/s)
9	SlimLine SAS Connector (SSATA1/SATA 6Gb/s)
10	IPMB Connector
11	SATA 6Gb/s Connector (SSATA5/SATA DOM Supported)
12	SATA DOM Support Power Connector (for sSATA Connector 5)
13	SATA 6Gb/s Connector (SSATA4/SATA DOM Supported)
14	SATA DOM Support Power Connector (for sSATA Connector 4)
15	VROC Upgrade Module Connector
16	M.2 Connector (PCIe3 x4, Supports NGFF-22110)
17	Front Panel Connector

18	HDD Back Plane Board Connector
19	TPM Module Connector (LPC Interface)
20	Serial Port Cable Connector
21	PCIe x16 Slot #1 (From CPU0)
22	PCIe x8 Slot #2 (From CPU0 share PCIe x4 with M.2)
23	PCIe x16 Slot #3 (From CPU0)
24	PCIe x8 Slot #4 (From CPU1 share with Slot 5)
25	PCIe x16 Slot #5 (From CPU1)
26	PCIe x16 Slot #6 (From CPU1)
27	PCIe x16 Slot #7 (From CPU1)
28	M.2 Connector (PCIe3 x4, Supports NGFF-22110)
29	System Battery
30	BMC Firmware Readiness LED

4-2 Jumper Settings



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Chapter 5 BIOS Setup

BIOS (Basic Input and Output System) records hardware parameters of the system in the EFI on the motherboard. Its major functions include conducting the Power-On Self-Test (POST) during system startup, saving system parameters, loading the operating system etc. The BIOS includes a BIOS Setup program that allows the user to modify basic system configuration settings or to activate certain system features. When the power is turned off, the battery on the motherboard supplies the necessary power to the CMOS to keep the configuration values in the CMOS.

To access the BIOS Setup program, press the key during the POST when the power is turned on.



- BIOS flashing is potentially risky, if you do not encounter any problems when using the current BIOS version, it is recommended that you don't flash the BIOS. To flash the BIOS, do it with caution. Inadequate BIOS flashing may result in system malfunction.
- It is recommended that you not alter the default settings (unless you need to) to prevent system instability or other unexpected results. Inadequately altering the settings may result in system's failure to boot. If this occurs, try to clear the CMOS values and reset the board to default values. (Refer to the Exit section in this chapter or introductions of the battery/clearing CMOS jumper in Chapter 1 for how to clear the CMOS values.)

BIOS Setup Program Function Keys

<←><→>	Move the selection bar to select the screen
<↑><↓>	Move the selection bar to select an item
<+>	Increase the numeric value or make changes
<->	Decrease the numeric value or make changes
<enter></enter>	Execute command or enter the submenu
<esc></esc>	Main Menu: Exit the BIOS Setup program
	Submenus: Exit current submenu
<f1></f1>	Show descriptions of general help
<f3></f3>	Restore the previous BIOS settings for the current submenus
<f9></f9>	Load the Optimized BIOS default settings for the current submenus
<f10></f10>	Save all the changes and exit the BIOS Setup program

Main

This setup page includes all the items of the standard compatible BIOS.

Advanced

This setup page includes all the items of AMI BIOS special enhanced features.

(ex: Auto detect fan and temperature status, automatically configure hard disk parameters.)

Chipset

This setup page includes all the submenu options for configuring the functions of the Platform Controller Hub.

Server Management

Server additional features enabled/disabled setup menus.

Security

Change, set, or disable supervisor and user password. Configuration supervisor password allows you to restrict access to the system and BIOS Setup.

A supervisor password allows you to make changes in BIOS Setup.

A user password only allows you to view the BIOS settings but not to make changes.

Boot

This setup page provides items for configuration of the boot sequence.

Save & Exit

Save all the changes made in the BIOS Setup program to the CMOS and exit BIOS Setup. (Pressing <F10> can also carry out this task.)

Abandon all changes and the previous settings remain in effect. Pressing <Y> to the confirmation message will exit BIOS Setup. (Pressing <Esc> can also carry out this task.)

5-1 The Main Menu

Once you enter the BIOS Setup program, the Main Menu (as shown below) appears on the screen. Use arrow keys to move among the items and press <Enter> to accept or enter other sub-menu.

Main Menu Help

The on-screen description of a highlighted setup option is displayed on the bottom line of the Main Menu.

Submenu Help

While in a submenu, press <F1> to display a help screen (General Help) of function keys available for the menu. Press <Esc> to exit the help screen. Help for each item is in the Item Help block on the right side of the submenu.



When the system is not stable as usual, select the **Restore Defaults** item to set your system to its defaults.

The BIOS Setup menus described in this chapter are for reference only and may differ by BIOS version.

	Utility – Copyright (C) 2019 American Meg Server Mgmt Security Boot Save & Exit	atrends, Inc.
BIOS Information Project Name Project Version Build Date and Time	Model Name T06b 08/27/2019 09:57:03	5
BMC Information BMC Firmware Version	12.40.6	
Processor Information CPU 0 Brand String CPU 1 Brand String Max CPU Speed CPU Signature Processor Core	Intel(R) Xeon(R) Gold 522 Not Present 2200 MHz 50657 18	++: Select Screen
Microcode Patch Platform Information Processor PCH RC Revision	05000029 CLX L1 LBG QS/PRQ - 2 - S1 0591.D01	11: Select Item Enter: Select +/-: Change Opt. F1: General Help F3: Previous Values F9: Optimized Defaults F10: Save & Exit
Memory Information Total Memory Memory Frequency	16384 MB 2400 MHz	FID: SAVE & EXIT ESC: EXIT
Incode: A	20 1276 Conunight (C) 2019 American Magat	

Aptio Setup Utility Main Advanced Chipset Server N	<mark>– Copyright (C) 2019 American Ma</mark> Mgmt Security Boot Save & Exit	
Processor Information CPU 0 Brand String CPU 1 Brand String Max CPU Speed CPU Signature Processor Core Microcode Patch	Intel(R) Xeon(R) Gold 522 Not Present 2200 MHz 50657 18 05000029	
Platform Information Processor PCH RC Revision	CLX L1 LBG QS/PRQ - 2 - S1 0591.D01	
Memory Information		++: Select Screen
Total Memory Memory Frequency	16384 MB 2400 MHz	↑↓: Select Item Enter: Select
Onboard LAN Information LANI MAC Address LAN2 MAC Address LAN3 MAC Address LAN4 MAC Address	B4-2E-99-3B-7D-6C B4-2E-99-3B-7D-6D B4-2E-99-3B-7D-6E B4-2E-99-3B-7D-6F	H/-: Change Opt. F1: General Help F3: Previous Values F9: Optimized Defaults F10: Save & Exit ESC: Exit
System Date	[Thu 09/26/2019]	
System Time	[11:44:10]	

Parameter	Description
Falallelel	Description
BIOS Information	
Project Name	Displays the project name information.
Project Version	Displays version number of the BIOS setup utility.
Build Date and Time	Displays the date and time when the BIOS setup utility was created.
BMC Information ^(Note1)	
BMC Firmware Version ^(Note1)	Displays BMC firmware version information.
Processor Information	
CPU0 Brand String/ CPU1 Brand String/ Max CPU Speed / CPU Signature / Processor Core / Microcode Patch	Displays the technical specifications for the installed processor(s).
Memory Information	
Total Memory ^(Note2)	Displays the total memory size of the installed memory.
Memory Frequency(Note2)	Displays the frequency information of the installed memory.

(Note1) Functions available on selected models.

(Note2) This section will display capacity and frequency information of the memory that the customer has installed.

Parameter	Description
Onboard LAN Information	
LAN1 MAC Address ^(Note)	Displays LAN MAC address information.
LAN2 MAC Address (Note)	Displays LAN MAC address information.
LAN3 MAC Address ^(Note)	Displays LAN MAC address information.
LAN4 MAC Address (Note)	Displays LAN MAC address information.
System Date	Sets the date following the weekday-month-day-year format.
System Time	Sets the system time following the hour-minute-second format.

5-2 Advanced Menu

The Advanced Menu displays submenu options for configuring the function of various hardware components. Select a submenu item, then press <Enter> to access the related submenu screen.

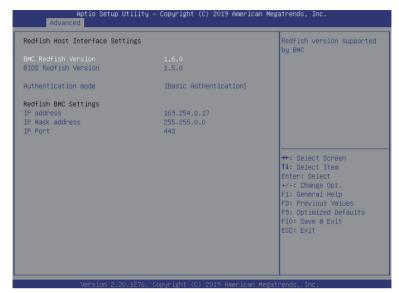
Main Advanced Chipset Server Mgmt Trusted Computing Redfish Host Interface Settings Serial Port Console Redirection SID Configuration PCI Subsystem Settings USB Configuration Post Report Configuration NVMe Configuration Chipset Configuration	Security boot Save & Exit	Trusted Computing Settings
 Network Stack Configuration ISSI Configuration Intel(R) Ethernet Connection X722 for WLAN Configuration (MAC:842E99387D6D) Intel(R) Ethernet Connection X722 for VLAN Configuration (MAC:842E99387D6D) Intel(R) Ethernet Connection X722 for VLAN Configuration (MAC:842E99387D6E) Intel(R) Ethernet Connection X722 for VLAN Configuration (MAC:842E99387D6F) VLAN Configuration (MAC:842E99387D6F) VLAN Configuration (MAC:84293373C5E) Driver Health 	106BASE-T - 84:28:99:3 16be - 84:28:99:38:7D:68	++: Select Screen f4: Select Item Enter: Select +/-: Change Opt. F1: General Help F3: Previous Values F9: Optimized Defaults F10: Save & Exit ESC: Exit

5-2-1 Trusted Computing

Configuration Security Device Support	Enables or Disables BIOS support for security
NO Security Device Found	device. O.S. will not show Security Device. TCG EFI protocol and INTIA interface will not be available.
	→+: Select Screen 11: Select Item
	Enter: Select +/-: Change Opt.
	F1: General Help F3: Previous Values F9: Optimized Defaults
	F10: Save & Exit ESC: Exit

Parameter	Description
Configuration	
Security Device Support	Enable/Disable the TPM support feature. Options available: Enable/Disable. Default setting is Enable .
Current Status Information	Displays current TPM status information.

5-2-2 Redfish Host Interface Settings



Parameter	Description
Redfish Host Interface Settings	
BMC Redfish Version	Displays the Redfish version supported by BMC.
BIOS Redfish Version	Displays the Redfish version supported by BIOS.
Authenticaion mode	Selects Authentication mode. Options available: Basic Authentication/Session Authentication. Default setting is Enable .
Redfish BMC Settings	
IP address	Enter IP address.
IP Mask address	Enter IP Mask address.
IP Port	Enter IP Port.

5-2-3 Serial Port Console Redirection

COM1 Console Redirection Console Redirection Settings	Console Redirection Enable or Disable.
Legacy Console Redirection Legacy Console Redirection Settings	
Serial Port for Out-of-Band Managemen Windows Emergency Management Services Console Redirection Console Redirection Settings	
	++: Select Screen f1: Select Item Enter: Select +/-: Change Opt. F1: General Help
	F3: Previous Values F9: Optimized Defaults F10: Save & Exit FSS: Fxit

Parameter	Description	
COM1 Console Redirection ^(Note))	Console redirection enables the users to manage the system from a remote location. Options available: Enabled/Disabled. Default setting is Disabled .	
COM1 Console Redirection Settings	 Press [Enter] to configure advanced items. Please note that this item is configurable when COM1 Console Redirection is set to Enabled. Terminal Type Selects a terminal type to be used for console redirection. Options available: VT100, VT100+, ANSI, VT-UTF8. Default setting is VT100+. Bits per second Selects the transfer rate for console redirection. Options available: 9600, 19200, 38400, 57600, 115200. Default setting is 115200. Data Bits Selects the number of data bits used for console redirection. Options available: 7/8. Default setting is 8. 	

(Note) Advanced items prompt when this item is defined. BIOS Setup - 57 -

Parameter	Description
COM1 Console Redirection Settings (continued)	 Parity A parity bit can be sent with the data bits to detect some transmission errors. Even: parity bit is 0 if the num of 1's in the data bits is even. Odd: parity bit is 0 if num of 1's in the data bits is odd. Mark: parity bit is always 1. Space: Parity bit is always 0. Mark and Space Parity do not allow for error detection. Options available: None, Even, Odd, Mark, Space. Default setting is None. Stop Bits Stop Bits Stop bits indicate the end of a serial data packet. (A start bit indicates the beginning). The standard setting is 1 stop bit. Communication with slow devices may require more than 1 stop bit. Options available: 1/2. Default setting is 1. Flow Control Flow Control can prevent data loss from buffer overflow. When sending data, if the receiving buffers are full, a 'stop' signal can be sent to stop the data flow. Once the buffers are empty, a 'start' signal can be sent to re-start the flow. Hardware flow control uses two wires to send start/stop signals. Options available: None, Hardware RTS/CTS. Default setting is None. VTI-UTF8 Combo Key Support Enable/Disable the VT-UTF8 Combo Key Support. Options available: Enabled/Disabled. Default setting is Disabled. Recorder Mode^{Notee)} When this mode enabled, only texts will be send. This is to capture Terminal data. Options available: Enabled/Disabled. Default setting is Disabled. Resolution 100x31^(Note) Enable/Disable extended terminal resolution. Options avai

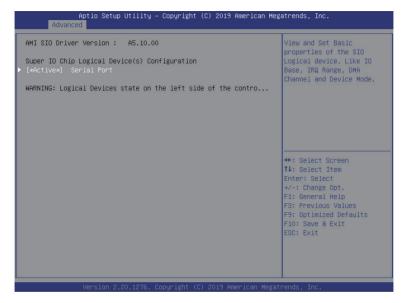
Default setting is VT100.

Parameter	Description
Legacy Console Redirection	
Legacy Console Redirection Settings	 Press [Enter] to configure advanced items. Redirection COM Port Selects a COM port for Legacy serial redirection. Default setting is COM1. Resolution Selects the number of rows and columns used in Console Redirection for legacy OS support. Options available: 80x24, 80x25. Default setting is 80x24. Redirect After POST When Bootloader is selected, then Legacy Console Redirection is disabled before booting to legacy OS. When Always Enable is selected, then Legacy Console Redirection is enabled for legacy OS. Options available: Always Enable, BootLoader. Default setting is Always Enable.
Serial Port for Out-of-Band Management / Windows Emergency Management Services (EMS) Console Redirection ^(Note)	EMS console redirection allows the user to configure Console Redirection Settings to support Out-of-Band Serial Port management. Options available: Enabled/Disabled. Default setting is Disabled .
Serial Port for Out-of-Band EMS Console Redirection Settings	 Press [Enter] to configure advanced items. Please note that this item is configurable when Serial Port for Out-of-Band Management EMS Console Redirection is set to Enabled. Out-of-Band Mgmt Port Microsoft Windows Emerency Management Service (EMS) allows for remote management of a Windows Server OS through a serial port. Default setting is COM1. Terminal Type Selects a terminal type to be used for console redirection. Options available: VT100, VT100+, ANSI, VT-UTF8. Default setting is VT100+. Bits per second Selects the transfer rate for console redirection. Options available: 9600, 19200, 38400, 57600, 115200. Default setting is 115200.

(Note) Advanced items prompt when this item is defined. BIOS Setup - 59 -

Parameter	Description	
Serial Port for Out-of-Band EMS Console Redirection Settings(continued)	 Flow Control Flow control can prevent data loss from buffer overflow. When sending data, if the receiving buffers are full, a 'stop' signal can be sent to stop the data flow. Once the buffers are empty, a 'start' signal can be sent to re-start the flow. Hardware flow control uses two wires to send start/stop signals. Options available: None, Hardware RTS/CTS, Software Xon/Xoff. Default setting is None. 	

5-2-4 SIO Configuration



Parameter	Description	
AMI SIO Driver Version	Displays the AMI SIO driver version information.	
Super IO Chip Logical Device(s) Configuration	Press [Enter] to configure advanced items.	
[*Active*] Serial Port	 Use This Device When set to Enabled allows you to configure the serial port settings. When set to Disabled, displays no configuration for the serial port. Options available: Enabled/Disabled. Default setting is Enabled. Current: Displays the serial port base I/O address and IRQ. Possible: Configures the serial port base I/O address and IRQ. Possible: Configures the serial port base I/O address and IRQ. Use Automatic Settings IO=3F8h; IRQ=3, 4, 5, 7, 9, 10, 11, 12; DMA; IO=3F8h; IRQ=3, 4, 5, 7, 9, 10, 11, 12; DMA; IO=3E8h; IRQ=3, 4, 5, 7, 9, 10, 11, 12; DMA; IO=2E8h; IRQ=3, 4, 5, 7, 9, 10, 11, 12; DMA; IO=2E8h; IRQ=3, 4, 5, 7, 9, 10, 11, 12; DMA; Default setting is Use Automatic Settings. 	

5-2-5 PCI Subsystem Settings

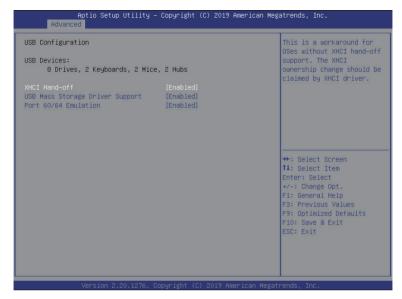
PCI Bus Driver Version	A5.01.18	Enable/Disable PCI-Express
PCI Express Slot #1 I/O ROM	[Enabled]	slot #1 I/O ROM.
PCI Express Slot #2 I/O ROM	[Enabled]	STOC HT 170 Koll.
PCI Express Slot #3 I/O ROM	[Enabled]	
PCI Express Slot #4 I/O ROM	[Enabled]	
PCI Express Slot #5 I/O ROM	[Enabled]	
PCI Express Slot #6 I/O ROM	[Enabled]	
PCI Express Slot #7 I/O ROM	[Enabled]	
PCI Express Slot #8 I/O ROM	[Enabled]	
Onboard LAN1 Controller	[Enabled]	
Onboard LAN1 I/O ROM	[Enabled]	
Onboard LAN2 I/O ROM	[Enabled]	
Onboard LANS I/O ROM	[Enabled]	
Onboard LAN4 I/O ROM	[Enabled]	++: Select Screen
		↑↓: Select Item
PCI Devices Common Settings:		Enter: Select
Above 4G Decoding	[Enabled]	+/-: Change Opt.
SR-IOV Support	[Enabled]	F1: General Help
		F3: Previous Values
		F9: Optimized Defaults
		F10: Save & Exit
		ESC: Exit

Parameter	Description
PCI Bus Driver Version	Displays the PCI Bus Driver version information.
PCI Express Slot # I/O ROM ^(Note1)	When enabled, this setting will initialize the device expansion ROM for the related PCI-E slot. Options available: Enabled/Disabled. Default setting is Enabled .
Onboard LAN1 Controller ^(Note2)	Enable/Disable the onboard LAN1 controller. Options available: Enabled/Disabled. Default setting is Enabled .
Onboard LAN1 / LAN2 / LAN3 / LAN4 I/O ROM ^(Note2)	Enable/Disable the onboard LAN1/ LAN2/ LAN3/ LAN4 devices, and initializes device expansion ROM. Options available: Enabled/Disabled. Default setting is Enabled .
PCI Devices Common Settings	
Above 4G Decoding	Enable/Disable memory mapped I/O to 4GB or greater address space (Above 4G Decoding). Options available: Enabled/Disabled. Default setting is Enabled .
SR-IOV Support	If the system has SR-IOV capable PCIe devices, this item Enable/Disable Single Root IO Virtualization Support. Options available: Enabled/Disabled. Default setting is Enabled .

(Note1) This section is dependent on the available PCIe Slot.

(Note2) This section is dependent on the available LAN controller. BIOS Setup - 62 -

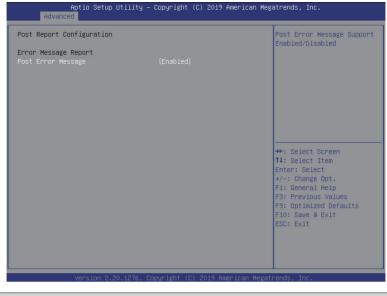
5-2-6 USB Configuration



Parameter	Description
USB Configuration	
USB Devices:	Displays the USB devices connected to the system.
XHCI Hand-off	Enable/Disable the XHCI (USB 3.0) Hand-off support. Options available: Enabled/Disabled. Default setting is Enabled .
XHCI Hand-off	Enable/Disable the XHCI (USB 3.0) Hand-off support. Options available: Enabled/Disabled. Default setting is Enabled .
USB Mass Storage Driver Support ^(Note)	Enable/Disable the USB Mass Storage Driver Support. Options available: Enabled/Disabled. Default setting is Enabled .
Port 60/64 Emulation	Enables the I/O port 60h/64h emulation support. This should be enabled for the complete USB Keyboard Legacy support for non- USB aware OS. Options available: Enabled/Disabled. Default setting is Enabled .

(Note) This item is present only if you attach USB devices.

5-2-7 Post Report Configuration



Parameter	Description	
Post Report Configuration		
Error Message Report		
Post Error Message	Enable/Disable the POST Error Message support. Options available: Enabled/Disabled. Default setting is Enabled .	

5-2-8 NVMe Configuration

NVMe Configuration	BIOS Build–In is default setting. Select Device
NVME OPROM Select No NVME Device Found	Itself, then this NVMe page will not display any NVMe device. Unless the device doesn't have OPROM it will show.
	++: Select Screen fl: Select Item Enter: Select +/-: Change Opt. F1: General Help F3: Previous Values F3: Optimized Defaults F10: Save & Exit ESC: Exit

Parameter	Description
NVMe Configuration	Displays the NVMe devices connected to the system
NVMe OPROM Select	Options available: BIOS Build-In/NVMe Device. Default setting is BIOS Build-In.

5-2-9 Chipset Configuration

Restore AC Power Loss Skip Above 4G Decoding for VGA P2P Bridge IO Size	[Last State] [Disabled] [0x1000]	Specify what state when power is re-applied after a power failure (G3 state
Chassis Opened Warning	[Disabled]	
		++: Select Screen T4: Select Item Enter: Select +/-: Change Opt. F1: General Help F3: Previous Values F9: Optimized Defaults F10: Save & Exit ESC: Exit

Parameter	Description
Restore on AC Power Loss ^(Note)	Defines the power state to resume to after a system shutdown that is due to an interruption in AC power. When set to Last State, the system will return to the active power state prior to shutdown. When set to Power Off, the system remains off after power shutdown. Options available: Last State, Power Off, Power On, Unspecified. The default setting depends on the BMC setting.
Skip Above 4G Decoding for VGA	Enable/Disable 64bit capable devices to be decoded in Skip Above 4G Address VGA Space. Options available: Enabled/Disabled. Default setting is Disabled .
P2P Bridge IO Size	Sets P2P Bridge IO aligned to the size. Options available: 0x100, 0x150, 0x1000. Default setting is 0x1000 .
Chassis Opened Warning	Enable/Disable the chassis intrusion alert function. Options available: Enabled, Disabled, Clear. Default setting is Disabled .

(Note) When the power policy is controlled by BMC, please wait for 15-20 seconds for BMC to save the last power state.

5-2-10 Network Stack Configuration

Network Stack Ipv4 PXE Support Ipv4 HTTP Support	[Enabled] [Enabled] [Disabled]	Enable∕Disable UEFI Network Stack
Ipv6 PXE Support Ipv6 HTTP Support IPSEC Certificate PXE boot wait time	[Disabled] [Disabled] [Enabled] 0	
Media detect count	1	
		++: Select Screen ↑↓: Select Item
		Enter: Select +/-: Change Opt. F1: General Help F3: Previous Values
		F9: Optimized Defaults F10: Save & Exit ESC: Exit

Parameter	Description
Network Stack	Enable/Disable the UEFI network stack. Options available: Enabled/Disabled. Default setting is Enabled .
Ipv4 PXE Support ^(Note)	Enable/Disable the Ipv4 PXE feature. Options available: Enabled/Disabled. Default setting is Enabled .
Ipv4 HTTP Support(Note)	Enable/Disable the Ipv4 HTTP feature. Options available: Enabled/Disabled. Default setting is Disabled .
Ipv6 PXE Support(Note)	Enable/Disable the Ipv6 PXE feature. Options available: Enabled/Disabled. Default setting is Disabled .
Ipv6 HTTP Support ^(Note)	Enable/Disable the Ipv6 HTTP feature. Options available: Enabled/Disabled. Default setting is Disabled .
IPSEC Certificate ^(Note)	Enable/Disable the IPSEC Certificate feature. Options available: Enabled/Disabled. Default setting is Enabled .
PXE boot wait time ^(Note)	Press the <+> / <-> keys to increase or decrease the desired values.
Media detect count ^(Note)	Press the <+> / <-> keys to increase or decrease the desired values.

5-2-11 iSCSI Configuration

		The worldwide unique name
Add an Attempt		of iSCSI Initiator. Only IQN format is accepted. Range is from 4 to 223
elete Attempts		Range IS Truin 4 tu 223
Change Attempt Order		
		++: Select Screen
		↑↓: Select Item Enter: Select
		+/-: Change Opt. F1: General Help
		F3: Previous Values F9: Optimized Defaults
		F10: Save & Exit ESC: Exit
		ESC. EXIL
Versi	on 2.20.1276. Copyright (C) 2019 A	merican Megatrends, Inc.
eter	Description	

Parameter	Description
iSCSI Initiator Name	
Add an Attempt	Press [Enter] to configure advanced items.
Delete Attempts	Press [Enter] to configure advanced items.
Change Attempt Order	Press [Enter] to configure advanced items.

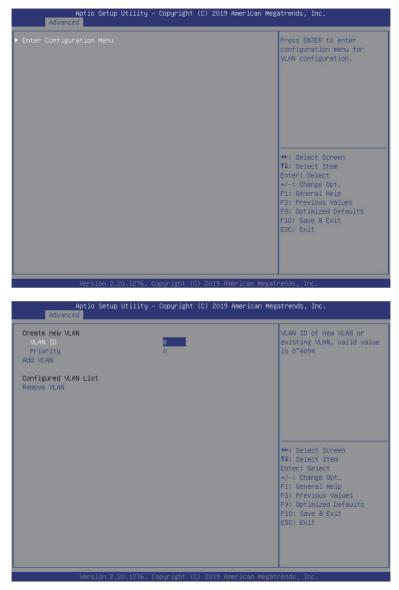
5-2-12 Intel(R) X722 Gigabit Network Connection

Aptio Setup Utility - Advanced	Copyright (C) 2019 American Meg	atrends, Inc.
 NIC Configuration Blink LEDs 	0	Click to configure the network device port.
UEFI Driver Adapter PBA Device Name Chip Type PCI Device ID PCI Address	0 Intel(R) 40GbE 2.1.14 304900-000 Intel(R) Ethernet Connect Intel X722 3702 19:00:00 [Disconnected]	
MAC Address Virtual MAC Address	B4:2E:99:3B:7D:6C 00:00:00:00:00:00	<pre>+*: Select Screen 11: Select Item Enter: Select +/-: Change Opt. F1: General Help F3: Previous Values F9: Optimized Defaults F10: Save & Exit ESC: Exit</pre>
Version 2.20.1276. Co	pyright (C) 2019 American Megatı	rends, Inc.

		Enables power on of the system via LAN. Note that
Link Speed Wake On LAN	[Auto Negotiated] [Enabled]	configuring Wake on LAN in the operating system does not change the value of this setting, but does override the behavior of Wake on LAN in DS controlled power states.
		++: Select Screen 14: Select Item Enter: Select +/-: Change Opt. F1: General Help F3: Previous Values F9: Optimized Defaults F10: Save & Exit ESO: Exit

Parameter	Description
NIC Configuration	 Press [Enter] to configure advanced items. Link Speed Allows for automatic link speed adjustment. Options available: Auto Negotiated, 10 Mbps Half, 10 Mbps Full, 100 Mbps Half, 100 Mbps Full. Default setting is Auto Negotiated. Wake On LAN Enables power on of the system via LAN. Note that configuring Wake on LAN in the operating system does not change the value of this setting, but does override the behavior of Wake on LAN in OS controlled power states. Options available: Enabled/Disabled. Default setting is Enabled.
Blink LEDs	Identifies the physical network port by blinking the associated LED. Press the numeric keys to adjust desired values.
UEFI Driver	Displays the technical specifications for the Network Interface Controller.
Adapter PBA	Displays the technical specifications for the Network Interface Controller.
Device Name	Displays the technical specifications for the Network Interface Controller.
Chip Type	Displays the technical specifications for the Network Interface Controller.
PCI Device ID	Displays the technical specifications for the Network Interface Controller.
PCI Address	Displays the technical specifications for the Network Interface Controller.
Link Status	Displays the technical specifications for the Network Interface Controller.
MAC Address	Displays the technical specifications for the Network Interface Controller.
Virtual MAC Address	Displays the technical specifications for the Network Interface Controller.

5-2-13 VLAN Configuration



Parameter	Description
Enter Configuration Menu	 Press [Enter] to configure advanced items. Create new VLAN VLAN ID Sets VLAN ID for a new VLAN or an existing VLAN. Press the <+> / <-> keys to increase or decrease the desired values. The valid range is from 0 to 4094. Priority Sets 802.1Q Priority for a new VLAN or an existing VLAN. Press the <+> / <-> keys to increase or decrease the desired values. The valid range is from 0 to 7. Add VLAN Press [Enter] to create a new VLAN or update an existing VLAN. Configured VLAN List Remove VLAN Press [Enter] to remove an existing VLAN.

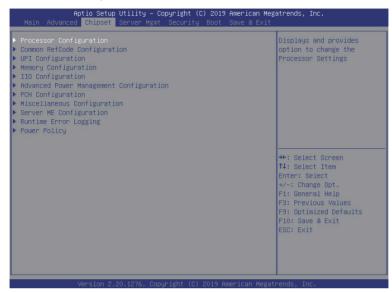
5-2-14 Driver Health

Intel(R) DCP4 Intel(R) 40G Intel(R) 40G Intel(R) 40G Intel(R) 40G	DE 2.1.14 DE 2.1.14 DE 2.1.14	Driver Healthy Healthy Healthy Healthy		Provides Health Status fo the Drivers/Controllers
				<pre>++: Select Screen 14: Select Item Enter: Select +/-: Change Opt. F1: General Help F3: Previous Values F9: Optimized Defaults F10: Save & Exit ESC: Exit</pre>

Parameter	Description
Driver Health	Displays driver health status of the devices/controllers if installed

5-3 Chipset Setup Menu

Chipset Setup menu displays submenu options for configuring the function of Platform Controller Hub(PCH). Select a submenu item, then press <Enter> to access the related submenu screen.



5-3-1 Processor Configuration

Chipset		
Processor Configuration		▲ Change Per-Socket Setting
Per–Socket Configuration		
Processor Socket	Socket 0 N/A	
Processor ID	00050657* N/A	
Processor Frequency	2.200GHz N/A	
Processor Max Ratio	16H N/A	
Processor Min Ratio	OAH N/A	
Microcode Revision	05000029 N/A	
L1 Cache RAM	64KB N/A	
L2 Cache RAM	1024KB N/A	
L3 Cache RAM	25344KB N/A	
Processor O Version	Intel(R) Xeon(R) Gold 5	
	220R CPU @ 2.20GHz	++: Select Screen
Processor 1 Version	Not Present	↑↓: Select Item
		Enter: Select
		+/-: Change Opt.
Hyper-Threading [ALL]	[Enable]	F1: General Help
Enable Intel(R) TXT	[Disable]	F3: Previous Values
VMX	[Enable]	F9: Optimized Defaults
Enable SMX	[Disable]	F10: Save & Exit
Hardware Prefetcher	[Enable]	ESC: Exit
L2 RFO Prefetch Disable	[Disable]	881
Adjacent Cache Prefetch	[Enable]	
DCU Streamer Prefetcher	[Enable]	- M
Ap <u>tio Setup</u> Util.	76. Copyright (C) 2019 American M ity – Copyright (C) 2019 American	
Ap <u>tio Setup</u> Util.		
Aptio Setup Util. Chipset Per-Socket Configuration		Megatrends, Inc.
Aptio Setup Util Chipset Per-Sacket Configuration Processor Socket	ity – Copyright (C) 2019 American	Megatrends, Inc.
Aptio Setup Util Chipset Per-Socket Configuration Processor Socket Processor ID	ity – Copyright (C) 2019 American Socket 0 N/A	Megatrends, Inc.
Aptio Setup Util. Chipset	ity – Copyright (C) 2019 American Socket 0 N/A 00050657* N/A	Megatrends, Inc.
Aptio Setup Util Chinset Per-Socket Configuration Processor Socket Processor To Processor Frequency Processor Min Ratio	ity – Copyright (C) 2019 American Socket 0 N/A 00050657* N/A 2.2006Hz N/A 16H N/A 0H N/A	Megatrends, Inc.
Aptio Setup Util Chipset Pro-Socket Configuration Processor ID Processor ID Processor Frequency Processor Max Ratio Processor Min Ratio Microcode Revision	ity - Copyright (C) 2019 American Socket 0 N/A 00050657* N/A 2.200GHz N/A 16H N/A 06H N/A 05000029 N/A	Megatrends, Inc.
Aptio Setup Util Chipset Pro-Socket Configuration Processor Socket Processor D Processor Prequency Processor Max Ratio Processor Min Ratio Microcode Revision L1 Cache RAM	ity - Copyright (C) 2019 American Socket 0 N/A 00050657* N/A 2.200GHz N/A 16H N/A 0HH N/A 06H N/A 64KB N/A	Megatrends, Inc.
Aptio Setup Util Chipset Processor Socket Processor ID Processor Frequency Processor Min Ratio Microcode Revision L1 Cache RAM	Ity - Copyright (C) 2019 American Sacket 0 N/A 00050657* N/A 2.2006Hz N/A 16H N/A 0H N/A 05000029 N/A 64KB N/A 1024KB N/A	Megatrends, Inc.
Aptio Setup Util Chipset Pro-Socket Configuration Processor IO Processor ID Processor Min Ratio Processor Min Ratio Microcode Revision L1 Cache RAM L2 Cache RAM	Ity - Copyright (C) 2019 American Socket 0 N/A 00050657* N/A 2.200GHz N/A 16H N/A 06H N/A 05000029 N/A 64KB N/A 1024KB N/A	Megatrends, Inc.
Aptio Setup Util Chipset Pro-Socket Configuration Processor IO Processor ID Processor Min Ratio Processor Min Ratio Microcode Revision L1 Cache RAM L2 Cache RAM	Ity - Copyright (C) 2019 American Socket 0 N/A 00050657* N/A 2.200GHz N/A 16H N/A 04H N/A 0500029 N/A 64KB N/A 1024KB N/A Intel(R) Xeon(R) Gold 5	Megatrends, Inc.
Aptio Setup Util Chinset Per-Socket Configuration Processor DO Processor Frequency Processor Min Ratio Microcode Revision 11 Cache RAM 22 Cache RAM 33 Cache RAM Processor 0 Version	Ity - Copyright (C) 2019 American Sacket 0 N/A 2.2006Hz N/A 2.2006Hz N/A 16H N/A 04H N/A 05000029 N/A 64KB N/A 1024KB N/A 2S344KB N/A Intel(R) Xeon(R) Gold 5 220R CFU @ 2.206Hz	Megatrends, Inc.
Aptio Setup Util Chinset Per-Socket Configuration Processor ID Processor Frequency Processor Min Ratio Microcode Revision L1 Cache RAM L2 Cache RAM L3 Cache RAM Processor 0 Version	Ity - Copyright (C) 2019 American Socket 0 N/A 00050657* N/A 2.200GHz N/A 16H N/A 04H N/A 0500029 N/A 64KB N/A 1024KB N/A Intel(R) Xeon(R) Gold 5	Megatrends, Inc.
Aptio Setup Util Chinset Per-Socket Configuration Processor ID Processor Frequency Processor Min Ratio Microcode Revision L1 Cache RAM L2 Cache RAM L3 Cache RAM Processor 0 Version	Ity - Copyright (C) 2019 American Sacket 0 N/A 2.2006Hz N/A 2.2006Hz N/A 16H N/A 04H N/A 05000029 N/A 64KB N/A 1024KB N/A 2S344KB N/A Intel(R) Xeon(R) Gold 5 220R CFU @ 2.206Hz	Megatrends, Inc.
Aptio Setup Util Chinset Processor Socket Processor ID Processor Frequency Processor Min Ratio Microcode Revision L1 Cache RAM L2 Cache RAM L3 Cache RAM Processor 0 Version Processor 1 Version	Ity - Copyright (C) 2019 American Socket 0 N/A 2.2006Hz N/A 16H N/A 0H N/A 05000029 N/A 64KB N/A 1024KB N/A 2S344KB N/A Intel(R) Xeon(R) Gold 5 220R CFU @ 2.206Hz Not Present	Megatrends, Inc. Enable/disable AES-NI support ++: Select Screen 14: Select Item
Aptio Setup Util Chipset Per-Socket Configuration Processor IO Processor ID Processor Min Ratio Microcode Revision L1 Cache RAM L2 Cache RAM J3 Cache RAM Processor 0 Version Processor 1 Version	Ity - Copyright (C) 2019 American Socket 0 N/A 00050657* N/A 2.200GHz N/A 16H N/A 05000029 N/A 64KB N/A 1024KB N/A 1024KB N/A 25344KB N/A Intel(R) Xeon(R) Gold 5 220R CPU @ 2.20GHz Not Present [Enable]	Megatrends, Inc. Enable/disable AES-NI support +*: Select Screen 14: Select Item Enter: Select
Aptio Setup Util Chipset Per-Sacket Configuration Processor ID Processor TD Processor Max Ratio Processor Max Ratio Processor Max Ratio Processor Max Ratio Li Cache RAM L2 Cache RAM L2 Cache RAM L2 Cache RAM Processor 0 Version Processor 1 Version Hyper-Threading [ALL] Enable Intel(R) TXT	tty - Copyright (C) 2019 American Socket 0 N/A 00050657* N/A 2.200GHZ N/A 16H N/A 0AH N/A 064K N/A 1024KB N/A 1024KB N/A 25344KB N/A 10124KB N/A 25344KB N/A 1024KB N/A 1024KB N/A [Enable] [Disable]	Megatrends, Inc. Enable/disable AES-NI support ++: Select Screen 11: Select Item Enter: Select +/:: Change Opt.
Aptio Setup Util Chinset Chinset Per-Socket Configuration Processor ID Processor Max Ratio Processor Min Ratio Afforder Revision 1 Cache RAM 2 Cache RAM 3 Cache RAM 9 Processor O Version Processor 1 Version Processor 1 Version	Ity - Copyright (C) 2019 American Socket 0 N/A 2.2006Hz N/A 16H N/A 0H N/A 0500029 N/A 1024KB N/A 1024KB N/A 2S344KB N/A Intel(R) Xeon(R) Gold 5 220R CFU @ 2.206Hz Not Present [Enable] [Disable] [Enable]	Megatrends, Inc. Enable/disable AES-NI support ++: Select Screen I4: Select Item Enter: Select +-: Change Opt. F1: General Heip
Aptio Setup Util Chipset Per-Socket Configuration Processor IO Processor ID Processor Mix Ratio Microcode Revision 11 Cache RAM L3 Cache RAM L3 Cache RAM Processor 0 Version Processor 1 Version Hyper-Threading [ALL] Enable Intel(R) TXT VMX Enable SMX	Ity - Copyright (C) 2019 American Socket 0 N/A 00050657* N/A 2.2006Hz N/A 16H N/A 05000029 N/A 64KB N/A 1024KB N/A 1024KB N/A Intel(R) Xeon(R) Gold 5 220R CPU 0 2.206Hz Not Present [Enable] [Disable] [Enable] [Disable]	Megatrends, Inc. Enable/disable AES-NI support **: Select Screen 14: Select Item Enter: Select +/-: Change Opt. F1: General Help F3: Previous Values
Aptio Setup Util Chipset Per-Socket Configuration Processor ID Processor TD Processor Min Ratio Processor Min Ratio Microcode Revision L1 Cache RAM L2 Cache RAM L2 Cache RAM L2 Cache RAM L3 Cache RAM Processor 0 Version Processor 1 Version Processor 1 Version Hyper-Threading [ALL] Enable Intel(R) TXT WMX Enable SMX Hardware Prefetcher	Ity - Copyright (C) 2019 American Socket 0 N/A 2.2006Hz N/A 16H N/A 0H N/A 0500029 N/A 1024KB N/A 1024KB N/A 2S344KB N/A Intel(R) Xeon(R) Gold 5 220R CFU @ 2.206Hz Not Present [Enable] [Disable] [Enable]	Megatrends, Inc. Enable/disable AES-NI support +*: Select Screen 14: Select Item Enter: Select +/-: Change Opt. F1: General Help
Aptio Setup Util Chinset Chinset Processor Socket Processor ID Processor Frequency Processor Min Ratio Microcode Revision L1 Cache RAM L2 Cache RAM L3 Cache RAM L3 Cache RAM Processor 0 Version Processor 1 Version Auger-Threading [ALL] Enable Intel(R) TXT WX Enable SMX Hardware Prefetcher L2 RFD Prefetch Disable	Ity - Copyright (C) 2019 American Socket 0 N/A 00050657* N/A 2.200GHZ N/A 16H N/A 06H N/A 06H N/A 06H N/A 1024KB N/A 1024KB N/A 1024KB N/A 1024KB N/A 1024KB N/A 25344KB N/A 1024KB N/A	Megatrends, Inc. Enable/disable AES-NI support **: Select Screen 14: Select Item Enter: Select +/-: Change Opt. F1: General Help F3: Previous Values F9: Optimized Defaults
Aptio Setup Util Chipset Per-Socket Configuration Processor Socket Processor Frequency	Ity - Copyright (C) 2019 American Socket 0 N/A 00050657* N/A 2.2006Hz N/A 16H N/A 00H N/A 05000029 N/A 1024KB N/A 1024KB N/A 25344KB N/A 25344KB N/A Intel(R) Xeon(R) Gold 5 220R CFU @ 2.206Hz Not Present [Enable] [Disable] [Enable] [Enable] [Disable] [Enable] [Disable] [Enable] [Disable]	Megatrends, Inc. Enable/disable AES-NI support +*: Select Screen 14: Select Item Enter: Select +/-: Change Opt. F1: General Help F3: Previous Values F1: Save & Exit
Aptio Setup Util Chipset Per-Socket Configuration Processor DO Processor ID Processor Min Ratio Microcode Revision 11 Cache RAM L3 Cache RAM L3 Cache RAM Processor 0 Version Processor 1 Version Processor 1 Version Hyper-Threading [ALL] Enable Intel(R) TXT VMX Enable Intel(R) TXT VMX Enable SMX Hardware Prefetcher L2 RFD Prefetch Disable Hdjacent Cache Prefetch	Ity - Copyright (C) 2019 American Socket 0 N/A 00050657* N/A 2.2006Hz N/A 16H N/A 06H N/A 05000029 N/A 64KB N/A 1024KB N/A 1024KB N/A 1024KB N/A Intel(R) Xeon(R) Gold 5 220R CPU 0 2.206Hz Not Present [Enable]	Megatrends, Inc. Enable/disable AES-NI support +*: Select Screen 14: Select Item Enter: Select +/-: Change Opt. F1: General Help F3: Previous Values F1: Save & Exit
Aptio Setup Util Chipset Per-Socket Configuration Processor ID Processor TD Processor Min Ratio Processor Min Ratio Microcode Revision L1 Cache RAM L2 Cache RAM L2 Cache RAM L3 Cache RAM Processor 0 Version Processor 1 Version Processor 1 Version Processor 1 Version Hyper-Threading [ALL] Enable Intel(R) TXT WHX Enable SMX Hardware Prefetcher L2 RFD Prefetch Disable Rdjacent Cache Prefetcher DUG Streamer Prefetcher	Ity - Copyright (C) 2019 American Socket 0 N/A 00050657* N/A 2.200GHZ N/A 16H N/A 06H N/A 06H N/A 0500029 N/A 64KB N/A 1024KB N/A 1024KB N/A 25344KB N/A 25344KB N/A 25344KB N/A 25344KB N/A 1024KB N/A 2506 CPU @ 2.20GHZ Not Present [Enable] [Enable] [Enable] [Enable] [Enable] [Enable] [Enable] [Enable] [Enable] [Enable] [Enable] [Enable] [Enable] [Enable] [Enable] [Enable] [Enable] [Enable]	Megatrends, Inc. Enable/disable AES-NI support +*: Select Screen 14: Select Item Enter: Select +/-: Change Opt. F1: General Help F3: Previous Values F1: Save & Exit

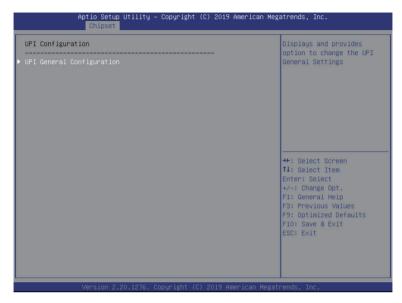
Parameter	Description
Processor Configuration	
Pre-Socket Configuration	 Press [Enter] to configure advanced items. CPU Socket 0/1 Configuration Press [Enter] to configure advanced items. Core Disable Bitmap(Hex) (for CPU socket 0/1) Number of Cores to enable. 0 means all cores. FFFFFFF means to disable all cores. The maximum value depends on the number of CPUs available. Press the numeric keys to adjust desired values.
Processor Socket / Processor ID / Processor Frequency / Processor Max Ratio / Processor Min Ratio / Microcode Revision / L1 Cache RAM / L2 Cache RAM / L3 Cache RAM / Processor 0 Version / Processor 1 Version	Displays the technical specifications for the installed processor(s).
Hyper-Threading [All]	The Hyper Threading Technology allows a single processor to execute two or more separate threads concurrently. When hyper-threading is enabled, multi-threaded software applications can execute their threads, thereby improving performance. Options available: Enable/Disable. Default setting is Enable .
Enable Intel(R) TXT	Enable/Disable the Intel Trusted Execution Technology support function. Options available: Enable/Disable. Default setting is Disable .
VMX (Vanderpool Technology)	Enable/Disable the Vanderpool Technology. This will take effect after rebooting the system. Options available: Enable/Disable. Default setting is Enable.
Enable SMX	Enable/Disable the Secure Mode Extensions (SMX) support function. Options available: Enable/Disable. Default setting is Disable .
Hardware Prefetcher	Select whether to enable the speculative prefetch unit of the processor. Options available: Enable/Disable. Default setting is Disable .
L2 RF0 Prefetch Disable	Options available: Enable/Disable. Default setting is Disable .
Adjacent Cache Prefetch	When enabled, cache lines are fetched in pairs. When disabled, only the required cache line is fetched. Options available: Enable/Disable. Default setting is Enable .
DCU Streamer Prefetcher	Prefetches the next L1 data line based upon multiple loads in same cache line. Options available: Enable/Disable. Default setting is Enable .
DCU IP Prefetcher	Prefetches the next L1 Data line based upon sequential load history. Options available: Enable/Disable. Default setting is Enable .
AES-NI	Enable/Disable the AES-NI (Intel Advanced Encryption Standard New Instructions) support function. Options available: Enable/Disable. Default setting is Enable .

5-3-2 Common RefCode Configuration

Common RefCode Configuration	 Select MMIO High Base
MMIO High Base MMIO High Granularity Size Isoc Mode Numa	
	++: Select Screen 14: Select Item Enter: Select +/-: Change Oot. F1: General Help F3: Previous Values F9: Optimized Defaults F10: Save & Exit ESC: Exit

Parameter	Description
Common RefCode Configuration	
MMIO High Base	Selects the MMIO High Base setting. Options available: 56T, 40T, 24T, 16T, 4T, 1T. Default setting is 56T .
MMIO High Granularity Size	Selects the allocation size used to assign mmioh resources. Total mmioh space can be up to 32xgranularity. Per stack mmioh resource assignments are multiples of the granularity where 1 unit per stack is the default allocation. Options available: 1G, 4G, 16G, 64G, 256G, 1024G. Default setting is 256G .
Isoc Mode	Enable/Disable the Isochronous support in order to meet the QoS requirements (Quality of Service). Options available: Auto, Enable, Disable. Default setting is Auto .
Numa (Non-Uniform Memory Access)	Enable/Disable Non-uniform Memory Access (NUMA) support to improve the system performance. Options available: Enable/Disable. Default setting is Enable .

5-3-3 UPI Configuration



Aptio Setup Util Chipset	ity – Copyright (C) 2019 Amer	ican Megatrends, Inc.
UPI General Configuration		UPI Status Help
 UPI Status Link Frequency Select SNC Stale AtoS LLC dead line alloc 	[Auto] [Disable] [Auto] [Enable]	++: Select Screen 14: Select Item Enter: Select
		+/-: Change Opt. F1: General Help F3: Previous Values F9: Optimized Defaults F10: Save & Exit ESC: Exit

Parameter	Description
UPI Configuration	
UPI General Configuration	 Press [Enter] to configure advanced items. UPI Status Press [Enter] to view the UPI status. Link Frequency Select Selects the UPI link frequency. Options available: 9.6GB/s, 10.4GB/s, Auto. Default setting is Auto. SNC Enable/Disable Sub NUMA Cluster function. Options available: Disable, Enable, Auto. Default setting is Disable. Stale AtoS Enable/Disable Stale A to S directory optimization. Options available: Disable, Enable, Auto. Default setting is Disable. LLC dead line alloc Enable/Disable fill dead lines in LLC. Options available: Disable, Enable, Auto. Default setting is Auto.

5-3-4 Memory Configuration

Integrated Memory Controller (1MC)	Enable - Enforces Plan Of Record restrictions for DDR4 frequency and voltage programming. Disable -
Enforce POR		Disables this feature. Auto – Sets it to the MRC
Memory Frequency	[Auto]	default setting; current
Enable ADR	[Enable]	default is Enable.
Legacy ADR Mode	[Disable]	
ADR Data Save Mode	[NVDIMMs]	
Erase-Arm NVDIMMs	[Enable]	
Restore NVDIMMs	[Enable]	
Interleave NVDIMMs	[Disable]	
Assert ADR on Reset	[Disable]	++: Select Screen
Assert ADR on S5	[Disable]	↑↓: Select Item
Memory Topology		Enter: Select
Memory Map Memory RAS Configuration		+/-: Change Opt. F1: General Help
Memory KHS Contiguration		F3: Previous Values
		F9: Optimized Defaults
		F10: Save & Exit
		ESC: Exit

Parameter	Description
Integrated Memory Controller (iMC)	
Enforce POR	When set to Enable, the system enforces Plan Of Record restrictions for DDR4 frequency and voltage programming. When set to Auto, the system sets it to the MRC default settings. Options available: Auto, POR, Disable. Default setting is Auto .
Memory Frequency	Configures the maximum memory frequency. Options available: Auto, 2133, 2400, 2666, 2933. Default setting is Auto .
Enable ADR	Enables the detecting and enabling of ADR (Asynchronous DRAM Refresh) function. Options available: Enable/Disable. Default setting is Enable .
Legacy ADR Mode	Enable/Disable the Legacy ADR Mode. Options available: Enable/Disable. Default setting is Disable .
ADR Data Save Mode	Specifies the Data Save Mode for ADR. Batterybacked or Type 01 NVDIMM. Options available: Disable, Batterybacked DIMMs, NVDIMMs. Default setting is NVDIMMs .
Erase-ARM NVDIMMs	Enable/Disable Erasing and Arming NVDIMMs. Options available: Enable/Disable. Default setting is Enable .
Restore NVDIMMs	Enable/Disable Automatic restoring of NVDIMMs. Options available: Enable/Disable. Default setting is Enable .

Parameter	Description	
Interleave NVDIMMs	Controls if NVDIMMs are interleaved together or not. Options available: Enable/Disable. Default setting is Disable .	
Assert ADR on Reset	Enable/Disable Assert ADR on Reset. Options available: Enable/Disable. Default setting is Disable .	
Assert ADR on S5	Enable/Disable Assert ADR on S5. Options available: Enable/Disable. Default setting is Disable .	
Memory Topology	Press [Enter] to view memory topology with DIMM population information.	
Memory Map	 Press [Enter] to configure advanced items. IMC Interleaving controls the interleaving between the Integrated Memory Controllers (IMCs). Options available: Auto, 1-way Interleave, 2-way Interleave. Default setting is Auto. 	
Memory RAS Configuration	 Press [Enter] to configure advanced items. RAS Type Displays the RAS type. Static Virtual Lockstep Mode Enable/Disable the Static Virtual Lockstep mode. Options available: Disable/Enable. Default setting is Disable. Mirror Mode Mirror Mode will set entire 1LM/2LM memory in system to be mirrored, consequently reducing the memory capacity by half. Enables the Mirror Mode will disable the XPT Prefetch. Options available: Disable/Enable Mirror Mode (1LM). Default setting is Disable. Memory Rank Sparing Enable/Disable Memory Rank Sparing. This feature is only available on 1LM. Options available: Disable/Enable. Default setting is Disable. Correctable Error Threshold Correctable Error Threshold (1-32767) used for sparing, tagging, and leaky bucket. Press the <+> / <-> keys to increase or decrease the desired values. SDDC Plus One Enable/Disable SDDC Plus One. Options available: Disable/Enable. Default setting is Disable. 	

5-3-5 IIO Configuration



Parameter	Description
IIO Configuration	
Intel® VT for Directed I/O (VT-d)	 Press [Enter] to configure advanced items. Intel® VT for Directed I/O (VT-d) Enable/Disable the Intel VT for Directed I/O (VT-d) support function by reporting the I/O device assignment to VMM through DMAR ACPI Tables. Options available: Enable/Disable. Default setting is Enable. ACS Control Enable: Programs ACS only to Chipset Pcie Root Ports Bridges. Disable: Programs ACS to all PCle bridges. Default setting is Enable. Interrupt Remapping Enable/Disable the interrupt remapping support function. Options available: Enable/Disable. Default setting is Enable. PassThrough DMA Enable/Disable the Non-Isoch VT_D Engine PassThrough DMA support function. Options available: Enable/Disable. Default setting is Enable.

Parameter	Description	
Intel® VT for Directed I/O (VT-d) (continued)	 Post Interrupt Enable/Disable VT_D posted interrupt. Options available: Enable/Disable. Default setting is Enable. Coherency Support (Non-Isoch) Enable/Disable Non-Isoch VT_D Engine Coherency support. Options available: Enable/Disable. Default setting is Enable. 	
Intel® VMD technology	 Press [Enter] to configure advanced items. Intel® VMD technology Intel® VMD Configuration Enable/Disable the Intel VMD support function. Options available: Enable/Disable. Default setting is Disable. 	
МСТР	Enable/Disable MCTP (Management Component Transport Protocol). Options available: Enable/Disable. Default setting is Disable .	

5-3-6 Advanced Power Management Configuration

Advanced Power Management Configuration	P State Control Configuration Sub Menu,
CPU P State Control Hardware FM State Control CPU C State Control Package C State Control CPU – Advanced FM Tuning	include Turbo, XE and etc
	++: Select Screen t1: Select Item Enter: Select +/-: Change Opt. F1: General Help F3: Previous Values F9: Optimized Defaults F10: Save & Exit ESC: Exit

Parameter	Description	
Advanced Power Management		
Configuration		
CPU P State Control	 Press [Enter] to configure advanced items. SpeedStep (Pstates) Conventional Intel SpeedStep Technology switches both voltage and frequency in tandem between high and low levels in response to processor load. Options available: Enable/Disable. Default setting is Enable. Turbo Mode When this item is enabled, the processor will automatically ramp 	
	 up the clock speed of 1-2 of its processing cores to improve its performance. When this item is disabled, the processor will not overclock any of its core. Options available: Enable/Disable. Default setting is Enable. 	

Parameter	Description
Hardware PM State Control	 Press [Enter] to configure advanced items. Hardware P-States When this item is disabled, the processor hardware chooses a P-state based on OS Request (Legacy P-States). In Native mode, the processor hardware chooses a P-state based on OS guidance. In Out of Band mode, the processor hardware autonomously chooses a P-state (with no OS guidance). Options available: Disable, Native Mode, Out of Band Mode, Native Mode with No Legacy Support. Default setting is Native Mode.
CPU C State Control	 Press [Enter] to configure advanced items. Autonomous Core C-State Enable/Disable the Autonomous Core C-State Control. Options available: Enable/Disable. Default setting is Disable. CPU C6 Report Allows you to determine whether to let the CPU enter C6 mode in system halt state. When enabled, the CPU core frequency and voltage will be reduced during system halt state to decrease power consumption. The C6 state is a more enhanced power-saving state than C1. Options available: Disable/Enable/Auto. Default setting is Auto. Enhanced Halt State (C1E)^(Note) Core C1E auto promotion control. Takes effect after reboot. Options available: Enable/Disable. Default setting is Enable.
Package C State Control	Configures the state for the C-State package limit. Options available: C0/C1 state, C2 state, C6(non Retention) state, C6(Retention) state, No Limit, Auto. Default setting is Auto .
CPU - Advanced PM Tuning	 Press [Enter] to configure advanced items. Energy Perf BIAS Enters the Energy Perf BIAS submenu. Power Performance Tuning^(Note) Tunes the Power Performance Configuration mode. When enabled, uses IA32_ENERGY_PERF_BIAS input from the core. When disabled, uses alternate performance BIAS input from ENERGY_PERF_BIAS_CONFIG. Options available: OS Controls EPB/BIOS Controls EPB. Default setting is OS Controls EPB. Energy_PERF_BIAS_CFG mode Selects the Energy Performance, Balanced Performance, Balanced Power, Power. Default setting is Balanced Performance. Please note that this item is configurable when Power Performance Tuning is set to BIOS Controls EPB.

5-3-7 PCH Configuration

PCH Configuration	SATA devices and settings
PCH SATA Configuration PCH SSATA Configuration	
	<pre>+*: Select Screen 14: Select Item Enter: Select +/-: Change Opt. F1: General Help F3: Previous Values F9: Optimized Defaults F10: Save & Exit ESC: Exit</pre>

Parameter	Description	
PCH Configuration		
PCH SATA Configuration	 Press [Enter] to configure advanced items. SATA Controller Enable/Disable SATA controller. Options available: Enable/Disable. Default setting is Enable. Configure SATA as Configures on chip SATA type. AHCI Mode: When set to AHCI, the SATA controller enables its AHCI functionality. Then the RAID function is disabled and cannot be access the RAID setup utility at boot time. RAID Mode: When set to RAID, the SATA controller enables both its RAID and AHCI functions. You will be allowed to access the RAID setup utility at boot time. Options available: AHCI/RAID. Default setting is AHCI. Alternate Device ID on RAID^(Note 1) Enable/Disable Alternate Device ID on RAID mode. Options available: Enable/Disable. Default setting is Disabled Please note that this option appears when HDD is in RAID Mode. SATA Port 0/1/2/3/4/5/6/7 The category identifies SATA hard drives that are installed in the computer. System will automatically detect HDD type. 	

Parameter	Description		
PCH SATA Configuration (continued)	 Port 0/1/2/3/4/5/6/7 Enable/Disable Port 0/1/2/3/4/5/6/7 device. Options available: Enable/Disable. Default setting is Enable. Hot Plug (for Port 0/1/2/3/4/5/6/7)^(Note 2) Enable/Disable HDD Hot-Plug function. Options available: Enable/Disable. Default setting is Disable. Spin Up Device (for Port 0/1/2/3/4/5/6/7)^(Note 2) On an edge detect from 0 to 1, the PCH starts a COM reset initialization to the device. Options available: Enable/Disable. Default setting is Disable. 		
PCH sSATA Configuration	 sSATA Controller Enable/Disable sSATA controller. Options available: Enable/Disable. Default setting is Enable. Configure sSATA as Configures on chip SATA type. AHCI Mode: When set to AHCI, the SATA controller enables its AHCI functionality. Then the RAID function is disabled and cannot be access the RAID setup utility at boot time. RAID Mode: When set to RAID, the SATA controller enables both its RAID and AHCI functions. You will be allowed to access the RAID setup utility at boot time. Options available: AHCI/RAID. Default setting is AHCI. Alternate Device ID on RAID^(Note 1) Enable/Disable Alternate Device ID on RAID mode. Options available: Enable/Disable. Default setting is Disabled. Please note that this option appears when HDD is in RAID Mode. SSATA Port 0/1/2/3/4/5 The category identifies sSATA hard drives that are installed in the computer. System will automatically detect HDD type. Port 0/1/2/3/4/5 Enable/Disable Port 0/1/2/3/4/5 device. Options available: Enable/Disable. Default setting is Enable. Hot Plug (for Port 0/1/2/3/4/5)^(Note 2) Enable/Disable HDD Hot-Plug function. Options available: Enable/Disable. Default setting is Disable. Spin Up Device (for Port 0/1/2/3/4/5)^(Note 2) On an edge detect from 0 to 1, the PCH starts a COM reset initialization to the device. Options available: Enable/Disable. Default setting is Disabled. 		

(Note 1) Only appears when HDD sets to **RAID** Mode. (Note 2) Only Supported when HDD is in **AHCI** or **RAID** Mode.

5-3-8 Miscellaneous Configuration

Miscellaneous Configu	ration	Select active Video type(Legacy only)
		++: Select Screen 14: Select Item Enter: Select +/-: Change Opt. F1: General Help F3: Previous Values F9: Optimized Defaults F10: Save & Exit ESC: Exit

Parameter	Description	
Miscellaneous Configuration		
	Selects the active video type.	
Active Video	Options available: Auto, Onboard Device, PCIE Device. Default setting is	
	Auto.	

5-3-9 Server ME Configuration

General ME Configuration Oper. Firmware Version 4.1.4.323 ME Firmware Status #1 0x002F0245 ME Firmware Status #2 0x08110266 Current State 0perational Error Code No Error Recovery Cause N/A PTT Support [Disable] Suppress PTT Commands [Disable] **: Select Screen 14: Select Item Enter: Select tem Fit General Help F3: Previous Values F3: Optimized Defaults F10: Save & Exit ESC: Exit

Parameter	Description	
General ME Configuration		
Oper. Firmware Version	Displays the operational firmware version.	
ME Firmware Status #1/#2	Displays ME Firmware status information.	
Current State (for ME Firmware)	Displays ME Firmware current status information.	
Error Code (for ME Firmware)	Displays ME Firmware status error code.	
Recovery Cause (for ME Firmware)	Displays ME Firmware recovery cause.	
PTT Support	Displays if the system supports the Intel® Platform Trust Technology.	
Suppress PTT Commands	Displays if the system supports to Bypass TPM2 commands submitting to PTT Firmware.	

5-3-10 Runtime Error Logging Settings

Runtime Error Logging		System Error Enable/Disable setup
System Errors S/W Error Injection Support Whea Settings Memory Error Enabling POIe Error Enabling	(Enable) (Disable)	options.
		<pre>++: Select Screen 11: Select Item Enter: Select +/-: Change Opt. F1: General Help F3: Previous Values F9: Optimized Defaults F10: Save & Exit ESC: Exit</pre>

Parameter	Description	
Runtime Error Logging		
System Errors	Enable/Disable system error logging function. Options available: Enable/Disable. Default setting is Enable .	
S/W Error Injection Support	Enable/Disable software injection error logging function. Options available: Enable/Disable. Default setting is Disable .	
Whea Settings	 Press [Enter] to configure advanced items. WHEA (Windows Hardware Error Architecture) Support Enable/Disable WHEA Support. Options available: Enable/Disable. Default setting is Enable. 	
Memory Error Enabling	 Press [Enter] to configure advanced items. Memory Error Enable/Disable Memory Error. Options available: Enable/Disable. Default setting is Enable. Memory Corrected Error Enable/Disable Memory Corrected Error. Options available: Enable/Disable. Default setting is Enable. Uncorrected Error disable Memory Enable/Disable Memory Enable/Disable Memory Enable/Disable the Memory that triggers Uncorrected Error. Options available: Enable/Disable. Default setting is Disable. 	

Parameter	Description
PCIe Error Enabling	 Press [Enter] to configure advanced items. Corrected Error Enables and escalates Correctable Errors to error pins. Options available: Enable/Disable. Default setting is Enable. Uncorrected Error Enables and escalates Uncorrectable/Recoverable Errors to error pins. Options available: Enable/Disable. Default setting is Enable. Fatal Error Enable Enables and escalates Fatal Errors to error pins. Options available: Enable/Disable. Default setting is Enable. Fatal Error Enable Enables and escalates Fatal Errors to error pins. Options available: Enable/Disable. Default setting is Enable. SERR Propagation Enable/Disable SERR propagation. Options available: Enable/Disable. Default setting is Enable. PERR Propagation Enable/Disable PERR propagation. Options available: Enable/Disable. Default setting is Enable.

5-3-11 Power Policy

Power Policy Quick Settings SpeedStep (Pstates) Turbo Mode CPU C6 report Enhanced Halt State (C1E) Package C State Hyper-Threading [ALL] Hardware Prefetcher Adjacent Cache Prefetch DCU Streamer Prefetcher Isoc Mode Intel® VT for Directed I/O (VT-d)		Select a Power Policy Quick Setting(The following items will be set based on the selected power policy)
Link Frequency Select	[Auto]	<pre>++: Select Screen t1: Select Item Enter: Select +/-: Change Opt. F1: General Help F3: Previous Values F3: Optimized Defaults F10: Save & Exit ESC: Exit</pre>

Parameter	Description
	Selects a Power Policy Quick Setting.
Power Policy Quick Settings	Options available: Standard, Best Performance, Energy Efficient, Turbo
	Lock.
	Conventional Intel SpeedStep Technology switches both voltage and
SpeedStep (Detetes)	frequency in tandem between high and low levels in response to processor
SpeedStep (Pstates)	load.
	Options available: Enable/Disable. Default setting is Enable.
	When this item is enabled, the processor will automatically ramp up the
Turbo Mode	clock speed of 1-2 of its processing cores to improve its performance.
	When this item is disabled, the processor will not overclock any of its core.
	Options available: Enable/Disable. Default setting is Enable.
CPU C6 report	Allows you to determine whether to let the CPU enter C6 mode in system
	halt state. When enabled, the CPU core frequency and voltage will be
	reduced during system halt state to decrease power consumption. The C6
	state is a more enhanced powersaving state than C1.
	Options available: Disable, Enable, Auto. Default setting is Auto.
Enhanced Halt State (C1E)(Note)	Core C1E auto promotion control. Takes effect after reboot.
Enhanced Halt State (C1E) ^(Note)	Options available: Enable/Disable. Default setting is Enable.

(Note) Advanced items prompt when this item is defined.

Parameter	Description
	Configures the state for the C-State package limit.
Package C State	Options available: C0/C1 state, C2 state, C6(non Retention) state,
	C6(Retention) state, No Limit, Auto. Default setting is Auto.
	The Hyper Threading Technology allows a single processor to execute
	two or more separate threads concurrently. When hyper-threading is
Hyper-Threading [ALL]	enabled, multi-threaded software applications can execute their threads,
	thereby improving performance.
	Options available: Enable/Disable. Default setting is Enable.
Hardware Prefetcher	Select whether to enable the speculative prefetch unit of the processor.
Hardware Preletcher	Options available: Enable/Disable. Default setting is Disable .
	When enabled, cache lines are fetched in pairs. When disabled, only the
Adjacent Cache Prefetch	required cache line is fetched.
	Options available: Enable/Disable. Default setting is Enable.
	Prefetches the next L1 data line based upon multiple loads in same cache
DCU Streamer Prefetcher	line.
	Options available: Enable/Disable. Default setting is Enable.
	Enable/Disable the Isochronous support in order to meet the QoS
Isoc Mode	requirements (Quality of Service).
	Options available: Auto, Enable, Disable. Default setting is Auto.
	Enable/Disable the Intel VT for Directed I/O (VT-d) support function by
Intel® VT for Directed I/O (VT-d)	reporting the I/O device assignment to VMM through DMAR ACPI Tables.
	Options available: Enable/Disable. Default setting is Enable .
Link Fraguanay Calaat	Selects the UPI link frequency.
Link Frequency Select	Options available: 9.6GB/s, 10.4GB/s, Auto. Default setting is Auto.

5-4 Server Management Menu

Aptio Setup Utilit Main Advanced Chipset <mark>Server</mark>	y – Copyright (C) 2019 Ameri Mgmt Security Boot Save	
FRB-2 Timer FRB-2 Timer timeout FRB-2 Timer Policy OS Watchdog Timer OS Wtd Timer Timeout OS Wtd Timer Policy Wait BMC Ready > System Event Log View FRU information BMC View TRU information BMC view Configuration	[Disabled] [6 minutes] [Do Nothing] [Disabled] [10 minutes] [Reset] [2 minutes]	Enable or Disable FRB-2 timer(POST timer)
▶ IPv6 BMC Network Configuration		<pre>++: Select Screen f↓: Select Item Enter: Select +/-: Change Opt. F1: General Help F3: Previous Values F9: Optimized Defaults F10: Save & Exit ESC: Exit</pre>

Parameter	Description
FRB-2 Timer	Enable/Disable FRB-2 timer (POST timer). Options available: Enabled/Disabled. Default setting is Disabled .
FRB-2 Timer timeout	Configure the FRB2 Timer timeout. Options available: 3 minutes, 4 minutes, 5 minutes, 6 minutes. Default setting is 6 minutes. Please note that this item is configurable when FRB-2 Timer is set to Enabled.
FRB-2 Timer Policy	Configure the FRB2 Timer policy. Options available: Do Nothing, Reset, Power Down, Power Cycle. Default setting is Do Nothing . Please note that this item is configurable when FRB-2 Timer is set to Enabled.
OS Watchdog Timer	Enable/Disable OS Watchdog Timer function. Options available: Enabled/Disabled. Default setting is Disabled .
OS Wtd Timer Timeout	Configure OS Watchdog Timer. Options available: 5 minutes, 10 minutes, 15 minutes, 20 minutes. Default setting is 5 minutes. Please note that this item is configurable when OS Watchdog Timer is set to Enabled.

Parameter	Description
OS Wtd Timer Policy	Configure OS Watchdog Timer Policy. Options available: Reset, Do Nothing, Power Down, Power Cycle. Default setting is Reset. Please note that this item is configurable when OS Watchdog Timer is set to Enabled.
Wait BMC Ready	POST wait BMC ready and reboot system. Options available: Disabled, 2 minutes, 4 minutes, 6 minutes. Default setting is 2 minutes .
System Event Log	Press [Enter] to configure advanced items.
View FRU Information	Press [Enter] to view the advanced items.
BMC VLAN Configuration	Press [Enter] to configure advanced items.
BMC network configuration	Press [Enter] to configure advanced items.
IPv6 BMC Network Configuration	Press [Enter] to configure advanced items.

5-4-1 System Event Log

Enabling/Disabling Options		Change this to enable or
		disable event logging for error/progress codes
Erasing Settings		during boot.
Enase SEL	[No]	
When SEL is Full	[Do Nothing]	
Custom EFI Logging Options		
Log EFI Status Codes	[Error code]	
		Enter: Select

Parameter	Description
Enabling / Disabling Options	
SEL Components	Change this item to enable or disable all features of System Event Logging during boot. Options available: Enabled/Disabled. Default setting is Enabled .
Erasing Settings	
Erase SEL	Choose options for erasing SEL. Options available: No/Yes, On next reset/Yes, On every reset. Default setting is No .
When SEL is Full	Choose options for reactions to a full SEL. Options available: Do Nothing, Erase Immediately, Delete Oldest Record. Default setting is Do Nothing .
Custom EFI Logging Options	
Log EFI Status Codes	Enable/Disable the logging of EFI Status Codes (if not already converted to legacy). Options available: Disabled, Both, Error code, Progress code. Default setting is Error code .

5-4-2 View FRU Information

The FRU page is a simple display page for basic system ID information, as well as System product information. Items on this window are non-configurable.

FRU Information		
System Manufacturer System Product Name System Serial Number Board Manufacturer Board Product Name Board Version Board Serial Number Chassis Manufacturer Chassis Version Chassis Serial Number	GIGABYTE Model Name 0100 0134557890123456789AB GIGABYTE Model Name 123455789AB S1963100016 GIGABYTE 01234567 01234567890123456789AB	++: Select Screen 14: Select Item Enter: Select +/-: Change Opt. F1: General Help F3: Previous Values F9: Optimized Defaults F10: Save & Exit ESC: Exit

5-4-3 BMC VLAN Configuration

BMC VLAN Configuration		VLAN ID of new VLAN or existing VLAN, valid valu
	0	is 0~4094, 0 is disable
BMC VLAN Priority	0	VLAN
		++: Select Screen t↓: Select Item
		Enter: Select +/-: Change Opt.
		F1: General Help
		F3: Previous Values F9: Optimized Defaults
		F10: Save & Exit
		ESC: Exit

Parameter	Description
BMC VLAN Configuration	
BMC VLAN ID	Select to configure BMC VLAN ID. The valid range is from 0 to 4094. When set to 0, BMC VLAN ID will be disabled.
BMC VLAN Priority	Select to configure BMC VLAN Priority. The valid range is from 0 to 7. When BMC VLAN ID is set to 0, BMC VLAN Priority will not be selected.

5-4-4 BMC Network Configuration

BMC network configuration		Select to configure LAN channel parameters
Lan channel 1		statically or
Configuration Address source Station IP address	[DynamicBmcDhcp] 10.1.27.65	dynamically(by BIOS or BMC). Unspecified option
Subnet mask	255.255.255.0	will not modify any BMC
Router IP address	10.1.27.253	network parameters during
Station MAC address	B4-2E-99-3B-7D-70	BIOS phase
		Enter: Select +/-: Change Opt. F1: General Help F3: Previous Values F3: Optimized Defaults F10: Save & Exit ESC: Exit

Parameter	Description
BMC network configuration	
Lan Channel 1	
Configuration Address source	Select to configure LAN channel parameters statically or dynamically (DHCP). Do nothing option will not modify any BMC network parameters during BIOS phase. Options available: Unspecified, Static, DynamicBmcDhcp. Default setting is DynamicBmcDhcp .
Station IP address	Displays IP Address information.
Subnet mask	Displays Subnet Mask information. Please note that the IP address must be in three digitals, for example, 192.168.000.001.
Router IP address	Displays the Router IP Address information.
Station MAC address	Displays the MAC Address information.
Real-time get BMC network address	Press [Enter] to synchronize the BMC network parameter values.

5-4-5 IPv6 BMC Network Configuration

IPv6 BMC Network Configuration	Enable/Disable IPv6 BMC LAN channel function.
IPv6 BMC Lan Channel 1: IPv6 BMC Lan Option IPv6 BMC Lan IP Address Source IPv6 BMC Lan IP Address/Prefix Len -> [::/0]	Disable option will not Disable option will not modify any BMC network during BIOS Phase
	++: Select Screen 14: Select Item Enter: Select +/-: Change Opt. F1: General Help F3: Previous Values F9: Optimized Defaults F10: Save & Exit ESC: Exit

Parameter	Description
IPv6 BMC Network Configuration	
IPv6 BMC Lan Channel 1	
IPv6 BMC Lan Option	Enable/Disable IPv6 BMC LAN channel function. When this item is disabled, the system will not modify any BMC network during BIOS phase. Options available: Unspecified, Enable, Disable. Default setting is Enable .
IPv6 BMC Lan IP Address Source	Select to configure LAN channel parameters statically or dynamically (by BIOS or BMC). Options available: Unspecified, Static, Dynamic-Obtained by BMC running DHCP. Default setting is Dynamic-Obtained by BMC running DHCP .
IPv6 BMC Lan IP Address/ Prefix Length	Check if the IPv6 BMC LAN IP address matches those displayed on the screen.

5-5 Security Menu

The Security menu allows you to safeguard and protect the system from unauthorized use by setting up access passwords.

	Utility – Copyright (C) 2019 Server Mgmt Security Boot	
Password Description		Set Administrator Password
If ONLY the Administrator' then this only limits acce only asked for when enter) If ONLY the User's passwor is a power on password and boot or enter Setup. In Se have Administrator rights. The password length must b in the following range: Winingum length	iss to Setup and is ng Setup. d is set, then this must be entered to tup the User will me 3	
Maximum length	20	↔: Select Screen
Administrator Password		↑↓: Select Item Enter: Select
0301 1 035001 0		+/-: Change Opt. F1: General Help
▶ Secure Boot		F3: Previous Values F9: Optimized Defaults F10: Save & Exit ESC: Exit
Vencion 2.2	0 1276 Conuright (C) 2019 (Amenican Medathende Inc

There are two types of passwords that you can set:

Administrator Password

Entering this password will allow the user to access and change all settings in the Setup Utility.

User Password

Entering this password will restrict a user's access to the Setup menus. To enable or disable this field, a Administrator Password must first be set. A user can only access and modify the System Time, System Date, and Set User Password fields.

Parameter	Description
Administrator Password	Press [Enter] to configure the administrator password.
User Password	Press [Enter] to configure the user password.
Secure Boot	Press [Enter] to configure advanced items.

5-5-1 Secure Boot

The Secure Boot submenu is applicable when your device is installed the Windows® 8 (or above) operating system.

Active if Secure Boot is Enabled, Platform Key(PK) is enrolled and the System i in User mode. The mode change requires platform reset
enrolled and the System i in User mode. The mode change requires
The mode change requires
platform reset
→+: Select Screen
†↓: Select Item
Enter: Select
+∕–: Change Opt.
F1: General Help
F3: Previous Values
F9: Optimized Defaults
F10: Save & Exit
F10: Save & Exit ESC: Exit

Parameter	Description
System Mode	Displays if the system is in User mode or Setup mode.
Secure Boot	Enable/ Disable the Secure Boot function. Options avaiable:Enabled/Disabled. Default setting is Disabled .
Secure Boot Mode ^(Note)	Secure Boot requires all the applications that are running during the booting process to be pre-signed with valid digital certificates. This way, the system knows all files being loaded before Windows loads to the login screen have not been tampered with. When set to Standard, it will automatically load the Secure Boot keys form the BIOS databases. When set to Custom, you can customize the Secure Boot settings and manually load its keys from the BIOS database. Options available: Standard/Custom. Default setting is Standard .
Restore Factory Keys	Installs all factory default keys. It will force the system in User Mode
Reset To Setup Mode	Installs the default keys when system is in setup mode.

(Note) Advanced items prompt when this item is set to Custom.

Parameter	Description
Key Management	 Press [Enter] to configure advanced items. Please note that this item is configurable when Secure Boot Mode is set to Custom. Factory Key Provision Allows to provision factory default Secure Boot keys when system is in Setup Mode. Options available: Enabled/Disabled. Default setting is Disabled. Restore Factory Keys Installs all factory default keys. It will force the system in User Mode. Options available: Yes/No. Enroll Efi Image Press [Enter] to enroll SHA256 hash of the binary into Authorized Signature Database (db). Restore DB defaults Restore DB variable to factory defaults. Secure Boot variable Displays the current status of the variables used for secure boot. Platform Key (PK) Displays the current status of the Platform Key (PK). Press [Enter] to configure a new PK. Options available: Set New. Key Exchange Keys (KEK) Displays the current status of the Key Exchange Key Database (KEK). Press [Enter] to configure a new KEK or load additional KEK from storage devices. Options available: Set New/Append. Authorized Signatures (DB) Displays the current status of the Authorized Signature Database. Press [Enter] to configure a new DB or load additional DB from storage devices. Options available: Set New/Append. Authorized Signatures (DBX) Displays the current status of the Forbidden Signature Database. Press [Enter] to configure a new DB or load additional DB from storage devices. Options available: Set New/Append. Authorized TimeStamps (DBT) Displays the current status of the Forbidden Signature Database. Press [Enter] to configure a new DBT or load additional DBT from storage devices. Options available: Set New/Appe

5-6 Boot Menu

The Boot menu allows you to set the drive priority during system boot-up. BIOS setup will display an error message if the legacy drive(s) specified is not bootable.

Boot Configuration Setup Prompt Timeout Bootup NumLock State Quiet Boot Setup Flash	1 [Dn] [Enabled]	Number of seconds to wait for setup activation key. 65535(0xFFFF) means indefinite waiting.
Boot mode select	(VEFI)	
FIXED BOOT ORDER Priorities Boot Option #1 Boot Option #2 Boot Option #3 Boot Option #4 Boot Option #5 > UEFI USB Drive BBS Priorities > UEFI NETWORK Drive BBS Priorities > UEFI Application Boot Priorities	[Hərd Disk] [CD/DVD] [USB Device:UEFI: USB F] [Network:UEFI: PXE IPv4] [UEFI AP:UEFI: Built-in]	++: Select Screen 11: Select Item Enter: Select +/-: Change Opt. F1: General Help F3: Optimized Defaults F3: Optimized Defaults F10: Save & Exit ESC: Exit

Parameter	Description
Boot Configuration	
Setup Prompt Timeout	Number of seconds to wait for setup activation key. 65535 (0xFFFF) means indefinite waiting. Press the numeric keys to input the desired values.
Bootup NumLock State	Enable/Disable the Bootup NumLock function. Options available: On/Off. Default setting is Off .
Quiet Boot	Enable/Disable showing the logo during POST. Options available: Enabled/Disabled. Default setting is Enabled .
Setup Flash	Press [Enter] to run setup flash.
Boot mode select	Selects the boot mode. Options available: LEGACY/UEFI. Default setting is UEFI .

Parameter	Description	
FIXED BOOT ORDER Priorities		
Boot Option #1 / #2 / #3 / #4 / #5	Press [Enter] to configure the boot priority. By default, the server searches for boot devices in the following sequence: 1. Hard drive. 2. CD-COM/DVD drive. 3. USB device. 4. Network. 5. UEFI.	
UEFI USB Drive BBS Priorities	Press [Enter] to configure the boot priority.	
UEFI Network Drive BBS Priorities	Press [Enter] to configure the boot priority.	
UEFI Application Boot Priorities	Press [Enter] to configure the boot priority.	

5-6-1 UEFI USB Drive BBS Priorities

The UEFI USB drive BBS priorities submenu allows you to specify the boot device priority from the available UEFI network drives during system boot-up. BIOS setup will display an error message if the legacy drive(s) specified is not bootable.

Aptio Setup Utility – Copyright (C) 2019 American Megatrends, Inc. Boot		
Boot Option #1	[UEFI: USB Flash Disk 1]	Sets the system boot order ++: Select Screen T1: Select Item Enter: Select +/-: Change Opt. F1: General Help F3: Previous Values F9: Optimized Defaults F10: Save & Exit ESC: Exit
Version 2.20.1276. C	opyright (C) 2019 American Megat	rends, Inc.

5-6-2 UEFI NETWORK Drive BBS Priorities

The UEFI network drive BBS priorities submenu allows you to specify the boot device priority from the available UEFI network drives during system boot-up. BIOS setup will display an error message if the legacy drive(s) specified is not bootable.

	Boot	
Boot Option #1 Boot Option #2 Boot Option #3 Boot Option #4	[UEFI: PXE IPV4 Intel(R] [UEFI: PXE IPv4 Intel(R] [UEFI: PXE IPv4 Intel(R] [UEFI: PXE IPv4 Intel(R]	Sets the system boot order
		★: Select Screen ↑1: Select Item Enter: Select +/-: Change Opt. F1: General Help F3: Previous Values F9: Optimized Defaults F10: Save & Exit ESC: Exit

5-6-3 UEFI Application Boot Priorities

The UEFI application boot priorities submenu allows you to specify the boot device priority from the available UEFI applications during system boot-up. BIOS setup will display an error message if the legacy drive(s) specified is not bootable.

Aptio Setup Utility -	Copyright (C) 2019 American Meg Boot	atrends, Inc.
Boot Option #1	[UEFI: Built-in EFI Sheli]	Sets the system boot order ++: Select Screen 14: Select Item Enter: Select +/-: Change Opt. F1: General Help F3: Previous Values F9: Optimized Defaults F10: Save & Exit ESC: Exit
Version 2.20.1276. C	opyright (C) 2019 American Megat	rends, Inc.

5-7 Save & Exit Menu

The Save & Exit menu displays the various options to quit from the BIOS setup. Highlight any of the exit options then press <Enter>.

Aptio Setup Utility – Copyright (C) 2019 American Meg Main Advanced Chipset Server Mgmt Security Boot <mark>Save & Exit</mark>	atrends, Inc.
Save Options Save Changes and Exit Discard Changes and Exit	Exit system setup after saving the changes.
Save Changes and Reset Discard Changes and Reset	
Save Changes Discard Changes	
Default Options Restore Defaults Save as User Defaults	
Restore User Defaults	↔: Select Screen †↓: Select Item
Boot Override UEFI: USB Flash Disk 1100, Partition 1 UEFI: PVE IPV4 Intel(R) Ethernet Connection X722 for 108BASE-T	Enter: Select +/-: Change Opt.
UEFI: PXE IPV4 Intel(R) Ethernet Connection X722 for 10GBASE-1 UEFI: PXE IPV4 Intel(R) Ethernet Connection X722 for 10GBASE-T UEFI: PXE IPV4 Intel(R) Ethernet Connection X722 for 1GbE UEFI: Built-in EFI Shell Launch EFI Shell from filesystem device	F1: General Help F3: Previous Values F9: Optimized Defaults F10: Save & Exit ESC: Exit
Launch Lrit Shail in dim Fridsgeven DEAIDS	

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Parameter	Description
Save Options	
Save Changes and Exit	Saves changes made and closes the BIOS setup. Options available: Yes/No.
Discard Changes and Exit	Discards changes made and exits the BIOS setup. Options available: Yes/No.
Save Changes and Reset	Restarts the system after saving the changes made. Options available: Yes/No.
Discard Changes and Reset	Restarts the system without saving any changes. Options available: Yes/No.
Save Changes	Saves changes made in the BIOS setup. Options available: Yes/No.
Discard Changes	Discards changes made and closes the BIOS setup. Options available: Yes/No.

Parameter	Description
Default Options	
Restore Defaults	Loads the default settings for all BIOS setup parameters. Setup Defaults are quite demanding in terms of resources consumption. If you are using low-speed memory chips or other kinds of low-performance components and you choose to load these settings, the system might not function properly. Options available: Yes/No.
Save as User Defaults	Saves the changes made as the user default settings. Options available: Yes/No.
Restore User Defaults	Loads the user default settings for all BIOS setup parameters. Options available: Yes/No.
Boot Override	Press [Enter] to configure the device as the boot-up drive.

5-8 BIOS POST Codes

5-8-1 AMI Standard - PEI

PEI_CORE_STARTED	0x10
PEI_CAR_CPU_INIT	0x11
PEI_CAR_NB_INIT	0x15
PEI_CAR_SB_INIT	0x19
PEI_MEMORY_SPD_READ	0x2B
PEI_MEMORY_PRESENCE_DETECT	0x2C
PEI_MEMORY_TIMING	0x2D
PEI_MEMORY_CONFIGURING	0x2E
PEI_MEMORY_INIT	0x2F
PEI_MEMORY_INSTALLED	0x31
PEI_CPU_INIT	0x32
PEI_CPU_CACHE_INIT	0x33
PEI_CPU_AP_INIT	0x34
PEI_CPU_BSP_SELECT	0x35
PEI_CPU_SMM_INIT	0x36
PEI_MEM_NB_INIT	0x37
PEI_MEM_SB_INIT	0x3B
PEI_DXE_IPL_STARTED	0x4F
DXE_CORE_STARTED	0x60
//Recovery	
PEI_RECOVERY_AUTO	0xF0
PEI_RECOVERY_USER	0xF1
PEI_RECOVERY_STARTED	0xF2
PEI_RECOVERY_CAPSULE_FOUND	0xF3
PEI_RECOVERY_CAPSULE_LOADED	0xF4
//S3	
PEI_S3_STARTED	0xE0
PEI_S3_BOOT_SCRIPT	0xE1
PEI_S3_VIDEO_REPOST	0xE2
PEI_S3_OS_WAKE	0xE3

5-8-2 AMI Standard - DXE

DXE_CORE_STARTED	0x60
DXE_NVRAM_INIT	0x61
DXE_SBRUN_INIT	0x62
DXE_CPU_INIT	0x63
DXE_NB_HB_INIT	0x68
DXE_NB_INIT	0x69
DXE_NB_SMM_INIT	0x6A

DXE SB INIT	0x70
DXE SB SMM INIT	0x71
DXE SB DEVICES INIT	0x72
DXE_ACPI_INIT	0x78
DXE_CSM_INIT	0x79
DXE_BDS_STARTED	0x90
DXE_BDS_CONNECT_DRIVERS	0x91
DXE_PCI_BUS_BEGIN	0x92
DXE_PCI_BUS_HPC_INIT	0x93
DXE_PCI_BUS_ENUM	0x94
DXE_PCI_BUS_REQUEST_RESOURCES	0x95
DXE_PCI_BUS_ASSIGN_RESOURCES	0x96
DXE_CON_OUT_CONNECT	0x97
DXE_CON_IN_CONNECT	0x98
DXE_SIO_INIT	0x99
DXE_USB_BEGIN	0x9A
DXE_USB_RESET	0x9B
DXE_USB_DETECT	0x9C
DXE_USB_ENABLE	0x9D
DXE_IDE_BEGIN	0xA0
DXE_IDE_RESET	0xA1
DXE_IDE_DETECT	0xA2
DXE_IDE_ENABLE	0xA3
DXE_SCSI_BEGIN	0xA4
DXE_SCSI_RESET	0xA5
DXE_SCSI_DETECT	0xA6
DXE_SCSI_ENABLE	0xA7
DXE_SETUP_VERIFYING_PASSWORD	0xA8
DXE_SETUP_START	0xA9
DXE_SETUP_INPUT_WAIT	0xAB
DXE_READY_TO_BOOT	0xAD
DXE_LEGACY_BOOT	0xAE
DXE_EXIT_BOOT_SERVICES	0xAF
RT_SET_VIRTUAL_ADDRESS_MAP_BEGIN	0xB0
RT_SET_VIRTUAL_ADDRESS_MAP_END	0xB1
DXE_LEGACY_OPROM_INIT	0xB2
DXE_RESET_SYSTEM	0xB3
DXE_USB_HOTPLUG	0xB4
DXE_PCI_BUS_HOTPLUG	0xB5
DXE_NVRAM_CLEANUP	0xB6
DXE_CONFIGURATION_RESET	0xB7

5-8-3 AMI Standard - ERROR

PEL_MEMORY_INVALID_TYPE 0x50 PEL_MEMORY_INVALID_SPEED 0x51 PEL_MEMORY_SPD_FAIL 0x51 PEL_MEMORY_INVALID_SIZE 0x52 PEL_MEMORY_INVALID_SIZE 0x53 PEL_MEMORY_NOT_DETECTED 0x53 PEL_MEMORY_NOT_DETECTED 0x53 PEL_MEMORY_ERROR 0x54 PEL_MEMORY_ERROR 0x56 PEL_OPU_INVALID_SPEED 0x56 PEL_CPU_SELF_TEST_FAILED 0x57 PEL_CPU_SELF_TEST_FAILED 0x58 PEL_OPU_NCOCODE_UPDATE_FAILED 0x59 PEL_OPU_INTERNAL_ERROR 0x54 PEL_OPU_INTERNAL_ERROR 0x53 PEL_OPU_INTERNAL_ERROR 0x58 PEL_OPU_INTERNAL_ERROR 0x58 PEL_OPU_REROR 0x58 PEL_OPU_REROR 0x58 PEL_RECOVERY_PI_NO_CAPSULE 0x68 PEL_RECOVERY_NO_CAPSULE 0x64 PEL_RECOVERY_INVALID_CAPSULE 0x6A PEL_S BOOT_SCRIPT_EROR 0x68 PEL_S BOOT_SCRIPT_EROR 0x68 PEL_S BOOT_SCRIPT_EROR 0x6A PEL		
PEI_MEMORY_SPD_FAIL 0x51 PEI_MEMORY_INVALID_SIZE 0x52 PEI_MEMORY_MISMATCH 0x52 PEI_MEMORY_NOT_DETECTED 0x53 PEI_MEMORY_NONE_USEFUL 0x53 PEI_MEMORY_NONE_USEFUL 0x53 PEI_MEMORY_NOT_INSTALLED 0x55 PEI_CPU_INVALID_TYPE 0x56 PEI_CPU_INVALID_SPEED 0x56 PEI_CPU_SELF_TEST_FAILED 0x58 PEI_CPU_SELF_TEST_FAILED 0x58 PEI_CPU_MICROCODE_UPDATE_FAILED 0x59 PEI_CPU_MICROCODE_UPDATE_FAILED 0x5A PEI_CPU_MICROCODE 0x5A PEI_CPU_BERGR 0x5A PEI_CPU_ERROR 0x5A PEI_RECOVERY_NO_CAPSULE 0x5A PEI_RECOVERY_NO_CAPSULE 0x5A PEI_RECOVERY_INVALID_CAPSULE 0x5A PEI_S3_RESUME_FAILED 0x5B PEI_S3_RESUME_FROR 0x5A PEI_S3_RESUME_FROR 0x5A PEI_S3_RESUME_FROR 0xEB DXE_COVERY_INVALID_CAPSULE 0x5A PEI_S3_RESUME_FROR 0xD1 DXE_ND_COR	PEI_MEMORY_INVALID_TYPE	0x50
PEI_MEMORY_INVALID_SIZE 0x52 PEI_MEMORY_MISMATCH 0x52 PEI_MEMORY_NOT_DETECTED 0x53 PEI_MEMORY_NOT_DETECTED 0x53 PEI_MEMORY_NOT_USTALLED 0x55 PEI_MEMORY_NOT_INSTALLED 0x55 PEI_CPU_INVALID_TYPE 0x56 PEI_CPU_INVALID_SPEED 0x56 PEI_CPU_MISMATCH 0x57 PEI_CPU_MISMATCH 0x58 PEI_CPU_MICROCODE_UPDATE_FAILED 0x58 PEI_CPU_MICROCODE 0x59 PEI_CPU_MICROCODE 0x59 PEI_CPU_NO_MICROCODE 0x58 PEI_CPU_RROR 0x5A PEI_RESET_NOT_AVAILABLE 0x58 //Recovery 0x58 PEI_RECOVERY_PPI_NOT_FOUND 0xF8 PEI_RECOVERY_NO_CAPSULE 0xF9 PEI_RECOVERY_INVALID_CAPSULE 0xFA //S3 Resume 0xE8 PEI_S3_DOT_SCRIPT_ERROR 0xE8 DXE_CPU_ERROR 0xEA PEI_S3_OS_WAKE_ERROR 0xD1 DXE_SB_ERROR 0xD1 DXE_NB_ERROR 0xD3		
PEI_MEMORY_MISMATCH 0x52 PEI_MEMORY_NOT_DETECTED 0x53 PEI_MEMORY_NONE_USEFUL 0x53 PEI_MEMORY_ERROR 0x54 PEI_MEMORY_ERROR 0x56 PEI_CPU_INVALID_TYPE 0x56 PEI_CPU_INVALID_SPEED 0x56 PEI_CPU_INVALID_SPEED 0x56 PEI_CPU_ACHE_ERROR 0x58 PEI_CPU_MICROCODE_UPDATE_FAILED 0x59 PEI_CPU_INTERNAL_ERROR 0x5A PEI_CPU_INTERNAL_ERROR 0x5A PEI_CPU_INTERNAL_ERROR 0x5A PEI_RESET_NOT_AVAILABLE 0x5B //Recovery 0x5A PEI_RECOVERY_PPI_NOT_FOUND 0xF8 PEI_RECOVERY_INVALID_CAPSULE 0xFA //S3 Resume PEI PEI_MEMORY_S3_RESUME_FAILED 0xE8 PEI_S3_OS_WAKE_ERROR 0xEA PEI_S3_OS_WAKE_ERROR 0xD1 DXE_CPU_ERROR 0xD2 DXE_CPU_ERROR 0xD2 DXE_CPU_ERROR 0xD3 DXE_CPU_ERROR 0xD4 DXE_NO_CON_ON_OSPACE 0xD3		0x51
PEI_MEMORY_NOT_DETECTED 0x53 PEI_MEMORY_NONE_USEFUL 0x53 PEI_MEMORY_NONE_USEFUL 0x53 PEI_MEMORY_EROR 0x54 PEI_MEMORY_EROR 0x55 PEI_CPU_INVALID_TYPE 0x56 PEI_CPU_INVALID_SPEED 0x56 PEI_CPU_MISMATCH 0x57 PEI_CPU_MISMATCH 0x57 PEI_CPU_MICROCODE_UPDATE_FAILED 0x58 PEI_CPU_INCACHE_ERROR 0x58 PEI_CPU_INCROCODE_UPDATE_FAILED 0x59 PEI_CPU_INTERNAL_ERROR 0x5A PEI_CPU_ERROR 0x5A PEI_RECOVERY_PPI_NOT_FOUND 0x58 //Recovery 0x58 PEI_RECOVERY_NO_CAPSULE 0x5A PEI_RECOVERY_NO_CAPSULE 0xF4 //S3 Resume 0xE8 PEI_S3_RESUME_FAILED 0xE8 PEI_S3_BOOT_SCRIPT_ERROR 0xEA PEI_S3_OS_WAKE_ERROR 0xEB DXE_CPU_ERROR 0xD1 DXE_CPU_ERROR 0xD3 DXE_CPU_ERROR 0xD3 DXE_CPU_ERROR 0xD3	PEI_MEMORY_INVALID_SIZE	0x52
PEI_MEMORY_NONE_USEFUL 0x53 PEI_MEMORY_ERROR 0x54 PEI_MEMORY_NOT_INSTALLED 0x55 PEI_CPU_INVALID_TYPE 0x56 PEI_CPU_INVALID_SPEED 0x56 PEI_CPU_SELF_TEST_FAILED 0x58 PEI_CPU_GACHE_ERROR 0x58 PEI_CPU_CACHE_ERROR 0x58 PEI_CPU_MICROCODE_UPDATE_FAILED 0x59 PEI_CPU_INO_MICROCODE 0x50 PEI_CPU_ERROR 0x5A PEI_CPU_ERROR 0x5A PEI_RESET_NOT_AVAILABLE 0x5B //Recovery PEI_RECOVERY_PPI_NOT_FOUND 0xF8 PEI_RECOVERY_NO_CAPSULE 0xF9 PEI_RECOVERY_INVALID_CAPSULE 0xF9 PEI_S3_RESUME_FAILED 0xE8 PEI_S3_RESUME_PPI_NOT_FOUND 0xE9 PEI_S3_RESUME_PRICOR 0xEA PEI_S3_RESUME_PRICOR 0xEA PEI_S3_RESUME_PRICOR 0xEA PEI_S3_ON_WAKE_ERROR 0xD1 DXE_NB_ERROR 0xD1 DXE_NB_ERROR 0xD2 DXE_NB_ERROR 0xD4 <td>PEI_MEMORY_MISMATCH</td> <td>0x52</td>	PEI_MEMORY_MISMATCH	0x52
PEI_MEMORY_ERROR 0x54 PEI_MEMORY_NOT_INSTALLED 0x55 PEI_CPU_INVALID_TYPE 0x56 PEI_CPU_INVALID_SPEED 0x56 PEI_CPU_MISMATCH 0x57 PEI_CPU_SELF_TEST_FAILED 0x58 PEI_CPU_CACHE_ERROR 0x58 PEI_CPU_MICROCODE_UPDATE_FAILED 0x59 PEI_CPU_INTERNAL_ERROR 0x5A PEI_CPU_ERROR 0x5A PEI_CPU_ERROR 0x5A PEI_RESET_NOT_AVAILABLE 0x5B //Recovery 0x58 PEI_RECOVERY_PI_NOT_FOUND 0x58 PEI_RECOVERY_NO_CAPSULE 0xFA //S3 Resume 0x5A PEI_RECOVERY_INVALID_CAPSULE 0xFA //S3 Resume 0xEA PEI_S3_DOT_SCRIPT_ERROR 0xEA PEI_S3_DOT_SCRIPT_ERROR 0xEA PEI_S3_DOT_SCRIPT_ERROR 0xD0 DXE_NB_ERROR 0xD1 DXE_SB_ERROR 0xD2 DXE_ARCH_PROTOCOL_NOT_AVAILABLE 0xD3 DXE_LEGACY_OPROM_NO_SPACE 0xD5 DXE_NO_CON_IN 0xD7 <td>PEI_MEMORY_NOT_DETECTED</td> <td>0x53</td>	PEI_MEMORY_NOT_DETECTED	0x53
PEI_MEMORY_NOT_INSTALLED 0x55 PEI_CPU_INVALID_TYPE 0x56 PEI_CPU_INVALID_SPEED 0x56 PEI_CPU_SELF_TEST_FAILED 0x57 PEI_CPU_SELF_TEST_FAILED 0x58 PEI_CPU_MICROCODE_UPDATE_FAILED 0x59 PEI_CPU_INCROCODE_UPDATE_FAILED 0x59 PEI_CPU_INTERNAL_ERROR 0x5A PEI_CPU_ERROR 0x5A PEI_CPU_ERROR 0x5A PEI_CPU_ERROR 0x5A PEI_RECOVERY_PPI_NOT_FOUND 0xF8 PEI_RECOVERY_NO_CAPSULE 0xF9 PEI_RECOVERY_INVALID_CAPSULE 0xFA //S3 Resume 0xE8 PEI_S3_RESUME_FAILED 0xE8 PEI_S3_RESUME_FAILED 0xE8 PEI_S3_RESUME_FAILED 0xE8 PEI_S3_OS_WAKE_ERROR 0xEA PEI_S3_OS_WAKE_ERROR 0xEA DXE_ND_ERROR 0xD1 DXE_ND_CON_ON_SPACE 0xD3 DXE_NO_CON_UT 0xD6 DXE_LEGACY_OPROM_NO_SPACE 0xD6 DXE_INVALID_PASSWORD 0xD8 DXE_LOON_ONIN 0xD7	PEI_MEMORY_NONE_USEFUL	0x53
PEI_CPU_INVALID_TYPE 0x56 PEI_CPU_INVALID_SPEED 0x56 PEI_CPU_MISMATCH 0x57 PEI_CPU_SELF_TEST_FAILED 0x58 PEI_CPU_CACHE_ERROR 0x59 PEI_CPU_INTCROCODE_UPDATE_FAILED 0x59 PEI_CPU_INTERNAL_ERROR 0x5A PEI_CPU_ERROR 0x5A PEI_CPU_ERROR 0x5A PEI_REST_NOT_AVAILABLE 0x5B //Recovery 0xF8 PEI_RECOVERY_PPI_NOT_FOUND 0xF8 PEI_RECOVERY_INVALID_CAPSULE 0xFA ///S3 Resume 0x53 PEI_S3_RESUME_FAILED 0xE8 PEI_S3_RESUME_FAILED 0xE8 PEI_S3_BOOT_SCRIPT_ERROR 0xEA PEI_S3_OS_WAKE_ERROR 0xEB DXE_OPU_ERROR 0xD1 DXE_SB_ERROR 0xD2 DXE_ARCH_PROTOCOL_NOT_AVAILABLE 0xD3 DXE_PCI_BUS_OUT_OF_RESOURCES 0xD4 DXE_LEGACY_OPROM_NO_SPACE 0xD5 DXE_NO_CON_OUT 0xD6 DXE_NO_CON_INN 0xD7 DXE_INVALID_PASSWORD 0xD8	PEI_MEMORY_ERROR	0x54
PEI_CPU_INVALID_SPEED 0x56 PEI_CPU_MISMATCH 0x57 PEI_CPU_SELF_TEST_FAILED 0x58 PEI_CPU_CACHE_ERROR 0x58 PEI_CPU_NO_MICROCODE 0x59 PEI_CPU_INTERNAL_ERROR 0x5A PEI_CPU_ERROR 0x5A PEI_CPU_ERROR 0x5A PEI_RESET_NOT_AVAILABLE 0x5B //Recovery 0xF8 PEI_RECOVERY_PPI_NOT_FOUND 0xF8 PEI_RECOVERY_NO_CAPSULE 0xFA //S3 Resume 0xE8 PEI_S3_RESUME_FAILED 0xE8 PEI_S3_RESUME_PPI_NOT_FOUND 0xE8 PEI_S3_RESUME_PRICAPSULE 0xEA PEI_S3_OS_WAKE_ERROR 0xEA PEI_S3_OS_WAKE_ERROR 0xEB DXE_CPU_ERROR 0xD0 DXE_NB_ERROR 0xD1 DXE_SB_ERROR 0xD2 DXE_ARCH_PROTOCOL_NOT_AVAILABLE 0xD3 DXE_LEGACY_OPROM_NO_SPACE 0xD5 DXE_LOCON_OUT 0xD6 DXE_NO_CON_UN 0xD7 DXE_LOCON_IN 0xD6 DXE_NO	PEI_MEMORY_NOT_INSTALLED	0x55
PEI_CPU_MISMATCH 0x57 PEI_CPU_SELF_TEST_FAILED 0x58 PEI_CPU_CACHE_ERROR 0x59 PEI_CPU_NO_MICROCODE 0x59 PEI_CPU_INTERNAL_ERROR 0x5A PEI_CPU_ERROR 0x5A PEI_CPU_ERROR 0x5A PEI_CPU_ERROR 0x5A PEI_RESET_NOT_AVAILABLE 0x5B //Recovery // PEI_RECOVERY_PPI_NOT_FOUND 0xF8 PEI_RECOVERY_INO_CAPSULE 0xFA //S3 Resume ////////////////////////////////////	PEI_CPU_INVALID_TYPE	0x56
PEI_CPU_SELF_TEST_FAILED 0x58 PEI_CPU_CACHE_ERROR 0x58 PEI_CPU_MICROCODE_UPDATE_FAILED 0x59 PEI_CPU_NO_MICROCODE 0x59 PEI_CPU_INTERNAL_ERROR 0x5A PEI_CPU_ERROR 0x5A PEI_RESET_NOT_AVAILABLE 0x5B //Recovery 0xF8 PEI_RECOVERY_PPI_NOT_FOUND 0xF8 PEI_RECOVERY_NO_CAPSULE 0xF9 PEI_RECOVERY_INVALID_CAPSULE 0xFA //S3 Resume 0xE8 PEI_S3_RESUME_FAILED 0xE8 PEI_S3_RESUME_FOLNOT_FOUND 0xE8 PEI_S3_RESUME_FOLNOT_FOUND 0xE9 PEI_S3_BOOT_SCRIPT_ERROR 0xEA PEI_S3_BOOT_SCRIPT_ERROR 0xEB DXE_CPU_ERROR 0xD0 DXE_NB_ERROR 0xD1 DXE_SB_ERROR 0xD2 DXE_NB_EROR 0xD3 DXE_PCI_BUS_OUT_OF_RESOURCES 0xD4 DXE_NO_CON_OUT 0xD6 DXE_NO_CON_OUT 0xD6 DXE_NO_CON_IN 0xD7 DXE_BOOT_OPTION_FAILED 0xDA	PEI_CPU_INVALID_SPEED	0x56
PEI_CPU_CACHE_ERROR0x58PEI_CPU_MICROCODE_UPDATE_FAILED0x59PEI_CPU_NO_MICROCODE0x59PEI_CPU_INTERNAL_ERROR0x5APEI_CPU_ERROR0x5APEI_RESET_NOT_AVAILABLE0x5B//Recovery//RecoveryPEI_RECOVERY_PPI_NOT_FOUND0xF8PEI_RECOVERY_INO_CAPSULE0xF9PEI_RECOVERY_INVALID_CAPSULE0xFA//S3 Resume//S3 ResumePEI_S3_RESUME_FAILED0xE8PEI_S3_RESUME_FAILED0xE8PEI_S3_RESUME_FAILED0xEAPEI_S3_BOOT_SCRIPT_ERROR0xEAPEI_S3_OS_WAKE_ERROR0xD0DXE_CPU_ERROR0xD1DXE_SB_ERROR0xD2DXE_ARCH_PROTOCOL_NOT_AVAILABLE0xD3DXE_LEGACY_OPROM_NO_SPACE0xD5DXE_NO_CON_UT0xD6DXE_NO_CON_IN0xD7DXE_NO_CON_IN0xD7DXE_BOOT_OPTION_LOAD_ERROR0xD8DXE_BOOT_OPTION_LOAD_ERROR0xD8DXE_BOOT_OPTION_FAILED0xDADXE_BOOT_OPTION_FAILED0xDADXE_ARCH_UPDATE_FAILED0xDA	PEI_CPU_MISMATCH	0x57
PEI_CPU_MICROCODE_UPDATE_FAILED0x59PEI_CPU_NO_MICROCODE0x53PEI_CPU_INTERNAL_ERROR0x5APEI_CPU_ERROR0x5APEI_RESET_NOT_AVAILABLE0x5B//Recovery0xF8PEI_RECOVERY_PPI_NOT_FOUND0xF8PEI_RECOVERY_NO_CAPSULE0xF9PEI_RECOVERY_INVALID_CAPSULE0xFA//S3 Resume0xE8PEI_S3_RESUME_FAILED0xE8PEI_S3_RESUME_FAILED0xE8PEI_S3_RESUME_FAILED0xE8DXE_CPU_ERROR0xD0DXE_CPU_ERROR0xD1DXE_SB_ERROR0xD1DXE_SB_ERROR0xD2DXE_ARCH_PROTOCOL_NOT_AVAILABLE0xD3DXE_LEGACY_OPROM_NO_SPACE0xD4DXE_NO_CON_UT0xD6DXE_NO_CON_IN0xD7DXE_NO_CON_IN0xD7DXE_BOOT_OPTION_LOAD_ERROR0xD8DXE_BOOT_OPTION_LOAD_ERROR0xD8DXE_BOOT_OPTION_FAILED0xDADXE_FLASH_UPDATE_FAILED0xDADXE_FLASH_UPDATE_FAILED0xDA	PEI_CPU_SELF_TEST_FAILED	0x58
PEI_CPU_NO_MICROCODE0x59PEI_CPU_INTERNAL_ERROR0x5APEI_CPU_ERROR0x5APEI_RESET_NOT_AVAILABLE0x5B//Recovery0xF8PEI_RECOVERY_PPI_NOT_FOUND0xF8PEI_RECOVERY_NO_CAPSULE0xF9PEI_RECOVERY_INVALID_CAPSULE0xFA//S3 Resume0xF8PEI_S3_RESUME_FAILED0xE8PEI_S3_RESUME_PPI_NOT_FOUND0xE9PEI_S3_RESUME_PPI_NOT_FOUND0xE9DEI_S3_OS_WAKE_ERROR0xEBDXE_CPU_ERROR0xD1DXE_SB_ERROR0xD2DXE_ARCH_PROTOCOL_NOT_AVAILABLE0xD3DXE_LEGACY_OPROM_NO_SPACE0xD5DXE_NO_CON_OUT0xD6DXE_NO_CON_IN0xD7DXE_NO_CON_IN0xD7DXE_BOOT_OPTION_LOAD_ERROR0xD8DXE_BOOT_OPTION_LOAD_ERROR0xD8DXE_BOOT_OPTION_FAILED0xD8DXE_BOOT_OPTION_FAILED0xD8DXE_BOOT_OPTION_FAILED0xD8	PEI_CPU_CACHE_ERROR	0x58
PEI_CPU_INTERNAL_ERROR0x5APEI_CPU_ERROR0x5APEI_RESET_NOT_AVAILABLE0x5B//Recovery0xF8PEI_RECOVERY_PPI_NOT_FOUND0xF8PEI_RECOVERY_NO_CAPSULE0xF9PEI_RECOVERY_INVALID_CAPSULE0xFA//S3 Resume0xE8PEI_MEMORY_S3_RESUME_FAILED0xE8PEI_S3_RESUME_PPI_NOT_FOUND0xE9PEI_S3_BOOT_SCRIPT_ERROR0xEADXE_CPU_ERROR0xD0DXE_NB_ERROR0xD1DXE_SB_ERROR0xD2DXE_ARCH_PROTOCOL_NOT_AVAILABLE0xD3DXE_PCI_BUS_OUT_OF_RESOURCES0xD4DXE_NO_CON_OUT0xD6DXE_NO_CON_IN0xD7DXE_NO_CON_IN0xD7DXE_NO_CON_IN0xD8DXE_BOOT_OPTION_LOAD_ERROR0xD9DXE_BOOT_OPTION_FAILED0xDADXE_FLASH_UPDATE_FAILED0xDADXE_FLASH_UPDATE_FAILED0xDA	PEI_CPU_MICROCODE_UPDATE_FAILED	0x59
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DXE_BOOT_OPTION_FAILED 0xDA DXE_FLASH_UPDATE_FAILED 0xDB	DXE_BOOT_OPTION_LOAD_ERROR	0xD9
DXE_FLASH_UPDATE_FAILED 0xDB	DXE_BOOT_OPTION_FAILED	0xDA
	DXE_FLASH_UPDATE_FAILED	
		0xDC

5-8-4 Intel UPI POST Codes

Initialize KTIRC inuput structure default values	0xA0
Collect info such as SBSP, Boot Mode, Reset type etc	0xA1
Setup IO SADs in SBSP to access the config space	0xA2
Setup up minimum path between SBSP & other sockets	0xA3
Add the node to the tree	
Parse the LEP of the discovered socket	
Check if the system has the supported topology	
Setup the boot path for the parent which is not	
directly connected to Legacy CPU	
Setup path from SBSP to the new found node	
Setup IO SADs in PBSP to access the config space	0xA4
System configurations that require some kind of reset	0xA5
Sync up with PBSPs	0xA6
Topology discovery and route calculation	0xA7
Program final route	0xA8
Program final IO SAD setting	0xA9
Protocol layer and other Uncore settings	0xAA
Transition links to full speed operation	0xAB
Phy layer settings	0xAC
Link layer settings	0xAD
Coherency Settings	0xAE
KTIRC is done	0xAF

5-8-5 Intel UPI Error Codes

When system BSP tries to setup path for remote sockets or sends a Boot_Go command to remote socket in SetupSbspPathToAllSockets() or SyncUpPbspForReset(). If the remote socket(s) hasn't checked-in, assert; it is a fatal condition, this error will be logged. No retry.	0xD8
RC Behavior: System Halt	
When SBSP tries to add this remote socket into system topology tree in SetupSbspPathToAllSockets(), there are some errors occur in the data structure. No retry. <i>RC Behavior: The current Socket is not added to the tree.</i> When SBSP setups the boot path for the parent which is not directly connected to Legacy CPU in SetupSbspPathToAllSockets(). The Child is not an immediate neighbor of Parent. No retry.	0xDA
SAD setup error RC Behavior: System Halt	0xDB
NO Denavior. Oystern nait	

Unsupported topology RC Behavior: System Halt	0xDC
SBSP cannot find KPIRC TXEQ Parameters for this link in GetSocketLinkEparams(). No retry. <i>RC Behavior: System Halt</i>	0xDD

5-8-6 Intel MRC POST Codes

Detect DIMM population	0xB0
Set DDR frequency	0xB1
Gather remaining SPD data	0xB2
Program registers on the memory controller level	0xB3
Evaluate RAS modes and save rank information	0xB4
Program registers on the channel level	0xB5
DDRIO Initialization	0xB6
Train DDR	0xB7
Initialize CLTT/OLTT	0xB8
Hardware memory test and init	0xB9
Execute memory init	0xBA
Program memory map and interleaving	0xBB
Program RAS configuration	0xBC
Rank margin tool	0xBD
MRC is done	0xBF

5-8-7 Intel MRC Error Codes

No memory was detected	0xE8
Memory test failure	0xEB
Different dimm types are detected installed in the system	0xED
Number of HAs found in system greater than	0xEE
MAX_HA defined in MRC build	
Indicates a CLTT table structure error	0xEF
Invalid VR mode, unable to set DRAM VDD	0xF0
Failure occurred reserving memory for IOT	0xF1
Reference code assert	0xF2
Unsupported MC frequency set	0xF3
Unable to get current MC frequency	0xF4

5-8-8 Intel PM POST Codes

Start of PPM structure initialization	0xD0
PPM CSR programming	0xD1
PPM MSR programming	0xD2
Start of PState transition init	0xD3
PPM exit	0xD4
PPM On ready to boot event	0xD5

5-8-9 Intel PM POST Codes

Start of IIO early Initialization	0xE0
Pre Link training	0xE1
Start of Gen3 EQ training	0xE2
Start of PState transition init	0xE3
Gen3 parameters override	0xE4
End of IIO Early Initialization	0xE5
Start of IIO Late initialization	0xE6
PCIE port initialization	0xE7
IOAPIC initialization	0xE8
VTD initialization	0xE9
IOAT initialization	0xEA
DFX initialization	0xEB
NTB initialization	0xEC
Security Initialization	0xED
IIO late initialization	0xEE
IIO On ready to boot event	0xEF

5-9 BIOS POST Beep code (AMI standard)

5-9-1 PEI Beep Codes

# of Beeps	Description
1	Memory not Installed.
1	Memory was installed twice (InstallPeiMemory routine in PEI Core called twice)
2	Recovery started
3	DXEIPL was not found
3	DXE Core Firmware Volume was not found
4	Recovery failed
4	S3 Resume failed
7	Reset PPI is not available

5-9-2 DXE Beep Codes

# of Beeps	Description
1	Invalid password
4	Some of the Architectural Protocols are not available
5	No Console Output Devices are found
5	No Console Input Devices are found
6	Flash update is failed
7	Reset protocol is not available
8	Platform PCI resource requirements cannot be met