

BIOS Release Notes

TITLE

SUBJECT: MZ52-G40 BIOS Release Notes version F00k
System: G482-Z50-00,G482-Z51-00,G482-Z52-00,G482-Z53-00,G492-Z50-00

About This Release

Build Date: 2019/10/15
BIOS Checksum: 0xDC2D4A66 (16M)
Release Owner: Ian.Liu

BIOS Components/Contents

Processor stepping(s) supported: AMD Naples Processors
System hardware configurations supported: MZ52-G40,
Microcode updates versions:

CPUID	Family	Microcode Update ID
Naples	Family 17h Model 00-0Fh	08001230

IPMI support:
AMI Kernel version: 5.013_NaplesCrb_0ACIJ022
AGESA PI version: NaplesPI 1.0.0.B
OPROM version :

OPROM	Version
AST2500	1.09.0

Installation Notes

IMPORTANT NOTES:

1. Please extract the MZ52-G40_F00k.zip to a bootable diskette that use FAT/FAT32 format

BIOS UPDATE INSTRUCTIONS FOR EFI Shell:

1. Insert USB flash drive to system for BIOS upgrade.
2. Power on system and boot to Build-In Shell.
3. Enter your USB filesystem, like "fs0:" or "fsx:", "x" is your USB filesystem number
4. Execute F.nsh for bios update
5. After bios flash finish, please reboot system

BIOS UPDATE INSTRUCTIONS FOR Windows:

1. Insert bios update USB flash drive.
2. Use Command Shell
3. Execute \Tool\WIN\Win32*.bat or \Tool\Win64*.bat for bios update
4. After bios flash finish, please reboot system

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BIOS UPDATE INSTRUCTIONS FOR LINUX:

1. Insert bios update USB flash drive.
2. Use Command Shell
3. Execute \Tool\LNx\afulnx32\f.sh or \Tool\LNx\afulnx64\f.sh for bios update
4. After bios flash finish, please reboot system

BIOS Version CHECK INSTRUCTIONS:

1. Power on system and press during POST
2. The bios version shows on the first main page

Known Issues/Workarounds

Issues fixed in this version

F00k

1. Update redriver.
 2. Fine tune PCIe item.
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F00j

1. Set flash BIOS code.
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F00i

1. Set SMBIOS size.
 2. Change recovery model to keep behavior.
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F00h

1. Set redriver.
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F00g

1. Set SMBIOS size.
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F00f

1. Update Naples kernel.
 2. Fine tune PCIe root port for BIOS recovery mode.
 3. Fine tune NVME hot plug.
 4. Change recovery file name.
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F00e

1. Fine tune SATA port and hot plug.
 2. Fine tune common BIOS code.
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F00d

1. Fine tune common BIOS code.
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F00c

1. Find tune smm code.
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F00b

1. Fix IPv6 IP address cannot show on setup.
 2. Add "Wait BMC Ready" item.
 3. Fine U2_P1_P0_A OP rom item and WebUI information.
 4. Set switch "U2_P0_P3 Lanes" and "U2_P1_P1 Lanes" item auto.
 5. Update TCG2 model.
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F00a

1. Set serial port number.
 2. Set GIGABYTE SVID on VGA and USB device.
 3. Fine tune above 4G behavior.
 4. Set AC power loss default(set power off) for GPU server.
 5. Fine tune USB recovery code.
 6. Enabled SPI TPM function.
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D08

1. Adjusted Redriver for CPBG250_2.
 2. Added Redriver Setup Control Item
 3. Adjusted SMBIOS Space.
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D07

1. Fixed NMI function failed.
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D06

1. Added Riser Porting Function.
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D05

1. Add new setup behavior.
 2. Set item [Terminal Type] default.
 3. Fine tune setup Chipset page.
 4. Support V-Color DIMM.
 5. Update SATA ReDriver.
 6. Update IR rom.
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D04

1. Support AMI BMC.
 2. Fine tune PCIe OP ROM page.
 3. Fine tune onboard LAN item.
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D03

1. Adjust redriver.
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D02

BIOS Release Notes

1. Fixed PCIe can't detect issue.

D01

1. First Release.
 2. Base on MZ91-FS0 F10.
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