

BIOS Release Notes

TITLE

SUBJECT: MX34-BS0 BIOS Release Notes version F06

System: MX34-BS0-000, R113-X30-AA02-000

About This Release

Build Date: 2024/01/09

BIOS Checksum: 0xB6B1B8DF (32M)

Release Owner: Patty.Liao

BIOS Components/Contents

Processor stepping(s) supported: Intel Raptor Lake-E Processors

System hardware configurations supported: MX34-BS0-000

MB Version : 1.X

Microcode updates versions:

CPUID	Family	Microcode Update ID
B0671	Intel(R) Server Raptor Lake-S B-0	0000011F
90672	Intel(R) Server Raptor Lake-S H-0	00000032

IPMI support:

AMI Kernel version: 5.27_1AZIQ_RC0C.00.6B.41_007

Intel CSI/MRC Package version: RC0C.00.6B.41

Intel ME-SPS Firmware version: E3_06.02.04.008

OPROM version:

OPROM	Version
AST2600 VGA	1.10.01

Installation Notes

IMPORTANT NOTES:

1. Please extract the MX34-BS0_F06.zip to a bootable diskette that use FAT/FAT32 format

BIOS UPDATE INSTRUCTIONS FOR EFI Shell:

1. Insert USB flash driveto system for BIOS upgrade.
2. Power on system and boot to Build-In Shell.
3. Enter your USB filesystem, like "fs0:" or "fsX:", "X" is your USB filesystem number
4. Execute F.nsh for bios update
5. After bios flash finish, system must Power-Off to have the changes take effect.

BIOS UPDATE INSTRUCTIONS FOR Windows:

1. Insert bios update USB flash drive.

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2. Use Command Shell.
3. Enter \Tool\Win32 and execute f.bat for Windows 32bit.
Or
Enter \Tool\Win64 and execute f.bat for Windows 64bit.
4. After bios flash finish, system must Power-Off to have the changes take effect.

BIOS UPDATEFOR Easy BIOS Refresh:

1. The system supports remotely update BIOS if BMC existent.
2. Please download the Easy BIOS Refresh User Guide from Gigabyte website, target system support page.

BIOS Version CHECK INSTRUCTIONS:

1. Power on system and press during POST
2. The bios version shows on the first main page

Known Issues/Workarounds

Issues fixed in this version

F06

1. Support "DMA Control Guarantee" item.
2. Modify all post error message to not stop.
3. Modify default year value.
4. Support CPU PCIE Slot to Gen5.

F05

1. Modify ECC Function.

F04

- 1.Update Source code (1AZIQ_RC0C.00.6B.41_007).

F03

1. Adjust Clear CMOS process.

F02

- 1.Update ME (SPS_E3_06.02.04.008) and Micocode (Microcode_400) for PVT version.

F01

- 1.First release for PVT version.