

BIOS Release Notes

TITLE

SUBJECT: MX33-BS0 BIOS Release Notes version **F11**
System: MX33-BS0-00, R112-X30-0E, W331-X30-AA01-ML0, R112-X30-00

About This Release

Build Date: **2024/02/20**
BIOS Checksum: **0xABFB1119** (16M), **0xB4C6DB5E** (32M)
Release Owner: Patty.Liao

BIOS Components/Contents

Processor stepping(s) supported: Intel Rocket Lake-S processors
System hardware configurations supported: MX33-BS0
MB Version: 1.0
Microcode updates versions:

CPUID	Family	Microcode Update ID
A0670	Intel Rocket Lake-S A-0 Stepping	0000002C
A0671	Intel Rocket Lake-S B-0 Stepping	00000058

IPMI support:
AMI Kernel version: **5.22_1AXCT_RC0B.01.34.60_011**
Intel CSI/MRC Package version: **0B.01.34.60_20**
SPI Descriptor version: **06.00.03.309**
OPROM version :

OPROM	Version
AST2500 VGA	1.09
I210 LAN PXE Rom	1.5.56
I210 LAN EFI Driver	6.5.01

Installation Notes

IMPORTANT NOTES:

1. Please extract the MX33-BS0_F11.zip to a bootable diskette that use FAT/FAT32 format

BIOS UPDATE INSTRUCTIONS FOR EFI Shell:

1. Insert USB flash drive to system for BIOS upgrade.
2. Power on system and boot to Build-In Shell.
3. Enter your USB filesystem, like "fs0:" or "fsX:", "X" is your USB filesystem number
4. Execute F.nsh for bios update
5. After bios flash finish, please reboot system

BIOS Release Notes

BIOS UPDATE INSTRUCTIONS FOR Windows:

1. Insert bios update USB flash drive.
2. Use Command Shell
3. Execute \Tool\Win32*.bat or \Tool\Win64*.bat for bios update
4. After bios flash finish, please reboot system

BIOS Version CHECK INSTRUCTIONS:

1. Power on system and press during POST
2. The bios version shows on the first main page

Known Issues/Workarounds

Issues fixed in this version

F11

1. Combine R112-X30-00 related function.

F10

1. Fine tune Setup item.

F09

1. Modify "Configuration Address source" item cannot be saved.

F08

1. Update RC code (0B.01.34.60_20), Microcode (379) for SmmEntryPoint Underflow Vulnerability security update.
(CVE-2021-38578)
2. Combine MX33-BS0/R112-X30-0E/W331-X30-AA01-ML0/R112-X30-00 related function.

F07

1. Update RC code (0B.01.34.60_19), Microcode (369) and SPS (6.0.3.309) for 2023.1 security update.
(CVE-2022-26837/ CVE-2022-30704/ CVE-2022-36794/ CVE-2022-38090)
2. Combine MX33-BS0/R112-X30-0E/W331-X30-AA01-ML0 related function.

F06

1. Modify Http ip4/ip6 Boot function.
2. Modify Platform Power Limit 1 and 2 to 95W
3. Set the front USB port as Gen1.
4. Modify the NIC Inventory show incorrect slot number.
5. Modify SERR Function.
6. Update RC code (0B.01.34.60_18), Microcode (361) and SPS (6.0.3.204) for 2022.3 security update.
(CVE-2022-21198/CVE-2022-26845/CVE-2022-29893/CVE-2022-27497/
CVE-2022-29515)

BIOS Release Notes

F05

1. Modify HLK function.
2. Modify NMI function.
3. Add Real-time synchronize BMC network parameter values item.

F04

1. Modify the system cannot be shut down with PCIe card.

F03

1. Updates RC code for 2022.1 IPU-PV.
2. Updates Microcode for security update.
3. Updates SPS to 06.00.03.039 for security update.

F02

1. Update Code 1AXCT_08 to 1AXCT_011
2. Modify Post Screen information not meet SPEC.
3. Modify WebUI Information.
4. Support CPU G6405 (Comet Lake)
5. Modify WebUI CPU Inventory information and Post screen display information.
6. Generate the error log on the SEL and the post screen after triggering the single/multi bit error.

F01

1. Base on BIOS D13 to rename F01.
 2. Modify SMBIOS 3.0 for WebUI.
-