

BIOS Release Notes

TITLE

SUBJECT: MB12-CE0 BIOS Release Notes version F03
System: MB12-CE0-000

About This Release

Build Date: 2022/08/12
BIOS Checksum: 0xB188A5BF(64M)
Release Owner: Ian.Liu

BIOS Components/Contents

Processor stepping(s) supported: Intel IceLake-D LCC Processors
System hardware configurations supported: MB12-CE0
Microcode updates versions:

CPUID	Family	Microcode Update ID
606C1	Intel IceLake-D LCC / B0	010001F0

IPMI support:
AMI Kernel version: 5.28_Idaville_0ACOY_LCC_026
Intel CSI/MRC Package version: 23.P19
Intel ME-SPS Firmware version: 05.00.04.032.0
Platform version: 1.X
OPROM version:

OPROM	Version
AST2600 VGA	1.10.01

Installation Notes

IMPORTANT NOTES:

1. Please extract the MB12-CE0_F03.zip to a bootable diskette that use FAT/FAT32 format

BIOS UPDATE INSTRUCTIONS FOR EFI Shell:

1. Insert USB flash drive to system for BIOS upgrade.
2. Power on system and boot to Build-In Shell.
3. Enter your USB filesystem, like "fs0:" or "fsx:", "x" is your USB filesystem number
4. Execute F.nsh for bios update
5. After bios flash finish, system must Power-Off to have the changes take effect.

BIOS UPDATE INSTRUCTIONS FOR Windows:

1. Insert bios update USB flash drive.
2. Use Command Shell.
3. Enter \Tool\Win32 and execute f.bat for Windows 32bit.

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Or

Enter \Tool\Win64 and execute f.bat for Windows 64bit.

4. After bios flash finish, system must Power-Off to have the changes take effect.

BIOS UPDATE FOR Easy BIOS Refresh:

1. The system supports remotely update BIOS if BMC existent.
2. Please download the Easy BIOS Refresh User Guide from Gigabyte website, target system support page.

BIOS Version CHECK INSTRUCTIONS:

1. Power on system and press during POST
2. The bios version shows on the first main page

Known Issues/Workarounds

Issues fixed in this version

F03

- 1.Update PV BIOS and SPS version.
 - 2.Set NVRAM behavior.
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F02

- 1.Fine tune PVT setup item.
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F01

1. Release PVT BIOS.
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D09

1. Set data default.
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D08

1. Update kernel and Intel SPS.
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D07

1. Fix some Linux OS cannot auto boot.
 2. Fine tune and set "Power Policy" item.
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D06

1. Update a patch for flash iRC hang code issue.
2. Set SATA LED function.
3. Fine tune Setup page and item default.

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D05

1. Set date default.
 2. Set BIOS recovery pin.
 3. Set BIOS read MB version timer and GPIO pin.
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D04

1. Set SVID.
 2. Fine tune setup menu.
 3. Fine tune memory ECC error behavior.
 4. Set SPS and setup default for BMC read CPU and DIMM temperature from SPS.
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D03

1. Add GBT BIOS Ready function and fine tune switch SMBus behavior.
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D02

1. Update kernel and Intel SPS
 2. Enable early display function.
 3. Fine tune BMC ready pin.
 4. Set PXE IPv6 default.
 5. Set CPU read/write access for flash BIOS ME.
 6. Add GBT memory ECC error function and fine tune setup.
 7. Add GBT PCIe error function.
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D01

1. Release DVT version.
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