

BIOS Release Notes

TITLE

SUBJECT: MZ71-CE0 BIOS Release Notes version [M12_R06](#)
System: [MZ71-CE0-00](#), [MZ71-CE1-00](#)

About This Release

Build Date: [2021/11/26](#)
BIOS Checksum: [0x955334ED](#) (32M)
Release Owner: Kyle.Huang

BIOS Components/Contents

Processor stepping(s) supported: AMD Milan processors, AMD Rome processors
System hardware configurations supported: MZ71-CE0, MZ71-CE1

Installation Notes

IMPORTANT NOTES:

1. Please extract the zip file to a bootable diskette that use FAT/FAT32 format

BIOS UPDATE INSTRUCTIONS FOR EFI Shell:

1. Insert USB flash drive to system for BIOS upgrade.
2. Power on system and boot to Build-In Shell.
3. Enter your USB filesystem, like "fs0:" or "fsx:", "x" is your USB filesystem number
4. Execute F.nsh for bios update
5. After bios flash finish, system must Power-Off to have the changes take effect.

BIOS UPDATE INSTRUCTIONS FOR Windows:

1. Insert bios update USB flash drive.
2. Use Command Shell.
3. Enter \Tool\Win32 and execute f.bat for Windows 32bit.
Or
Enter \Tool\Win64 and execute f.bat for Windows 64bit.
4. After bios flash finish, system must Power-Off to have the changes take effect.

BIOS UPDATE FOR Easy BIOS Refresh:

1. The system supports remotely update BIOS if BMC existent.
2. Please download the Easy BIOS Refresh User Guide from Gigabyte website, target system support page.

BIOS Version CHECK INSTRUCTIONS:

1. Power on system and press during POST
2. The bios version shows on the first main page

BIOS UPDATE INSTRUCTIONS FOR Web UI:

1. Connect BMC_IP by Internet browser.
2. Click Web-UI sub-page button "Update".

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3. Select Firmware Type to ROM.
4. Use Command Shell.
5. cd to /RBU.
6. Select File Path to image.RBU.
7. Click Upload.

BIOS UPDATE INSTRUCTIONS FOR GbtUtility:

1. Please copy the BIOS_ZIP file to GbtUtility folder.
2. Use Command Shell.
3. cd to GbtUtility folder.
4. Execute "java -jar GbtUtility.jar -H BMC_IP update bios BIOS_ZIP 1".

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Known Issues/Workarounds

Issues fixed in this version

- 1 . Milan CPU please reference Relnotes_MZ71-CE0_Milan.pdf
- 2 . Rome CPU please reference Relnotes_MZ71-CE0_Rome.pdf