

BIOS Release Notes

TITLE

SUBJECT: 6LASH BIOS Release Notes version [F02](#)

About This Release

Build Date: [2013/9/13](#)

BIOS Checksum: [0x4898FEA9](#) (8M), [0xB13BBB6B](#) (16M)

Release Owner: gary.wu

BIOS Components/Contents

Processor stepping(s) supported: supports Intel® Haswell processors

System hardware configurations supported: GA-6LASH Rev: [0.1/0.2/1.0](#)

Microcode updates versions:

CPUID	Family	Microcode Update ID
306C1	Haswell Processor A-0	FFFF0013
306C2	Haswell Processor B-0	FFFF0006
306C3	Haswell C-0	00000016

IPMI support:

AMI Kernel version: [Gbt_Kernel_E12u](#)

Intel CSI/MRC Package version: [1.6.0.0](#)

SPI Descriptor version: [SPS_EN_03.00.04.164.0_PV-D_LPT_REL](#)

OPROM version:

OPROM	Version
INTEL RAID	3.8.0.1009
INTEL PXE	1.5.01

Installation Notes

IMPORTANT NOTES:

1. Please extract the 6LASH_[F02](#).zip to a bootable diskette that can boot to real DOS
2. Please don't load HIMEM.SYS or any other memory management SW in the diskette

BIOS UPDATE INSTRUCTIONS FOR DOS:

1. Insert bios update USB flash drive.
2. Power on system and wait for boot into DOS
3. Execute F.BAT for bios update
4. After bios flash finish, system will reboot automatically

BIOS UPDATE INSTRUCTIONS FOR LINUX:

1. Insert bios update USB flash drive.

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2. Use Command Shell
3. Execute \Tool\LNx\afulnx32\f.sh or \Tool\LNx\afulnx64\f.sh for bios update
4. After bios flash finish, Please reboot windows system

BIOS Version CHECK INSTRUCTIONS:

1. Power on system and press F2 during POST
2. The bios version shows on the first main page

Known Issues/Workarounds

Issues fixed in this version

F02

1. Update verb table.
2. Correct the subsystem id for Realtek audio device.

F01

1. Add an item "Onboard Lan1 I/O ROM Option" in the setup menu to select "iSCSI" or "PXE" ROM.
2. Correct information of PCIe slots in SMBIOS.
3. Correct the setup item "Primary Display".

D04

1. [0015758], Fixed the issue "The user cannot press power button to trigger OS sleep or shutdown".
2. [0015757], Fixed the issue "PCI-E Option ROM function test fail , BIOS PCI-E Option ROM not match M/B print label".
3. Correct SMBIOS information.
4. Update CPU microcode.
5. Fine-tune OpROM shadow behavior.
6. Enable CLTM function.

D03

1. Fixed the issue "System hangs up on AC Loss for IPMI module when set nvram was fail".
2. Modify ITE8732 Pin 32 from PWM to GP31.
3. Fixed the issue "TPM warning message cannot be shown on the screen after PXE shadow".

D02

1. Update setup menu module for E11b kernel code

D01

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1. Add CLTM support to RC.
 2. Change the behavior of sending command to BMC to log NMI event.
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